TECHNICAL MANUAL

ORGANIZATIONAL MAINTENANCE MANUAL
RADAR INTERFACE EQUIPMENT MAINTENANCE
VIDEO PROCESSOR UNIT 1 AND 2

EXPANDED TROUBLESHOOTING
(LOGIC DIAGRAMS)

GUIDED MISSILE
AIR DEFENSE SYSTEM
AN/TSQ-73

HEADQUARTERS, DEPARTMENT OF THE ARMY
21 JANUARY 1985

Change
No. 1

HEADQUARTERS
DEPARTMENT OF THE ARMY
Washington, D.C., 28 December 1990

ORGANIZATIONAL MAINTENANCE MANUAL

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TM 9-1430-655-20-3-4, 21 January 1985, is changed as follows:

1. Remove old pages and insert new pages as indicated below. New or changed material is indicated by the applicable change number, i.e., Change 1, at the bottom of the page adjacent to the page number. Revised text will have a vertical bar in the margin next to the changed area. Revised illustrations will have suffix change letter added to the identification number.

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v/(vi blank)	vi(vi blank)
5-351 thru 5-369/(5-370 blank)	5-755 thru 5-773/(5-774 blank)
FO-3	FO-3
FO-38	FO-38
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2. File this change sheet in front of the publication for reference.

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Distribution:

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WARNING

DANGEROUS VOLTAGE

is used in the operation of this equipment

DEATH ON CONTACT

may result if personnel fail to observe safety precautions

Never work on electronic equipment unless there is another person nearby who is familiar with the operation and hazards of the equipment and who is competent in administering first aid. When the technician is aided by operators, he must warn them about dangerous areas.

Whenever possible, the power supply to the equipment must be shut off before beginning work on the equipment. Take particular care to ground every capacitor likely to hold a dangerous potential. When working inside the equipment, after the power has been turned off, always ground every part before touching it.

Be careful not to contact high-voltage connections when installing or operating this equipment.

Whenever the nature of the operation permits, keep one hand away from the equipment to reduce the hazard of current flowing through vital organs of the body.

WARNING

Do not be misled by the term "low voltage." Potentials as low as 50 volts may cause death under adverse conditions.

For Artificial Respiration, refer to FM 21-11,

EXTREMETLY DANGEROUS POTENTIALS
greater than 500 volts exist in the following units:
Display console high voltage power supply
Display console CRT

WARNING

For emergencies requiring immediate shutdown of system power, press SYSTEM POWER OFF switch located on power cabinet power transfer unit. Observe that SYSTEM POWER ON indicator light goes off.

LIST OF EFFECTIVE PAGES

Insert latest change pages. dispose of superseded pages in accordance with applicable regulations.

NOTE: On a changed page, the portion of the text affected by the changes is indicated by a vertical line in the outer margins of the page.

Dates of issue for original and change pages are:

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TOTAL NUMBER OF PAGES IN THIS PUBLICATION IS 178 CONSISTING OF THE FOLLOWING:

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v	1	FO-42	0	FO-87	0
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FO-13	0	FO-58	0	FO-103	0
FO-14	0	FO-59	0	FO-104	0
FO-15 (2 sheets)	0	FO-60 (2 sheets)	0	FO-105	0
FO-16	0	FO-61 (5 sheets)	0	FO-106	0
FO-17	0	FO-62	0	FO-107	0
FO- 18	0	FO-63	0	FO- 108	0
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FO-20	0	FO-65	0	FO- 110	0
FO-21	0	FO-66	1	FO-111	0
FO-22	0	FO-67	0	FO-112	0
FO-23	0	FO-68	0	FO-113 (9 sheets)	0
FO-24	0	FO-69	0	FO- 114	0
FO-25	0	FO-70	0	FO- 1 15	0
FO-26	0	FO-71	0	FO- 1 16	0
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DEPARTMENT OF THE ARMY
Washington, D.C., 21 January 1985

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VIDEO PROCESSOR UNIT 1 AND 2

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AN/TSQ-73

REPORTING OF ERRORS

You can help improve this publication. If you find any mistakes, or if you know of a way to improve the procedures, please let us know. Mail your letter, DA Form 2028 (Recommended Changes to Publications and Blank Forms), or DA Form 2028-2 located in back of this manual direct to: Commander, U.S. Army Missile Command, ATTN: AMSMI-LCME-P, Redstone Arsenal, AL 35898-5238. A reply will be furnished to you.

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Section XIV. VIDEO PROCESSOR UNITS

5-45. General. This manual is Volume 4 of TM 9-1430-655-20-3, Radar Interface Equipment Maintenance for Guided Missile Air Defense System AN/TSQ-73. It contains the logic diagrams covering video processor units (VPU) 1 and 2 for use and guidance of advanced personnel responsible for repair of the RIE. Foldouts 1 through 59 cover VPU 1 and foldouts 60 through 125 cover VPU 2. VPU I and 2 are located in equipment rack 1, 1A1A1, door A, in two card cage bays. Figure 5-2 illustrates bay I and figure 5-3 illustrates bay 2.

5-46. Logic Diagram. The logic diagrams in this manual provide the maintenance technician pin to pin signal flow, traceable by signal mnemonics and 1/0 tables, to help identify faulty cards and to troubleshoot faults in the backplane wiring and other areas that are beyond fault isolation capabilities of the MTS.

5-47. Using Logic Diagrams. Logic diagrams in this manual show signal flow in functional subsystems of VPU I and 2. Signal flow is traceable between circuit card pin numbers and is shown as inputs and outputs of integrated circuit logic devices on the circuit card. A specific signal can be followed between foldouts by using the signal mnemonic and the logic diagram input/output table. The circuit card slot is shown within the integrated circuit card device symbol. Table 5-37 contains the circuit card slot and the part number of the card. Table 5-38 contains, by card part number, the test point for each of the 80 pins of MTS testable cards.

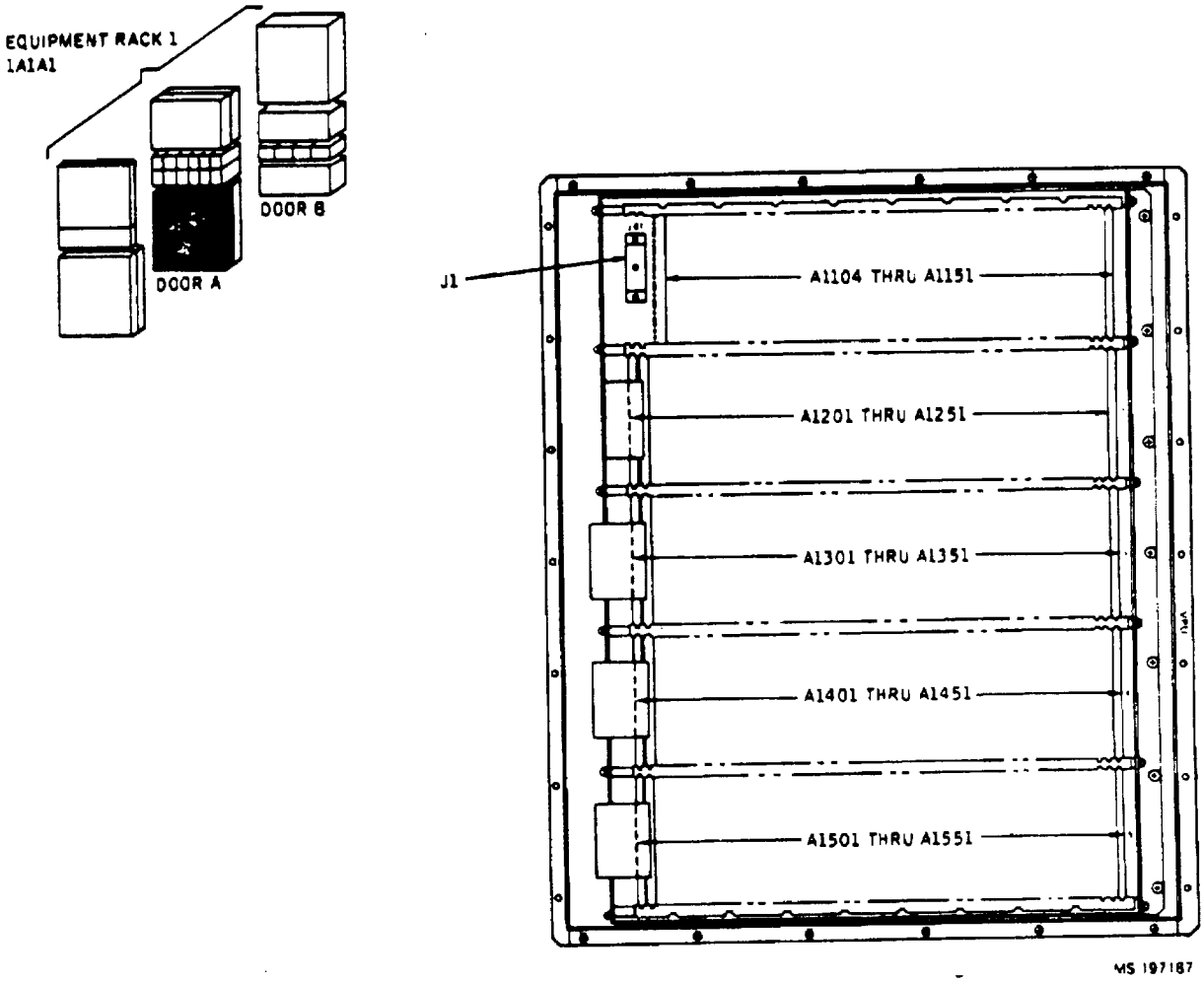


Figure 5-2. Video Processor Unit IAIAIAS Bay 1, Component Location

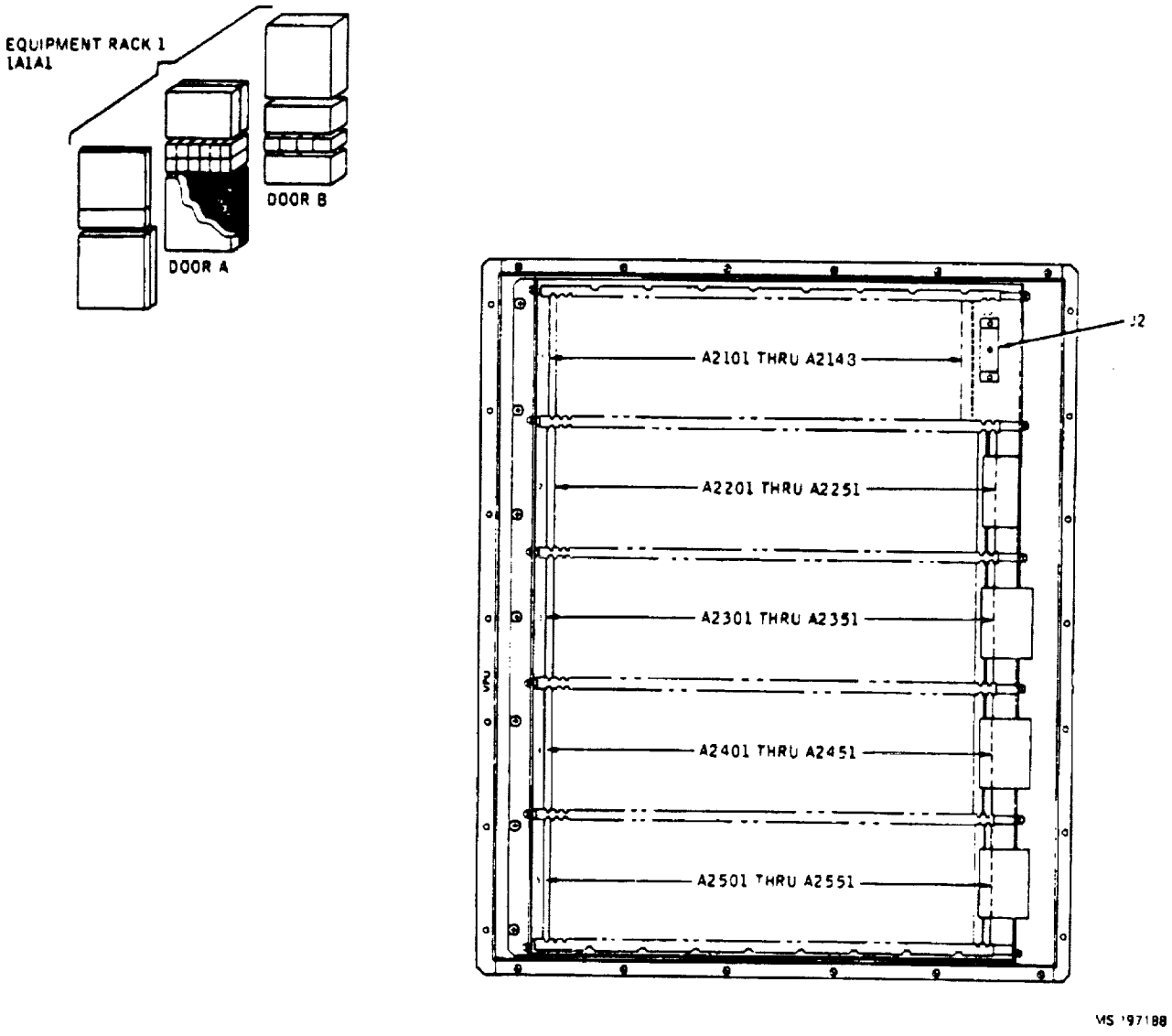


Figure 5-3. Video Processor Unit 1A1A1A5 Bay 2, Component Location

Table 5-37. Video Processor Unit 1A1A1A5, Circuit Card Location

CARD SLOT	PART NUMBER	CARD TYPE	COLOR CODE 1	2	3	4
		BAY-SHELF 1				
A1101	-	-	-	-	-	-
A1102	-	-	-	-	-	-
A1103	-	-	-	-	-	-
A1104	W390	Connector	-	-	-	-
A1105	W391	Connector	-	-	-	-
A1106	W532	Connector	-	-	-	-
A1107	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1108	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1109	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1110	587102-102	Quad 2-input NAND gate	-	-	Red	-
A111	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1112	-	-	-	-	-	-
A1113	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1114	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1115	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1116	587117-102	Hex inverter	Brown	-	Violet	-
A1117	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1118	587108-102	Single 8-input NAND gate	-	-	Gray	-
A1119	-	-	-	-	-	-
A1120	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1121	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1122	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1123	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A1124	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A1125	10281602	Counter/decoder	Brown	Blue	Black	Red
Al 126	10281602	Counter/decoder	Brown	Blue	Black	Red
A1127	10281602	Counter/decoder	Brown	Blue	Black	Red
A1128	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1129	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1130	-	-	-	-	-	-
A1131	-	-	-	-	-	-
A1132	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1133	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1134	587104-102	Dual 4-input NAND gate	-	-	Yellow	-

Table 5-37. Video Processor Unit1A1A1A5, Circuit Card Location -Continued-

CARD SLOT	PART NUMBER	CARD TYPE	COLOR CODE 1	2	3	4
A1135	-	-	-	-	-	-
A1136	-	-	-	-	-	-
A1137	-	-	-	-	-	-
A1138	-	-	-	-	-	-
A1139	-	-	-	-	-	-
A1140	-	-	-	-	-	-
A1141	-	-	-	-	-	-
A1142	-	-	-	-	-	-
A1143	-	-	-	-	-	-
A1144	-	-	-	-	-	-
A1145	-	-	-	-	-	-
A1146	-	-	-	-	-	-
A1147	-	-	-	-	-	-
A1148	-	-	-	-	-	-
A1149	-	-	-	-	-	-
A1150	-	-	-	-	-	-
A1150	-	-	-	-	-	-
		BAY 1-SHELF 2				
A1201	W533	Connector				
A120 2	W411	Connector				
A1203	W412	Connector	-	-	-	-
A1204	-					
A1205	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1206	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1207	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1208	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A1209	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A1210	587108-102	Single 8-input NAND gate	-	-	Gray	-
A1211	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1212	587117-102	Hex inverter	Brown	-	Violet	-
A1213	587102-102	Quad 2-input NAND gate	-	-	Red	-
A12141	0281603	4-bit multiplexer	Brown	Blue	Black	Orange
A1215	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A1216	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A1217	10281603	4-bit multiplexer	Brown	Blue	Black	Orange

Table 5-37. Video Processor Unit 1A1A1A5, Circuit Card Location -Continued-

CARD SLOT	PART NUMBER	CARD TYPE	COLOR CODE 1	2	3	4
A1218	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1219	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1220	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1221	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1222	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1223	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1224	587118-100	IK-ohm resistor	-	-	-	-
A1225	587117-102	Hex inverter	Brown	-	Violet	-
A1226	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1227	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1228	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1229	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1230	587119-100	240-ohm resistor	-	-	-	-
A1231	587108-102	Single 8-input NAND gate	-	-	Gray	-
A1232	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1233	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1234	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1235	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1236	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1237	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1238	-	-	-	-	-	-
A1239	-	-	-	-	-	-
A1240	-	-	-	-	-	-
A1241	-	-	-	-	-	-
A1242	-	-	-	-	-	-
A1243	-	-	-	-	-	-
A1244	-	-	-	-	-	-
A1245	-	-	-	-	-	-
A1246	-	-	-	-	-	-
A1147	-	-	-	-	-	-
A1248	-	-	-	-	-	-
A1249	-	-	-	-	-	-

Table 5-37. Video Processor Unit 1A1A1A5, Circuit Card Location -Continued-

CARD SLOT	PART NUMBER	CARD TYPE	COLOR CODE 1	2	3	4
A1250	-	-	-	-	-	-
A1251	-	-	-	-	-	-
		BAY 1-SHELF 3				
A 1301	W422	Connector	-	-	-	-
A1302	W417	Connector	-	-	-	-
A1303	W418	Connector	-	-	-	-
A1304	W534	Connector	-	-	-	-
A1305	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1306	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1307	10281606	Hex 4-bit shift register '	Brown	Blue	Black	Blue
A1308	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1309	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A1310	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A1311	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A1312	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A1313	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A1314	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A1315	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A1316	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A1317	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1318	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1319	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1320	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1321	10281602	Counter/decoder	Brown	Blue	Black	Red
A1322	10281602	Counter/decoder	Brown	Blue	Black	Red
A1323	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1324	587117-102	Hex inverter	Brown	-	Violet	-
A1325	10281610	Hex 4-bit comparator	Brown	Blue	Brown	Black
A1326	10281602	Counter/decoder	Brown	Blue	Black	Red
A1327	-	-	-	-	-	-
A1328-	-	-	-	-	-	-
A1329	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1330	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1331	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1332	587104-102	Dual 4-input NAND gate	-	-	Yellow	-

See footnote at end of table.

Table 5-37. Video Processor Unit 1A1A1A5, Circuit Card Location -Continued-

CARD SLOT	PART NUMBER	CARD TYPE	COLOR CODE 1	2	3	4
A1333	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1334	587108-102	Single 8-input NAND gate	--	Gray	-	
A1335	587117-102	Hex inverter	Brown	-	Violet	-
A1336	587117-102	Hex inverter	Brown	-	Violet	-
A1337	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A1338	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A1339	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1340	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1341	587102-102	Quad 2-input NAND gate	-	-	Red	
A1342	587102-102	Quad 2-input NAND gate	-	-	Red	
A1343	10281610	Hex 4-bit comparator	Brown	Blue	Brown	Black
A1344	10281610	Hex 4-bit comparator	Brown	Blue	Brown	Black
A1345	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1346	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1347	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1348'	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1349'	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1350'	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1351	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
		BAY 1-SHELF 4				
A1401	W450	Connector	-	-	-	-
A1402	W436	Connector	-	-	-	-
A1403	W443	Connector	-	-	-	-
A1404	-	-	-	-	-	-
A1405-	-	-	-	-	-	-
A1406-	-	-	-	-	-	-
A1407	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1408	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1409	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1410	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1411	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1412	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1413	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1414	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1415	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue

See footnote at end of table.

Table 5-37. Video Processor Unit 1A1A1A5, Circuit Card Location -Continued-

CARD SLOT	PART NUMBER	CARD TYPE	COLOR CODE 1	2	3	4
A1416	587108-102	Single 8-input NAND gate	-	-	Gray	-
A1417	587108-102	Single 8-input NAND gate	-	-	Gray	-
A1418	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1419	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1420	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1421	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1422	587117-102	Hex inverter	Brown	-	Violet	-
A1423	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1424	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1425	587108-102	Single 8-input NAND gate	-	-	Gray	-
A1426	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1427	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1428	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1429	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1430	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1431	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1432	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1433	587117-102	Hex inverter	Brown	-	Violet	-
A1434	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1435	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1436	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1437	-	-	-	-	-	-
A1438	587119-100	240-ohm resistor				
A1439	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A1440	10281780	Quad exclusive OR gate	Brown	Violet	Gray	Black
A1441	587108-102	Single 8-input NAND gate	-	-	Gray	-
A1442	.587108-102	Single 8-input NAND gate	-	-	Gray	-
A1443	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1444	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1445	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1446	587117-102	Hex inverter	Brown	-	Violet	-
A1447	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1448	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1449	-	-	-	-	-	-
A1450	-	-	-	-	-	-

Table 5-37. Video Processor Unit1A1A1A5, Circuit Card Location -Continued-

CARD SLOT	PART NUMBER	CARD TYPE	COLOR CODE 1	2	3	4
A1451-	-	-	-	-	-	-
		BAY 1-SHELF 5				
A1501	W451	Connector	-	-	-	-
A1502	W437	Connector	-	-	-	-
A1503	W449	Connector	-	-	-	-
A1504	-	-	-	-	-	-
A1505	-	-	-	-	-	-
A1506	-	-	-	-	-	-
A1507	587108-102	Single 8-input NAND gate	-	-	Gray	-
A1508	587108-102	Single 8-input NAND gate	-	-	Gray	-
A1509	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1510	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1511	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1512	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1513	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1514	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1515	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A1516	587117-102	Hex inverter	Brown	-	Violet	-
A1517	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1518	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1519	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A1520	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A1521	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A1522	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A1523	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A1524	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1525	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1526	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1527	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1528	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1529	587117-102	Hex inverter	Brown	-	Violet	-
A1530	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1531	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1532	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A1533	587108-102	Single 8-input NAND gate	-	-	Gray	-

Table 5-37. Video Processor Unit1A1A1A5, Circuit Card Location -Continued-

CARD SLOT	PART NUMBER	CARD TYPE	COLOR CODE 1	2	3	4
A1534	587104-102	Dual 4-input NAND gate	-	-	Yellow	
A1535'	10281610	Hex 4-bit comparator	Brown	Blue	Brown	Black
A1536	10281610	Hex 4-bit comparator	Brown	Blue	Brown	Black
A1537	10281610	Hex 4-bit comparator	Brown	Blue	Brown	Black
A1538	10281610	Hex 4-bit comparator	Brown	Blue	Brown	Black
A1539	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1540	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1541	-		-	-	-	-
A1542	-	-	-	-	-	-
A1543	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1544	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1545	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1546	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1547	587117-102	Hex inverter	Brown	-	Violet	-
A1548	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1549	587102-102	Quad 2-input NAND gate	-	-	Red	-
A1550	-	-	-	-	-	-
A1551	-	-	-	-	-	-
		BAY 2-SHELF 2				
A2101	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A2102	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A2103	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2104	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2105	-	-		-	-	-
A2106	-	-	-	-	-	-
A2107	-	-	-	-	-	-
A2108	-	-	-	-	-	-
A2109	-	-	-	-	-	-
A2110	-	-	-	-	-	-
A2111	-	-	-	-	-	-
A2112	-	-	-	-	-	-
A2113	-	-	-	-	-	-
A2114	-	-	-	-	-	-
A2115	-	-	-	-	-	-
A2116-	Test connector	-	-	-	-	-

See footnote at end of table.

Table 5-37. Video Processor Unit1A1A1A5, Circuit Card Location
-Continued-

CARD SLOT	PART NUMBER	CARD TYPE	COLOR CODE 1	2	3	4
A2117	10281610	Hex 4-bit comparator	Brown	Blue	Brown	Black
A2118	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2119	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2120	10281610	Hex 4-bit comparator	Brown	Blue	Brown	Black
A2121	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A2122	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2123	-	-	-	-	-	-
A2124	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2125	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2126	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2127-	_	_				
A2128	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A2129	587108-102	Single 8-input NAND gate	-	-	Gray	-
A2130	587119-100	240-ohm resistor				
A2131	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A2132	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2133	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2134	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2135-	Test connector					
A2136	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A2137	10-281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A2138	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A2139	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2140	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2141	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2142	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2143	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2144	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2145-	-	-	-	-	-	-
A2146	W441	Connector	-	-	-	-
A2147	W388	Connector	-	-	-	-
A2148	W389	Connector	-	--	-	-
		BAY 2-SHELF 2				
A2201	587102-102	Quad 2-input NAND gate	-	-	Red	-

Table 5-37. Video Processor Unit1A1A1A5, Circuit Card Location -Continued-

CARD SLOT	PART NUMBER	CARD TYPE	COLOR CODE 1	2	3	4
A2202	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A2203	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2204	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A2205	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A2206	587117-102	Hex inverter	Brown	-	Violet	-
A2207	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2208	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2209	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A2210	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A2211	10281610	Hex 4-bit comparator	Brown	Blue	Brown	Black
A2212	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2213	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A2214	10281610	Hex 4-bit comparator	Brown	Blue	Brown	Black
A2215	10281606	Hex 4-bit comparator	Brown	Blue	Black	Blue
A2216	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2217	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2218	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A2219	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A2220	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A2221	587104-102	Dual 4-input NAND gate	-	-	Yellow	-:
A2222	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2223	587117-102	Hex inverter	Brown	-	Violet	-
A2224	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A2225	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A2226	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2227	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2228	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A2229	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2230	587118-100	1K-ohm resistor	-	-		
A2231	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2232	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A2233	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2234	10281610	Hex 4-bit comparator	Brown	Blue	Brown	Black
A2235	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A2236	587117-102	Hex inverter	Brown	-	Violet	-

Table 5-37. Video Processor Unit1A1A1A5, Circuit Card Location -Continued-

CARD SLOT	PART NUMBER	CARD TYPE	COLOR CODE 1	2	3	4
A2237	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2238	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2239	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A2240	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A2241	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2242	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2243	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2244	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2245	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2246	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2247	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2248	W442	Connector	-	-	-	-
A2249	W409	Connector	-			
A2250	W410	Connector	-			
A2251	W535	Connector	-	-	-	-
		BAY 2-SHELF 3				
A2301	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2302	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2303	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2304	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A2305	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2306	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2307	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2308	587117-102	Hex inverter	Brown	-	Violet	-
A2309	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2310	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2311	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2312	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2313	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2314	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2315	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2316	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2317	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2318	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2319	10281603	4-bit multiplexer	Brown	Blue	Black	Orange

Table 5-37. Video Processor Unit1A1A1A5, Circuit Card Location -Continued-

CARD SLOT	PART NUMBER	CARD TYPE	COLOR CODE 1	2	3	4
A2320	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2321	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2322	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A2323	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2324	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2325	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2326	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2327	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2328	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2329	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2330	587119-100	240-ohm resistor				
A2331	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2332	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A2333	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2334	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2335	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2336	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2337	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2338	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2339	10281610	Hex 4-bit comparator	Brown	Blue	Brown	Black
A2340	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A2341	587104-102	Dual 4-input NAND gate	-	-	Yellow	
A2342	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2343	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2344	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A2345	-	Test connector	-	-	-	-
A2346	-	Test connector				
A2347	-	Test connector				
A2348	W408	Connector				
A2349	W415	Connector	-	-	-	-
A2350	W416	Connector				
A2351	W422	Connector				
		BAY 2-SHELF 4				
A2401	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A2402	587102-102	Quad 2-input NAND gate	-	-	Red	-

Table 5-37. Video Processor Unit1A1A1A5, Circuit Card Location
-Continued-

CARD SLOT	PART NUMBER	CARD TYPE	COLOR CODE 1	2	3	4
A2403	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A2404	10281610	Hex 4-bit comparator	Brown	Blue	Brown	Black
A2405	10281610	Hex 4-bit comparator	Brown	Blue	Brown	Black
A2406	10281610	Hex 4-bit comparator	Brown	Blue	Brown	Black
A2407	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A2408	587102-102	Quad 2-input NAND gate	-	Red		
A2409	587102-102	Quad 2-input NAND gate	-	-	Red	
A2410	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A2411	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A2412	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A2413	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2414	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A2415	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A2416	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A2417	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A2418	587117-102	Hex inverter	Brown	-	Violet	-
A2419	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2420	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A2421	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2422	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A1423	10281652	3-input J-K flip-flop	Brown	Blue	Green	Red
A2424	587102-102	Quad 2-input NAND gate	-	-	Red	
A2425	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2426	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2427	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2428	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2429	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2430	587119-100	240-ohm resistor	-	-	-	-
A2431	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2432	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2433	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2434	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2435	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2436	-	-	-	-	-	-
A2437	587102-102	Quad 2-input NAND gate	-	-	Red	-

Table 5-37. Video Processor Unit1A1A1A5, Circuit Card Location
-Continued-

CARD SLOT	PART NUMBER	CARD TYPE	COLOR CODE 1	2	3	4
A2438	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2439	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2440	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2441	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A2442	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A2443	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A2444	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A2445	587117-102	Hex inverter	Brown	-	Violet	-
A2446	587117-102	Hex inverter	Brown	-	Violet	-
A2447	W526	Connector	-	-	-	-
A2448	W440	Connector	-	-	-	-
A2450	W436	Connector	-	-	-	-
A2451	W450	Connector	-	-	-	-
		BAY 2-SHELF 5				
A2501	-	-	-	-	-	-
A2502	10281780	Quad exclusive OR gate	Brown	Violet	Gray	Black
A2503	10281610	Hex 4-bit comparator	Brown	Blue	Brown	Black
A2504	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2505	10281780	Quad exclusive OR gate	Brown	Violet	Gray	Black
A2506	10281780	Quad exclusive OR gate	Brown	Violet	Gray	Black
A2507	587117-102	Hex inverter	Brown	-	Violet	
A2508	587117-102	Hex inverter	Brown	-	Violet	
A2509	10281603	4-bit multiplexer	Brown	Blue	Black	Orange
A2510	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2511	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2512	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2513	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2514	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A2515	10281610	Hex 4-bit comparator	Brown	Blue	Brown	Black
A2516	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A2517	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A2518	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2519	587117-102	Hex inverter	Brown	-	Violet	-
A2520	587102-102	Quad 2-input NAND gate	-	-	Red	-

Table 5-37. Video Processor Unit1A1A1A5, Circuit Card Location
-Continued-

CARD SLOT	PART NUMBER	CARD TYPE	COLOR CODE 1	2	3	4
A2521-	-	_				
A2522	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2523	10281610	Hex 4-bit comparator	Brown	Blue	Brown	Black
A2524	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A2525	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A2526	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2527	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2528	10281606	Hex 4-bit shift register	Brown	Blue	Black	Blue
A2529	-	-	-	-	-	-
A2530	587119-100	240-ohm resistor	-	-	-	-
A2531	587104-102	Dual 4-input NAND gate	-	-	Yellow	-
A2532	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2533	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2534	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2535	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2536	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2537	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2538	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2539	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2540	587102-102	Quad 2-input NAND gate	-	-	Red	-
A2541	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A2542	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A2543	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A2544	10281609	Quint 4-bit adder	Brown	Blue	Black	White
A2545	587117-102	Hex inverter	Brown	-	Violet	-
A2546	587117-102	Hex inverter	Brown	-	Violet	-
A2547	W447	Connector	-	-	-	-
A2548	W448	Connector	-	-	-	-
A2549	W446	Connector	-	-	-	-
A2550	W437	Connector	-	-	-	-
A2551	W451	Connector	-	-	-	-

[1]Card retainers are marked white indicating that the card must be removed from the cabinet and installed in the MTS for A testing.

Table 5-38. Card Pin to Test Point Correlation

	Card Type 587xxx1				Card Type 10281xxx					
Pin	101 to 110	107	117	124	643[2]	606 602 603 780	610 652 601	609	629	645[2]
1	2B	2A	2B	5A	2B				3B	2B
2	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND
3	2A	3A	3A	6A	2B	3B	2B	2B	3A	
4	4A		2A		2A	2A	2A	2A	2B	4A
5	3B	4B	3B		3B	4B	3B	3B	4A	3B
6	5A		5A	14A	3A	3A	3A	3A	4B	5A
7	3A	5B	4B		4B	5B	4B	4B	6B	3A
8	6A	8B	4A	12A	4A	4A	4A	4A	6A	6A
9	4B	10B	5B	7A	5B	6B	5B	5B	2A	4B1
10	7A	9B	6B	13A	5A	5A	5A	5A	7A	7A
11	5B	11B	7A		6B	7B	6B	6B	5A	5B
12	+5V	+5V	+5V	+5V	+5V	+5V	+5V	+5V	+5V	+5V
13	6B		6A		7A	7A		7A	7A	6B
14	9A	8B	6A	6A	6A	6A	9A	6A		9A
15	7B		7B		7B	8B	7B	7B	5B	
16	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND
17	8B		9B	25A	8B	9B	9B	8B	17A	
18	10A	9A	18A	9A	9A	9A	9A	8B	10A	
19	9B		10B	24A	9B	10B	9B	9B	15B	9B
20	11A		10A	19A	10A	10A	10A	10A	15A	1A
21	10B		11B	26A	10B	11B	10B	10B		10B
22	12A		14A	2B	11A	11A	11A	11A	12B	12A
23	11B		12A		11B	12B	11B	11B		11B
24	13A		13A		12A	12A	12A	12A		13A
25	12B		11A		12B	14A	12B	12B		12B
26	14A		12B	3B	13A	13A	13A	13A	16A	14A
27	13B		13B		14A	13B	14A	14A		
28	+5V	+5V	+5V	+5V	+5V	+5V	+5V	+5V	+5V	+5V
29	14B		15B	10A	13B	14B	13B	13B		
30	15A		14B		15A	15A	15A	16A	13A	15A
31	15B		16B		14B	15B	14B	15A		15B
32	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND
33	16B		17B	9A	16A	16B	16A	14B	18B	16B
34	16A	12B	15A	15A	15B	16A	15B	15B	14B	16A
35	17B		13A		16B	17B	16B	16B	19B	17B
36	17A	13B	16A	16A	17A	17A	17A	17A	19A	17A
37	18B		17A	22A	17B	18B	17B	17B	22B	18B
38	18A	6B	20A		18A	18A	18A	18A	20A	18A
39	19B		19B	21A	15B	19B	18B	18B	24B	
40	19A	7B	19A		19A	19A	19A	19A	31A	19A
41	22B		23B		19B	22B		19B	19B	26B
42	20A		23B		20A	20A	20A	20A	23B	20A
43	23B		24B		22B	23B	22B	22B	23B	23B
44	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND

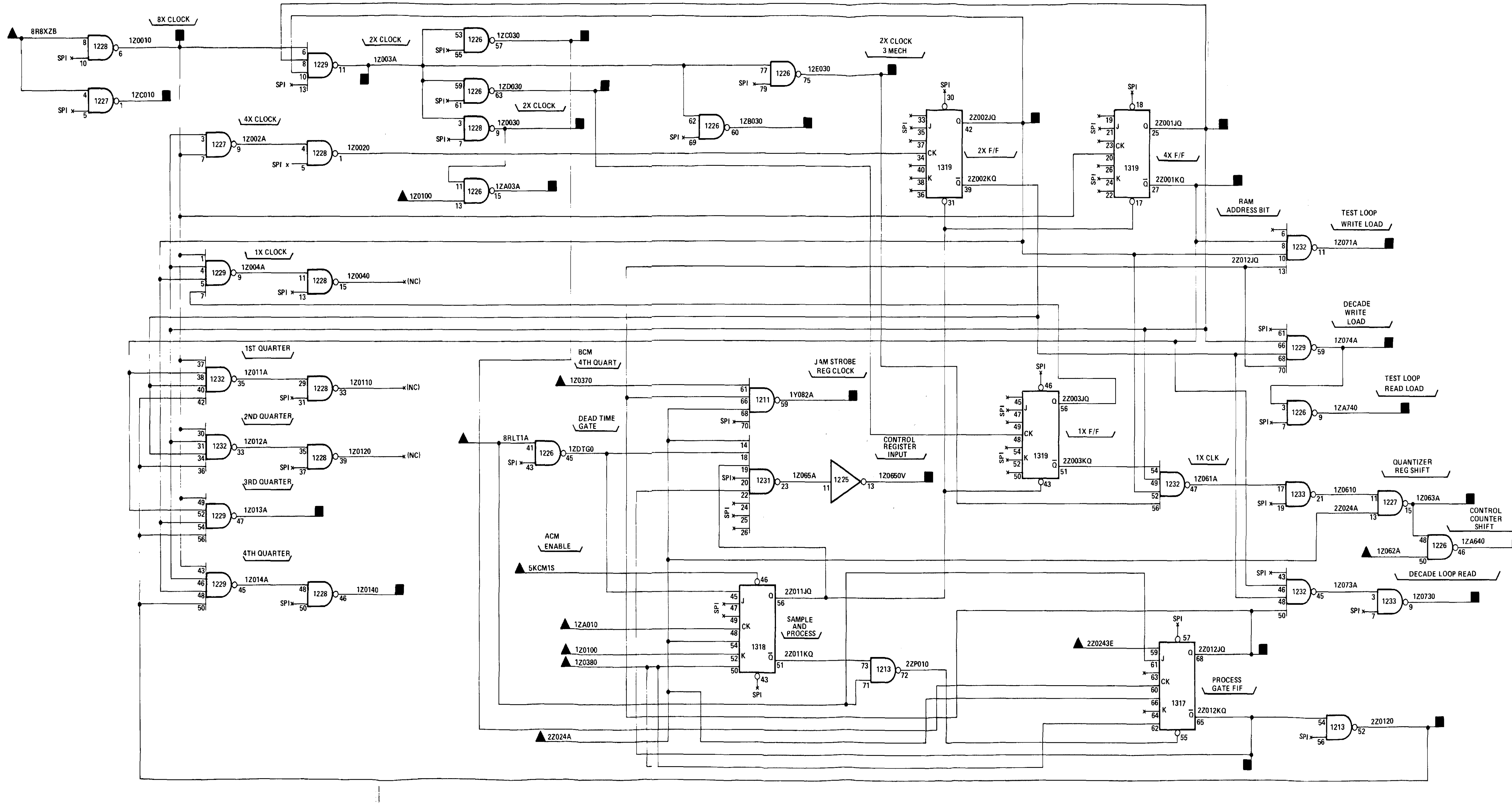
See footnote at end of table.

Table 5-38. Card Pin to Test Point Correlation
-Continued-

	Card Type 587xxx1					Card Type 10281xxx					
Pin	101 to 110	107	117	124		643[2]	606 602 603 780	610 652 601	609	629	645[2]
45	24B		25B			23B	24B	23B	23B		24B
46	21A		23B			21A	21A	21A	21A	25B	21A
47	25B		24A			23A	25B	23A	23A		25B
48	22A		21A			22A	22A	22A	22A	31B	22A
49	26B		23A			24B	26B	24A	24B		26B
50	23A		22A			24A	23A		24A	28A	23A
51	27B		27B			25B	27B	25B	25A		
52	24A		26A		20A	25A	24A	26B	25B	29B	24A
53	28B		26B			26B	28B	26A	26A		
54	25A		25A			26A	25A	27B	26B		26A
55	29B		29B			27B	29B	28B	28B		29B
56	26A		28B			28B	26A	28A	27B		26A
57	30B		30B			29B	28A	29B	29B		30B
58	GND	GND	GND		GND	GND	GND	GND	GND	GND	GND
59	31B		31B			30B	30B	30B	30B		31B
60	28A		28A			28A	29A	29A	28A		28A
61	32B		31A			31B	31B	31B	31B		32B
62	29A		29A			29A	30A	30A	29A	32A	29A
63	33B		30A			31A	32B		31A		
64	30A		33A			30A	31A	31A	30A		30A
65	34B		33B				33B		33B		
66	31A		32A			32B	32A	33A	32B		31A
67	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND
68	32A			32B		33A	33A	34A	32A		32A
69	35A			35B		32A	34B	33B	33B	32B	35A
70	33A			34B		34A	34A	35A	33A	34B	33A
71	36A			36B		33B	35B	34B	34B	36B	36A
72	34A			34A		35A	35A	36A	34A	35B	34A
73	36B			37B		34B	36B	35B	35B	38B	36B
74	35B			35A		36A	36A	26A	25A	33A	35B
75	36B			39A		35B	36B	36B	36B	38A	37B
76	37A			37A		37A	37A	36A	36A	37B	37A
77	35B			38A		36B	38B	37B	37B		38B
78	33A			36A		39A	38A		37A	34A	38A
79	39B			39B		37B	39B	38B	38B	39B	39B
80	39A			38B		38B	39A		38A		

[1]110283XXX card types have identical 1C/test point/card pin correlation as 587XXX card types as follows:
10283626 is same as 587102
10283627 is same as 587108
10283628 is same as 587103
10283629 is same as 587106
10283630 is same as 587107
[2]10283XXX

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
1ZA010	00703	1Y082A	00500
1Z0100	00200	1ZA03A	00600
1Z0370	00703	1ZA640	00400
1Z0380	00200	1ZA740	00800
1Z062A	00200	1ZB030	01101, 01102, 01103
2Z024A	00400	1ZC010	00701, 00702, 00703, 00800
2Z0243E	00400	1ZC030	00400, 00600
5KCM1S	13400	1ZD030	00200, 00704
8RLTIA	14500	1ZE030	00500
8RLTIA	26802	1Z0010	05900
8RLTIA	26901	1Z003A	00400
8R8XZB	14300	1Z0030	00400
8R8XZB	27201	1Z013A	00703
		1Z0140	00200
		1Z063A	00400
		1Z0650V	00400
		1Z071A	00800
		1Z0730	00701, 00703
		1Z074A	00800
		2Z001JQ	01400
		2Z001KQ	00800, 00900, 26802, 30502
		2Z002JQ	00800, 01400
		2Z012JQ	00500, 00600, 00800
		2Z012KQ	00800
		2Z0120	00200, 00400

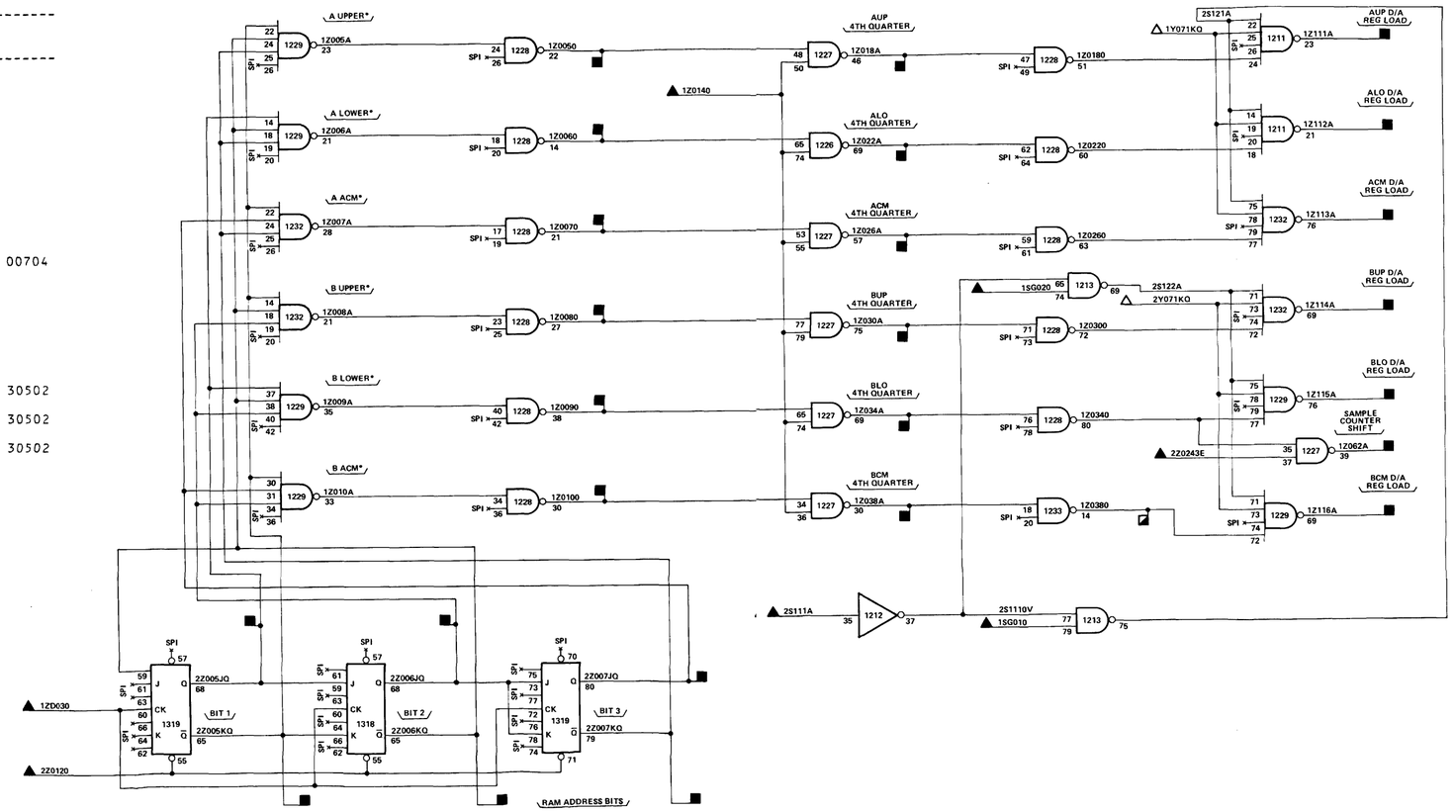


NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INPUT FROM ANOTHER FIGURE
   - △ INPUT FROM SAME FIGURE
   - ■ OUTPUT TO ANOTHER FIGURE
   - □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   - ◪ OUTPUT TO SAME FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.
    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.
    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.
11. SPIXXX INDICATES PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

FO-1. CFAR Modulo-Eight Counter Logic Diagram

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
1SG010	26601	1Z0050	00703, 00704
1SG010	31102	1Z0060	00703, 00704
1SG020	31102	1Z0070	00703, 00704
1Y071KQ	00500	1Z0080	00703, 00704
1Z0030	00100	1Z0090	00703, 00704
1Z0140	00100	1Z0100	00100, 00703, 00704
2S111A	01200	1Z018A	00400
2Y071KQ	00500	1Z022A	00400
2Z0120	00100	1Z026A	00400
2Z0243E	00400	1Z030A	00400
		1Z034A	00400
		1Z038A	00400
		1Z0380	00100, 00500, 00600, 00704
		1Z062A	00100, 00400
		1Z111A	01101
		1Z112A	01101
		1Z113A	01102
		1Z114A	01102
		1Z115A	01103
		1Z116A	01103
		2Z005JQ	01000, 01400
		2Z005KQ	00900, 01200, 26802, 30502
		2Z006JQ	01000, 01400
		2Z006KQ	00900, 01200, 26802, 30502
		2Z007JQ	01000, 01400
		2Z007KQ	00900, 01200, 26802, 30502



FO-2. CFAR Modulo-Six Counter Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

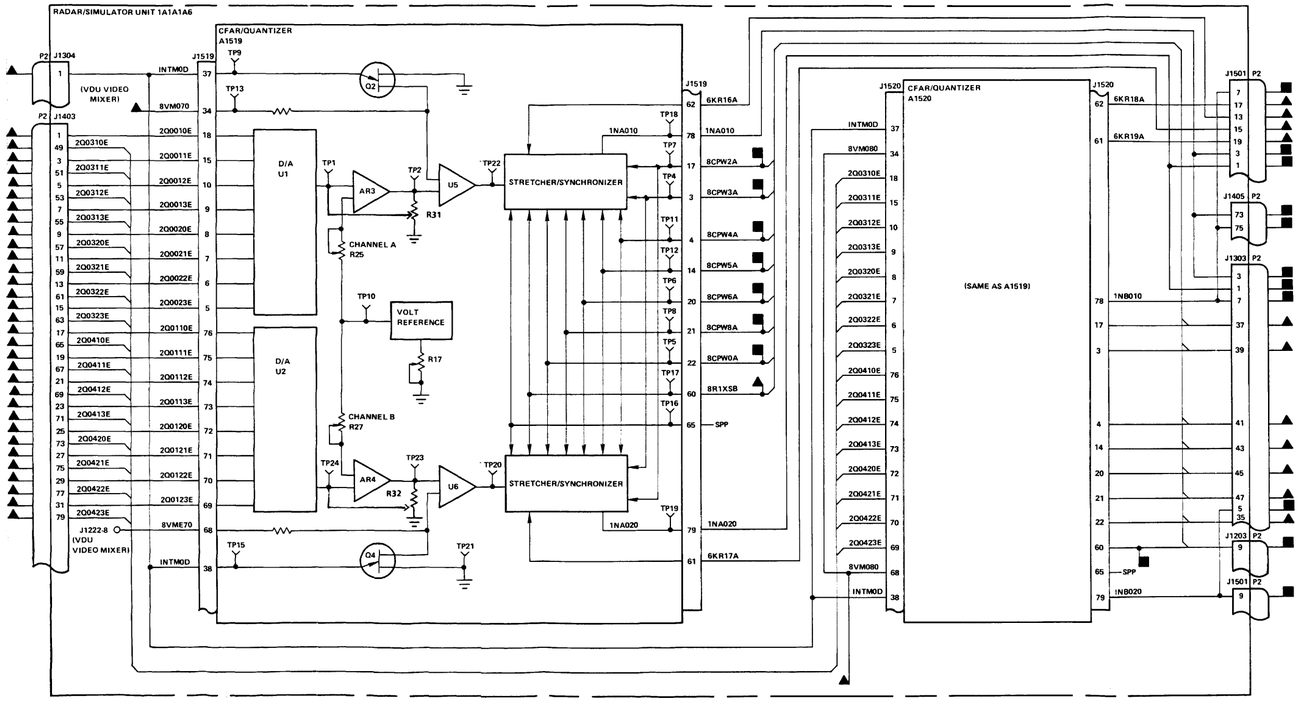
    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

MS20057

INPUT SIGNAL	INPUT SOURCE FO-SH	INPUT SIGNAL	INPUT SOURCE FO-SH	OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
INTM0D	26601	2Q0410E	01103	1NA010	00400, 28901
INTM0D	28801	2Q0410E	29702	1NA020	00400, 28901
2Q0010E	01101	2Q0411E	01103	1NB010	00400, 28901
2Q0010E	29701	2Q0411E	29702	1NB020	00400, 28901
2Q0011E	01101	2Q0412E	01103	8R1XSB	00300, 01501, 03800, 06600,
2Q0011E	29701	2Q0412E	29702		07600, 08300, 08900, 11200,
2Q0012E	01101	2Q0413E	01103		11400, 11600, 11700, 11800,
2Q0012E	29701	2Q0413E	29702		14300, 15001, 15002, 15100,
2Q0013E	01101	2Q0420E	01103		15200, 16700, 26802, 26803,
2Q0013E	29701	2Q0420E	29702		27501
2Q0020E	01101	2Q0421E	01103		
2Q0020E	29701	2Q0421E	29702		
2Q0021E	01101	2Q0422E	01103		
2Q0021E	29701	2Q0422E	29702		
2Q0022E	01101	2Q0423E	01103		
2Q0022E	29701	2Q0423E	29702		
2Q0023E	01101	6KR16A	05702		
2Q0023E	29701	6KR17A	05702		
2Q0110E	01101	6KR18A	05702		
2Q0110E	29701	6KR19A	05702		
2Q0111E	01101	8CPW0A	26504		
2Q0111E	29701	8CPW0A	26803		
2Q0112E	01101	8CPW0A	27101		
2Q0112E	29701	8CPW0A	28901		
2Q0113E	01101	8CPW2A	26802		
2Q0113E	29701	8CPW2A	26803		
2Q0120E	01101	8CPW2A	27101		
2Q0120E	29701	8CPW2A	28901		
2Q0121E	01101	8CPW2A	32102		
2Q0121E	29701	8CPW3A	26803		
2Q0122E	01101	8CPW3A	27101		
2Q0122E	29701	8CPW3A	28901		
2Q0123E	01101	8CPW4A	26803		
2Q0123E	29701	8CPW4A	27101		
2Q0310E	01102	8CPW4A	28901		
2Q0310E	29702	8CPW5A	26803		
2Q0311E	01102	8CPW5A	27101		
2Q0311E	29702	8CPW5A	28902		
2Q0312E	01102	8CPW6A	26803		
2Q0312E	29702	8CPW6A	27101		
2Q0313E	01102	8CPW6A	28902		
2Q0313E	29702	8CPW8A	26803		
2Q0320E	01102	8CPW8A	27101		
2Q0320E	29702	8CPW8A	28902		
2Q0321E	01102	8R1XSB	00300		
2Q0321E	29702	8R1XSB	14900		
2Q0322E	01102	8R1XSB	26802		
2Q0322E	29702	8R1XSB	26803		
2Q0323E	01102	8R1XSB	27501		
2Q0323E	29702	8VM070	14202		



Change 1 FO-3. CFAR Quantizers Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

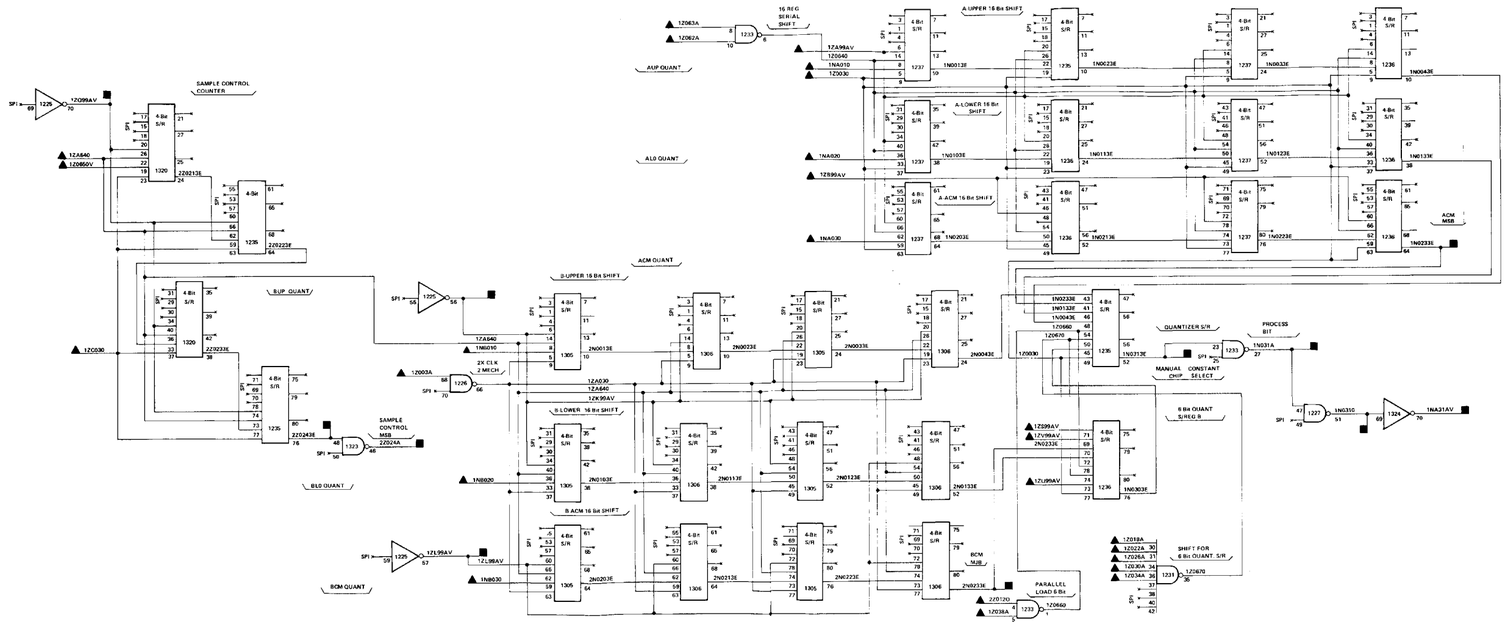
1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, RSU (1A1A1A6).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO APPROPRIATE TABLE IN TM 9-1430-655-20-3 FAR CARD PART NUMBER.

MS200576A

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
1NA010	00300	1NA31AV	00800
1NA010	26802	1N0233E	00500
1NA010	27001	1N031A	00800, 01200, 01300, 26802, 30602
1NA020	00300		
1NA020	26802	1N0310	00701, 01200
1NA020	27001	1N0313E	00800, 01300, 01400, 26802, 30601
1NA030	03800		
1NA030	30601	1ZK99AV	00702, 00703, 01101
1NB010	00300	1ZL99AV	01101, 01102, 01103
1NB010	26802	1ZG99AV	00500, 01101
1NB010	27001	2N0233E	00500
1NB020	00300	2Z0244	00100, 00500
1NB020	26802	2Z0243E	00100, 00200
1NB020	27001		
1NB030	03800		
1NB030	30601		
1ZA640	00100		
1ZA99AV	00500		
1ZB99AV	00500		
1ZC030	00100		
1ZS99AV	00600		
1ZS99AV	30502		
1ZU99AV	00600		
1ZV99AV	01101		
1ZD03A	00100		
1ZD030	00100		
1ZD18A	00200		
1ZD22A	00200		
1ZD26A	00200		
1ZD30A	00200		
1ZD34A	00200		
1ZD38A	00200		
1ZD62A	00200		
1ZD63A	00100		
1ZD650V	00100		
2Z0120	00100		



FO-4. CFAR Quantizer Input Register, Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

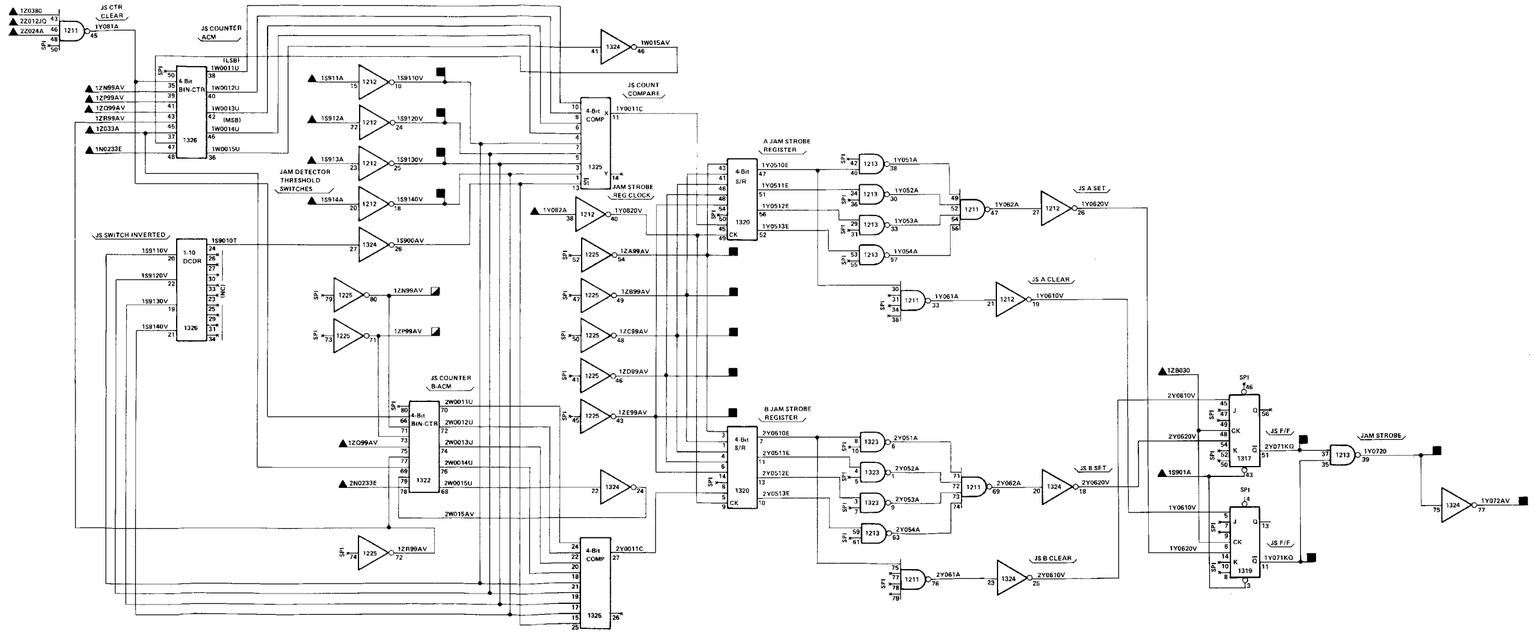
    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

MS200577

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
1N0233E	00400	1S9110V	01400
1S901A	31901	1S9120V	01400
1S911A	26802	1S9130V	01400
1S911A	31901	1S9140V	01400
1S912A	26802	1Y071KQ	00200
1S912A	31901	1Y072AV	03900
1S913A	26802	1Y0720	01400, 26802
1S913A	31901	1ZA99AV	00400, 00800
1S914A	26802	1ZB99AV	00400, 00701, 00702, 00703, 00800
1S914A	31901		
1Y082A	00100	1ZC99AV	00701, 00702, 00703, 00800
1ZE030	00100	1ZD99AV	00800
1ZE99AV	00400	1ZE99AV	00703, 00800
1ZQ33A	00703	1ZN99AV	01101, 01102, 01103, 01200
1ZQ380	00200	1ZP99AV	01101, 01102, 01103
2N0233E	00400	2Y071KQ	00200
2Z012JQ	00100		
2Z024A	00400		



FO-5. CFAR Jam Strobe Detector Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOCK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

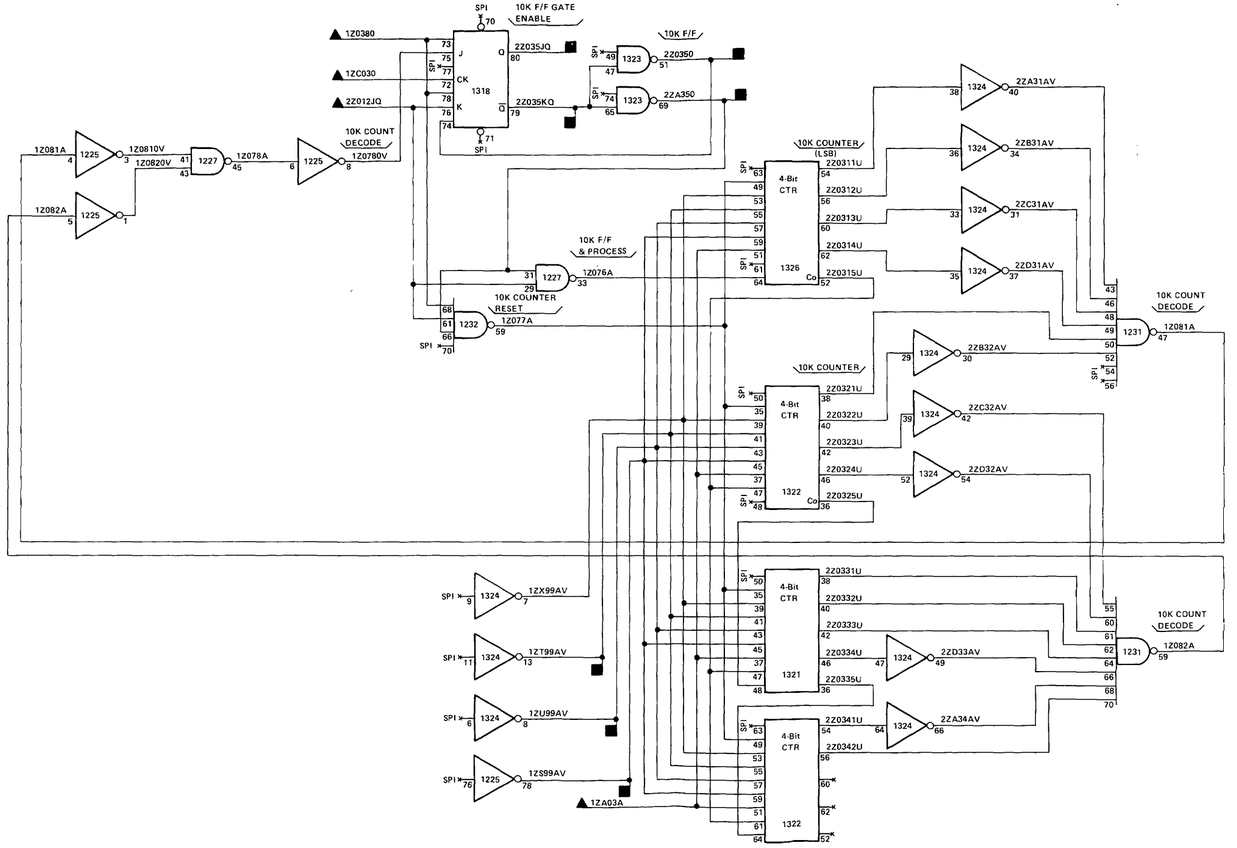
    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

MS200578

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
1ZA03A	00100	1ZS99AV	00400, 00800, 00900, 26802, 30502
1ZC030	00100	1ZT99AV	00800, 01101
1Z0380	00200	1ZU99AV	00400, 00800, 01101
2Z012JQ	00100	2ZA350	00702
		2Z035JQ	00701, 00702, 00703, 00704
		2Z035KQ	00701, 00704
		2Z0350	00703, 01400



FO-6. CFAR 10K Counter Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

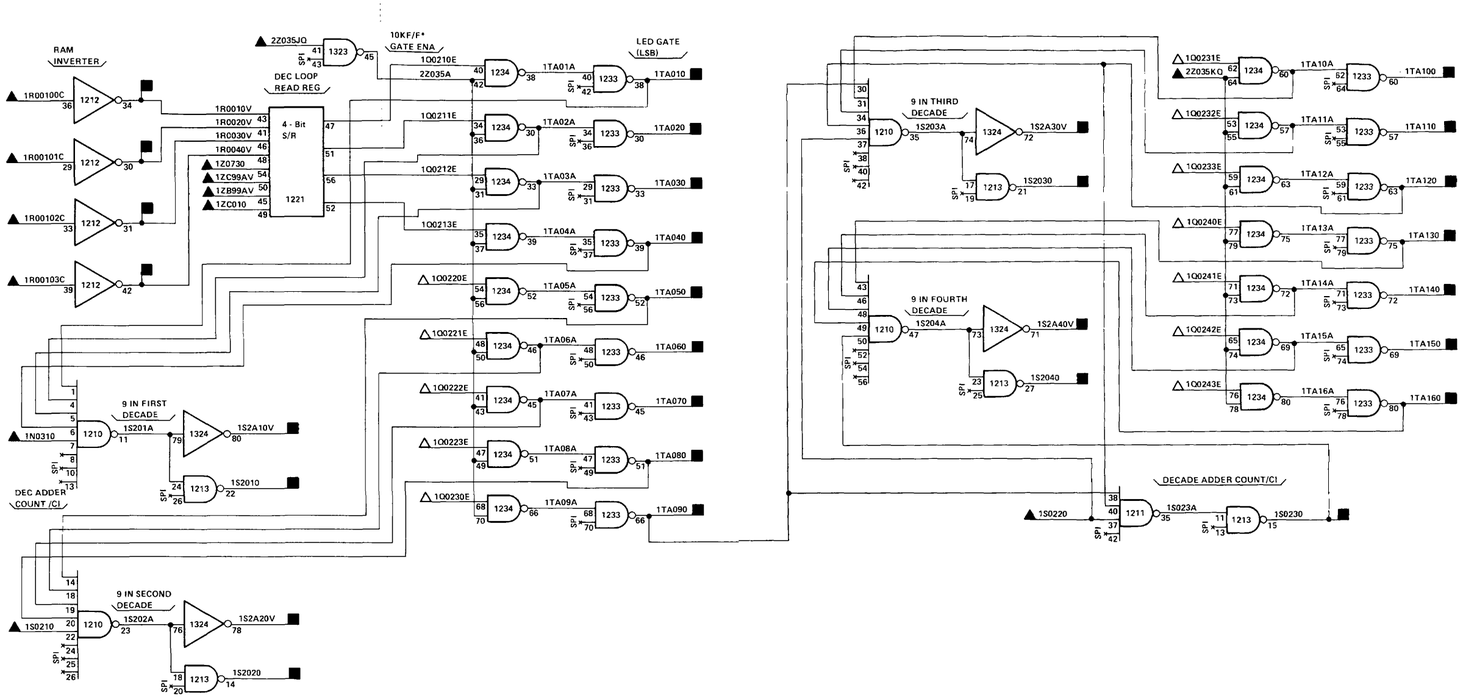
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
1N0310	00400	1R0010V	01400
1R00100C	00900	1R0020V	01400
1R00100C	26802	1R0030V	00800, 01400
1R00100C	30501	1R0040V	00800, 01400
1R00101C	00900	1S0230	00800
1R00101C	26802	1S2A10V	00800
1R00101C	30501	1S2A20V	00800
1R00102C	00900	1S2A30V	00800
1R00102C	26802	1S2A40V	00800
1R00102C	30501	1S2010	00800
1R00103C	00900	1S2020	00800
1R00103C	26802	1S2030	00800
1R00103C	30501	1S2040	00800
1S0210	00800	1TA010	00800
1S0220	00800	1TA020	00800
1ZB99AV	00500	1TA030	00800
1ZC010	00100	1TA040	00800
1ZC99AV	00500	1TA050	00800
1ZD730	00100	1TA060	00800
2Z035JQ	00600	1TA070	00800
2Z035KQ	00600	1TA080	00800
		1TA090	00800
		1TA100	00800
		1TA110	00800
		1TA120	00800
		1TA130	00800
		1TA140	00800
		1TA150	00800
		1TA160	00800



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1AS).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

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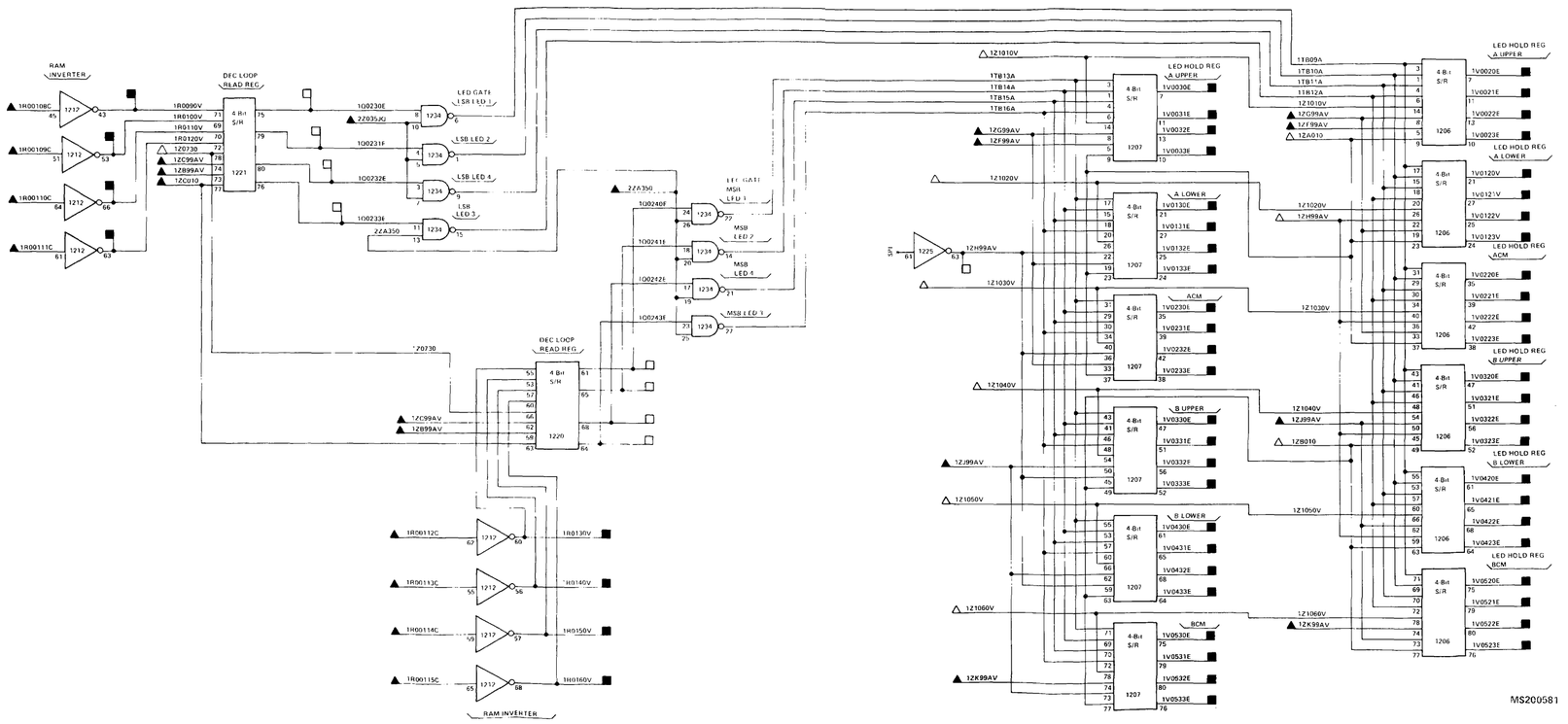
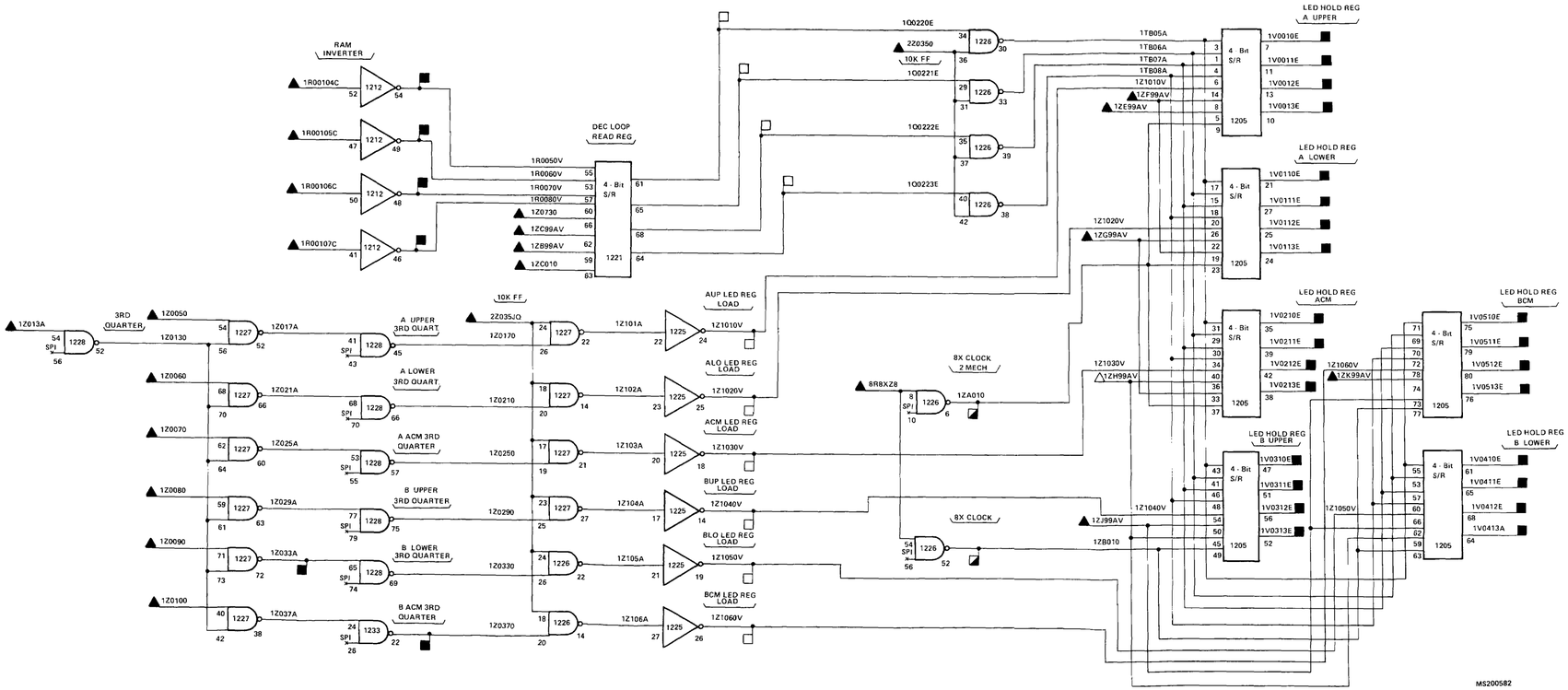
FO-7. CFAR Decode Loop Read Register/LED Holding Register Logic Diagram (Sheet 1of 4)

INPUT		OUTPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH	SIGNAL	DESTINATION FO-SH
1R00108C	00900	1R0090V	00800, 01400	1V0432E	26802, 31102
1R00108C	26802	1R0100V	00800, 01400	1V0433E	26802, 31102
1R00108C	30502	1R0110V	00800, 01400	1V0520E	26802, 31102
1R00109C	00900	1R0120V	00800, 01400	1V0521E	26802, 31102
1R00109C	26802	1R0130V	01400	1V0522E	26802, 31102
1R00109C	30502	1R0140V	01400	1V0523E	26802, 31102
1R00110C	00900	1R0150V	01400	1V0530E	26802, 31102
1R00110C	26802	1R0160V	01400	1V0531E	26802, 31102
1R00110C	30502	1V0020E	26601, 26802, 31101	1V0532E	26802, 31102
1R00111C	00900	1V0021E	26601, 26802, 31101	1V0533E	26802, 31102
1R00111C	26802	1V0022E	26601, 26802, 31101		
1R00111C	30502	1V0023E	26601, 26802, 31101		
1R00112C	00900	1V0030E	26601, 26802, 31101		
1R00112C	26802	1V0031E	26601, 26802, 31101		
1R00112C	30502	1V0032E	26601, 26802, 31101		
1R00113C	00900	1V0033E	26601, 26802, 31101		
1R00113C	26802	1V0120E	26802, 29401		
1R00113C	30502	1V0121E	26802, 29401		
1R00114C	00900	1V0122E	26802, 29401		
1R00114C	26802	1V0123E	26802, 29401		
1R00114C	30502	1V0130E	26802, 29401		
1R00115C	00900	1V0131E	26802, 29401		
1R00115C	26802	1V0132E	26802, 29401		
1R00115C	30502	1V0133E	26802, 29401		
1ZB99AV	00500	1V0220E	26802, 29401		
1ZC010	00100	1V0221E	26802, 29401		
1ZC99AV	00500	1V0222E	26802, 29401		
1ZF99AV	01000	1V0223E	26802, 29401		
1ZG99AV	00800	1V0230E	26802, 29401		
1ZJ99AV	01200	1V0231E	26802, 29401		
1ZK99AV	00400	1V0232E	26802, 29401		
2ZA350	00600	1V0233E	26802, 29401		
2ZQ35JQ	00600	1V0320E	26802, 31101		
		1V0321E	26802, 31101		
		1V0322E	26802, 31101		
		1V0323E	26802, 31101		
		1V0330E	26802, 31102		
		1V0331E	26802, 31102		
		1V0332E	26802, 31102		
		1V0333E	26802, 31102		
		1V0420E	26802, 31102		
		1V0421E	26802, 31102		
		1V0422E	26802, 31102		
		1V0423E	26802, 31102		
		1V0430E	26802, 31102		
		1V0431E	26802, 31102		



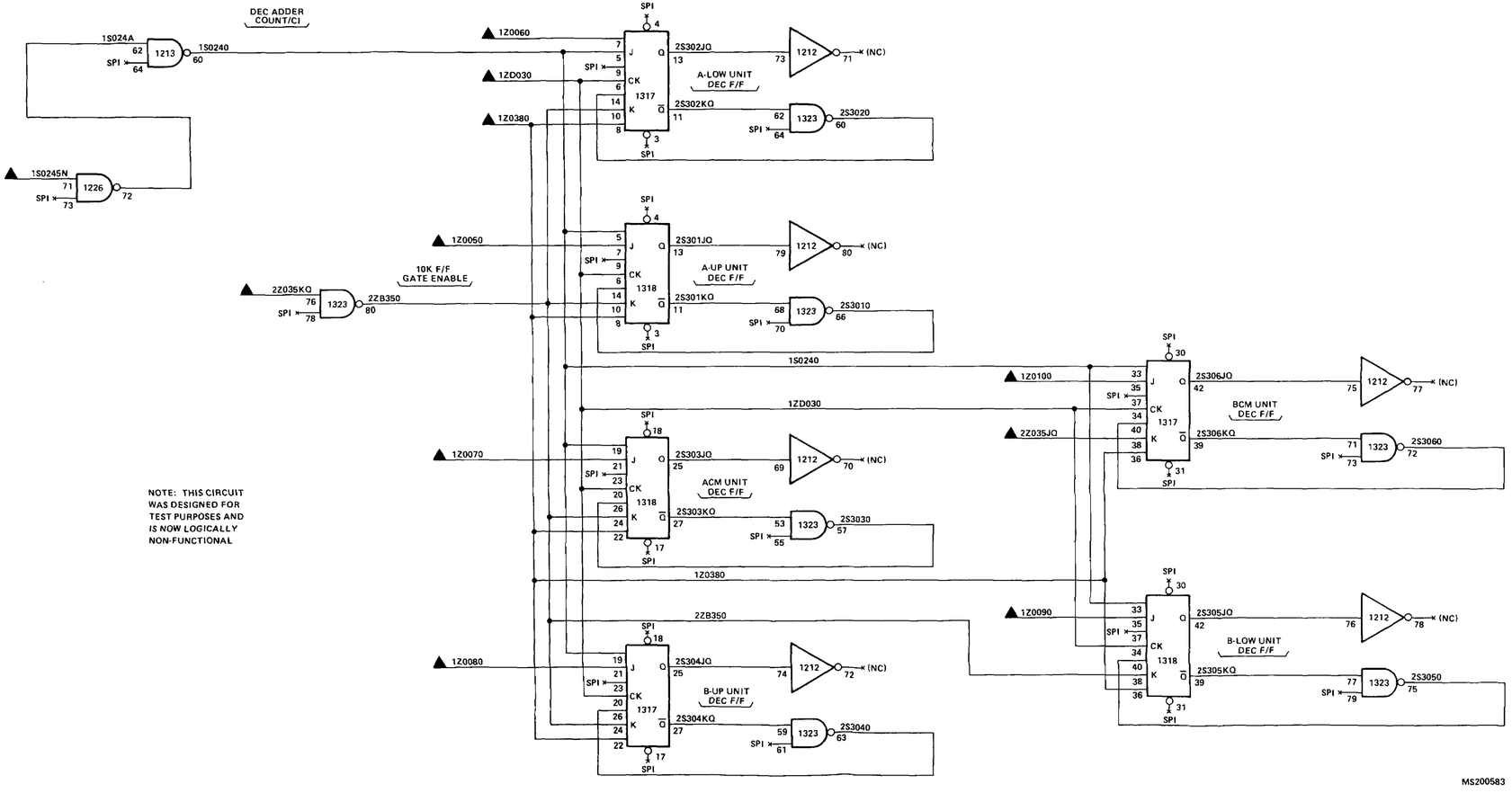
FO-7. CFAR Decode Loop Read Register/LED Holding Register Logic Diagram (Sheet 2 of 4)

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
1R00104C	00900	1R0050V	00800, 01400
1R00104C	26802	1R0060V	00800, 01400
1R00104C	30501	1R0070V	00800, 01400
1R00105C	00900	1R0080V	00800, 01400
1R00105C	26802	1V0010E	26601, 26802, 31101
1R00105C	30501	1V0011E	26601, 26802, 31101
1R00106C	00900	1V0012E	26601, 26802, 31101
1R00106C	26802	1V0013E	26601, 26802, 31101
1R00106C	30501	1V0110E	26802, 29401
1R00107C	00900	1V0111E	26802, 29401
1R00107C	26802	1V0112E	26802, 29401
1R00107C	30502	1V0113E	26802, 29401
1ZB99AV	00500	1V0210E	26802, 29402
1ZC010	00100	1V0211E	26802, 29402
1ZC99AV	00500	1V0212E	26802, 29401
1ZE99AV	00500	1V0213E	26802, 29402
1ZF99AV	01000	1V0310E	26802, 31101
1ZG99AV	00800	1V0311E	26802, 31101
1ZJ99AV	01200	1V0312E	26802, 31101
1ZK99AV	00400	1V0313E	26802, 31101
1Z0050	00200	1V0410E	26802, 31102
1Z0060	00200	1V0411E	26802, 31102
1Z0070	00200	1V0412E	26802, 31102
1Z0080	00200	1V0413E	26802, 31102
1Z0090	00200	1V0510E	26802, 31102
1Z0100	00200	1V0511E	26802, 31102
1Z013A	00100	1V0512E	26802, 31102
1Z0730	00100	1V0513E	26802, 31102
2Z035JQ	00600	1ZA010	00100
2Z0350	00600	1ZB010	00800
8R8XZB	14300	1Z033A	00500
8R8XZB	27201	1Z0370	00100



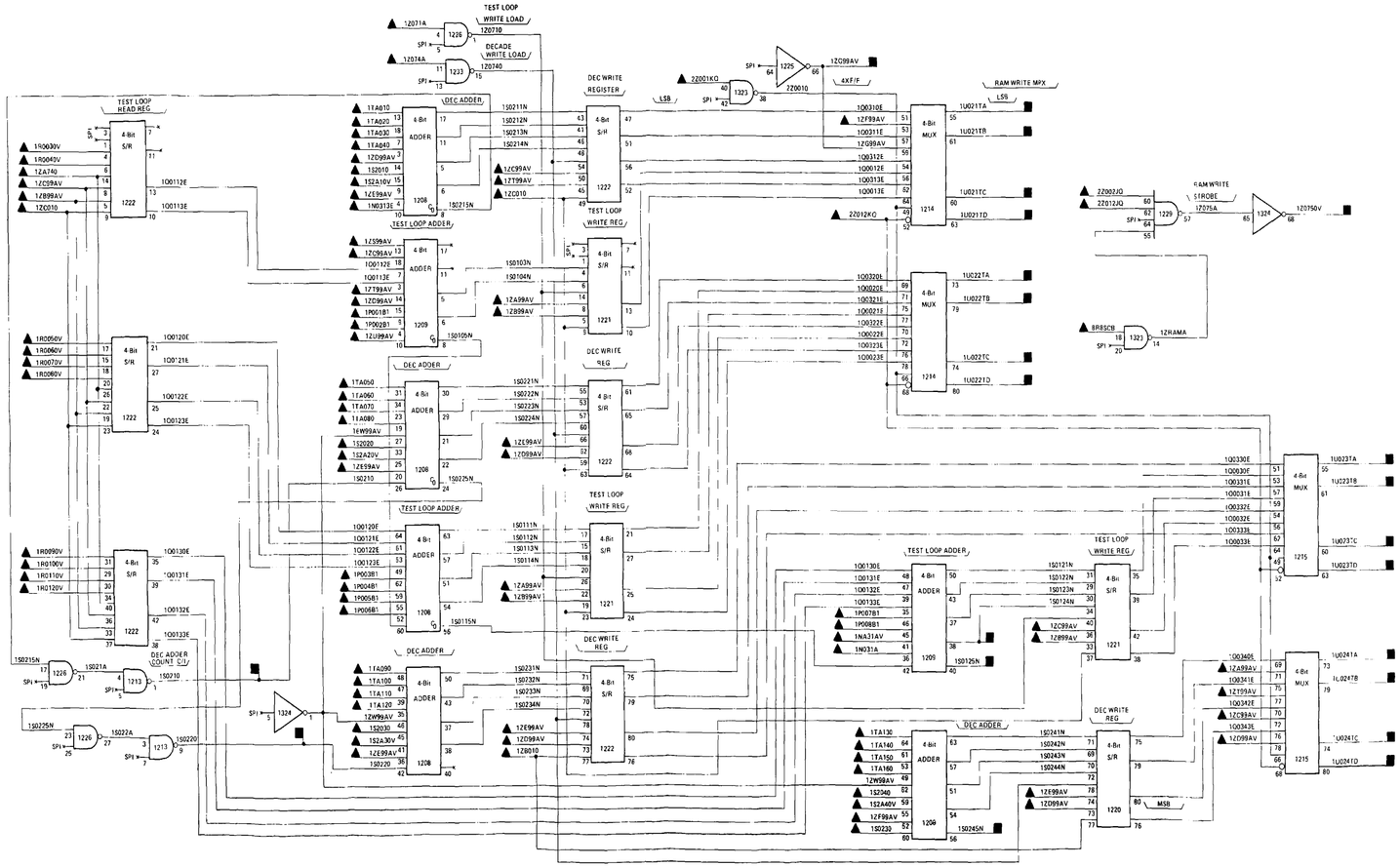
FO-7. CFAR Decode Loop Read Register/LED Holding Register Logic Diagram (Sheet 3 of 4)

INPUT SIGNAL	SOURCE FO-SH
1S0245N	00800
1ZD030	00100
1Z0050	00200
1Z0060	00200
1Z0070	00200
1Z0080	00200
1Z0090	00200
1Z0100	00200
1Z0380	00200
2Z035JQ	00600
2Z035KQ	00600



FO-7. CFAR Decode Loop Read Register/LED Holding Register Logic Diagram (Sheet 4 of 4)

INPUT		INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
1NA31AV	00400	1TA150	00701	1S0124N	01200
1N031A	00400	1TA160	00701	1S0125N	01200
1N031A	30602	1ZA740	00100	1S0210	00701
1N0313E	00400	1ZA99AV	00500	1S0220	00701
1N0313E	30601	1ZB010	00703	1S0245N	00704
1P001B1	26802	1ZB99AV	00500	1U021TA	00900, 26802, 30501
1P002B1	26802	1ZC010	00100	1U021TB	00900, 26802, 30501
1P003B1	26802	1ZC99AV	00500	1U021TC	00900, 26802, 30501
1P004B1	26802	1ZD99AV	00500	1U021TD	00900, 26802, 30501
1P005B1	26802	1ZE99AV	00500	1U022TA	00900, 26802, 30501
1P006B1	26802	1ZF99AV	01000	1U022TB	00900, 26802, 30501
1P007B1	26802	1ZS99AV	00600	1U022TC	00900, 26802, 30501
1P008B1	26802	1ZS99AV	30502	1U022TD	00900, 26802, 30501
1R0030V	00701	1ZT99AV	00600	1U023TA	00900, 26802, 30501
1R0040V	00701	1ZU99AV	00600	1U023TB	00900, 26802, 30501
1R0050V	00703	1ZU71A	00100	1U023TC	00900, 26802, 30501
1R0060V	00703	1ZU74A	00100	1U023TD	00900, 26802, 30501
1R0070V	00703	2Z001KQ	00100	1U024TA	00900, 26802, 30501
1R0080V	00703	2Z001KQ	30502	1U024TB	00900, 26802, 30501
1R0090V	00702	2Z002JQ	00100	1U024TC	00900, 26802, 30501
1R0100V	00702	2Z012JQ	00100	1U024TD	00900, 26802, 30501
1R0110V	00702	2Z012KQ	00100	1Z699AV	00702, 00703
1R0120V	00702	8R8SCB	26802	1Z0750V	26802
1S0230	00701				
1S2A10V	00701				
1S2A20V	00701				
1S2A30V	00701				
1S2A40V	00701				
1S2010	00701				
1S2020	00701				
1S2030	00701				
1S2040	00701				
1TA010	00701				
1TA020	00701				
1TA030	00701				
1TA040	00701				
1TA050	00701				
1TA060	00701				
1TA070	00701				
1TA080	00701				
1TA090	00701				
1TA100	00701				
1TA110	00701				
1TA120	00701				
1TA130	00701				
1TA140	00701				



FO-8. CFAR RAM Write Register/Multiplexer Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INPUT FROM ANOTHER FIGURE
   - △ INPUT FROM SAME FIGURE
   - ■ OUTPUT TO ANOTHER FIGURE
   - □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   - ◪ OUTPUT TO SAME FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.
    - B. REFER TO TABLE 5037 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

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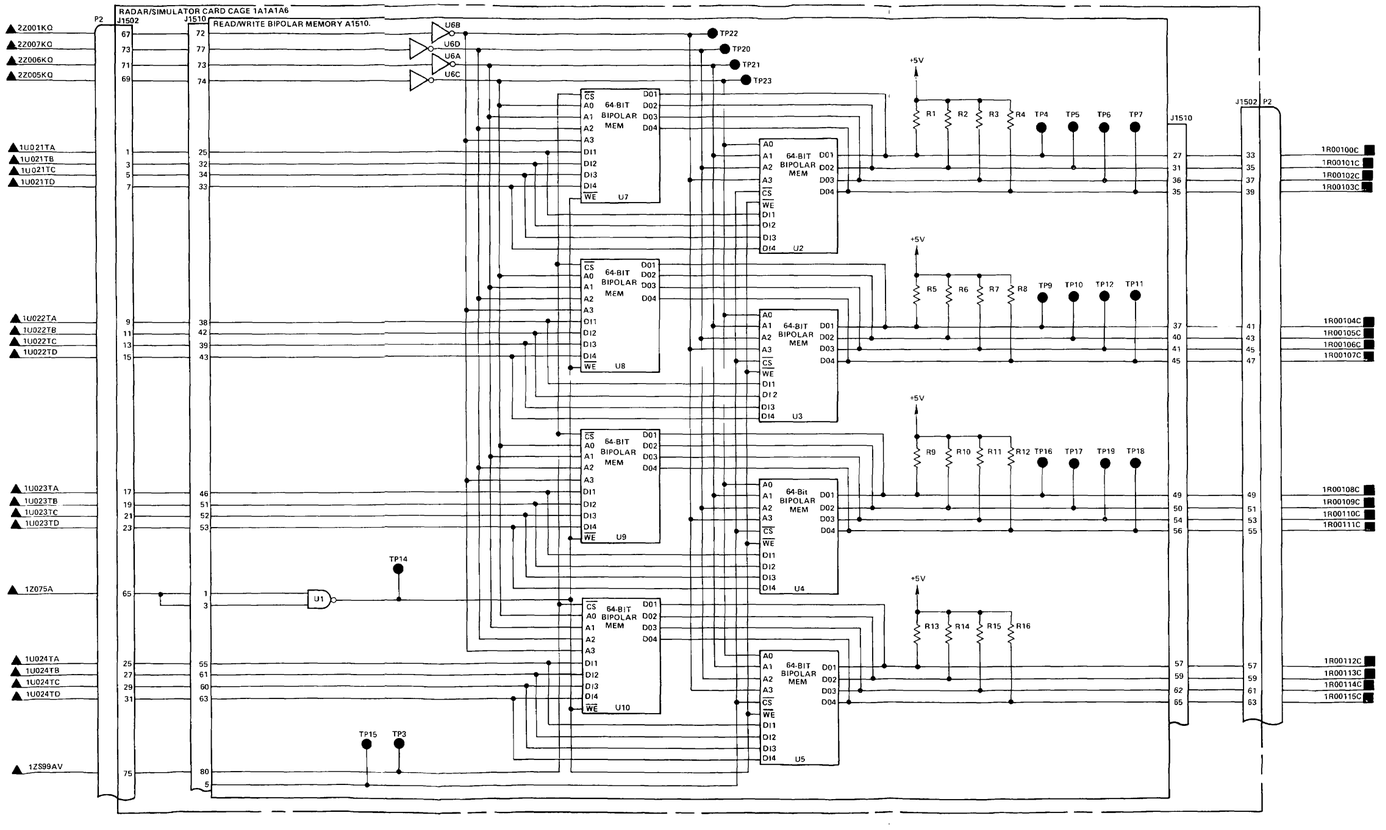
INPUT	
SIGNAL	SOURCE FO-SH
1U021TA	00800
1U021TA	30501
1U021TB	00800
1U021TB	30501
1U021TC	00800
1U021TC	30501
1U021TD	00800
1U021TD	30501
1U022TA	00800
1U022TA	30501
1U022TB	00800
1U022TB	30501
1U022TC	00800
1U022TC	30501
1U022TD	00800
1U022TD	30501
1U023TA	00800
1U023TA	30501
1U023TB	00800
1U023TB	30501
1U023TC	00800
1U023TC	30501
1U023TD	00800
1U023TD	30501
1U024TA	00800
1U024TA	30501
1U024TB	00800
1U024TB	30501
1U024TC	00800
1U024TC	30501
1U024TD	00800
1U024TD	30501
1ZS99AV	00600
1ZS99AV	30502
1Z075A	30502
2Z001KQ	00100
2Z001KQ	30502
2Z005KQ	00200
2Z005KQ	30502
2Z006KQ	00200
2Z006KQ	30502
2Z007KQ	00200
2Z007KQ	30502

OUTPUT	
SIGNAL	DESTINATION FO-SH
1R00100C	00701, 30501
1R00101C	00701, 30501
1R00102C	00701, 30501
1R00103C	00701, 30501
1R00104C	00703, 30501
1R00105C	00703, 30501
1R00106C	00703, 30501
1R00107C	00703, 30502
1R00108C	00702, 30502
1R00109C	00702, 30502
1R00110C	00702, 30502
1R00111C	00702, 30502
1R00112C	00702, 30502
1R00113C	00702, 30502
1R00114C	00702, 30502
1R00115C	00702, 30502



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FO-9. CFAR RAM Logic Diagram

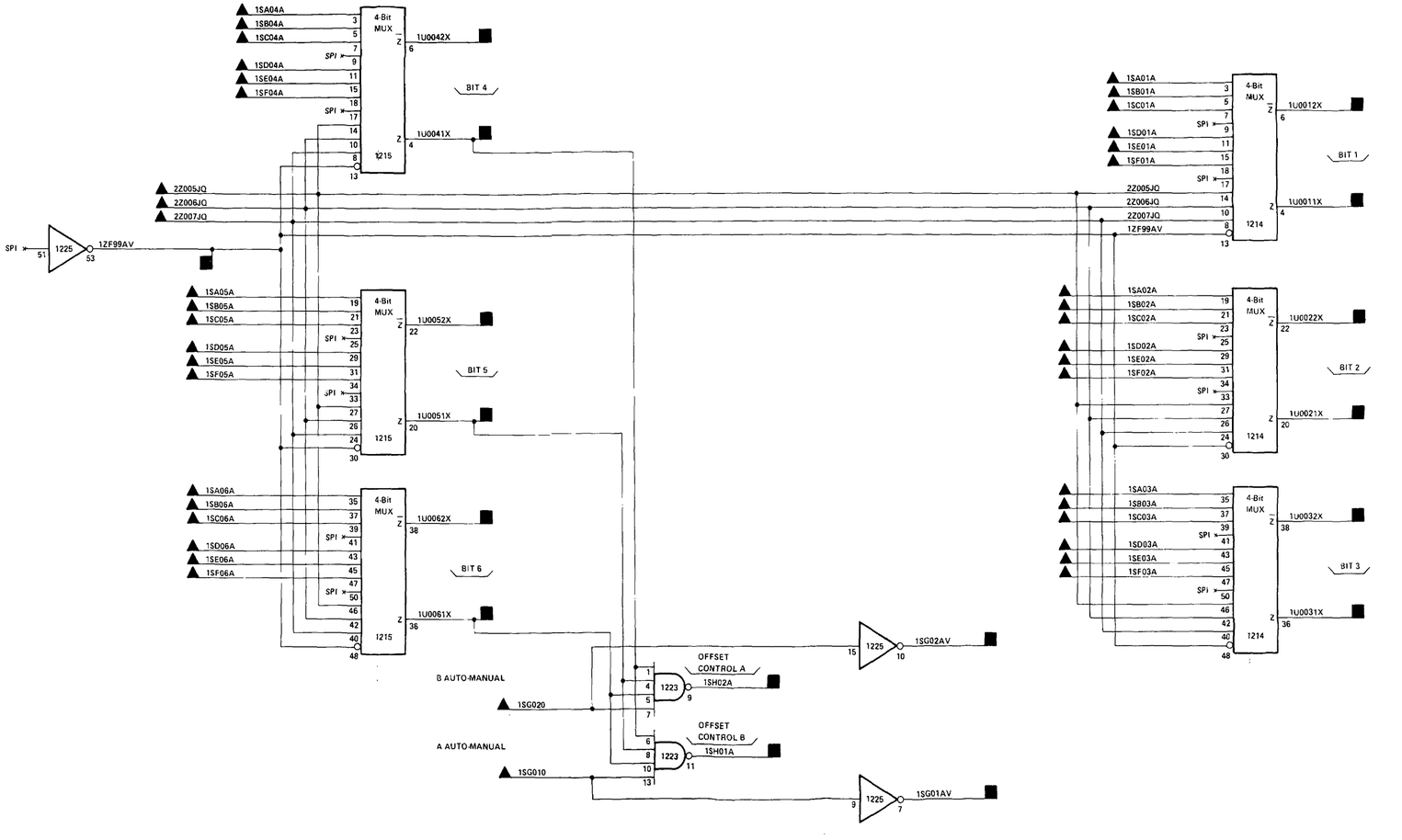
NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, RSU (1A1A1A6).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO APPROPRIATE TABLE IN TM 9-1430-655-20-3 FAR CARD PART NUMBER.

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
1SA01A	31901	1SG01AV	01101, 01102, 01400
1SA02A	31901	1SG02AV	01102, 01103, 01400
1SA03A	31901	1SH01A	01101
1SA04A	31901	1SH02A	01102
1SA05A	31901	1U0011X	01400
1SA06A	31901	1U0012X	01300, 26802, 30601
1SB01A	31901	1U0021X	01400
1SB02A	31901	1U0022X	01300, 26802, 30601
1SB03A	31901	1U0031X	01400
1SB04A	31901	1U0032X	01300, 26802, 30601
1SB05A	31901	1U0041X	01400
1SB06A	31901	1U0042X	01300, 26802, 30601
1SC01A	31901	1U0051X	01400
1SC02A	31901	1U0052X	01300, 26802, 30601
1SC03A	31901	1U0061X	01400
1SC04A	31901	1U0062X	01300, 26802, 30601
1SC05A	31901	1ZF99AV	00702, 00703, 00800
1SC06A	31901		
1SD01A	31901		
1SD02A	31901		
1SD03A	31901		
1SD04A	31901		
1SD05A	31901		
1SD06A	31901		
1SE01A	31901		
1SE02A	31901		
1SE03A	31901		
1SE04A	31901		
1SE05A	31901		
1SE06A	31901		
1SF01A	31102		
1SF02A	31102		
1SF03A	31102		
1SF04A	31102		
1SF05A	31102		
1SF06A	31102		
1SG01D	26601		
1SG01D	31102		
1SG02D	31102		
2Z005JQ	00200		
2Z006JQ	00200		
2Z007JQ	00200		



FO-10. CFAR PROM Address Switch Multiplexer Logic Diagram

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NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUITCATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

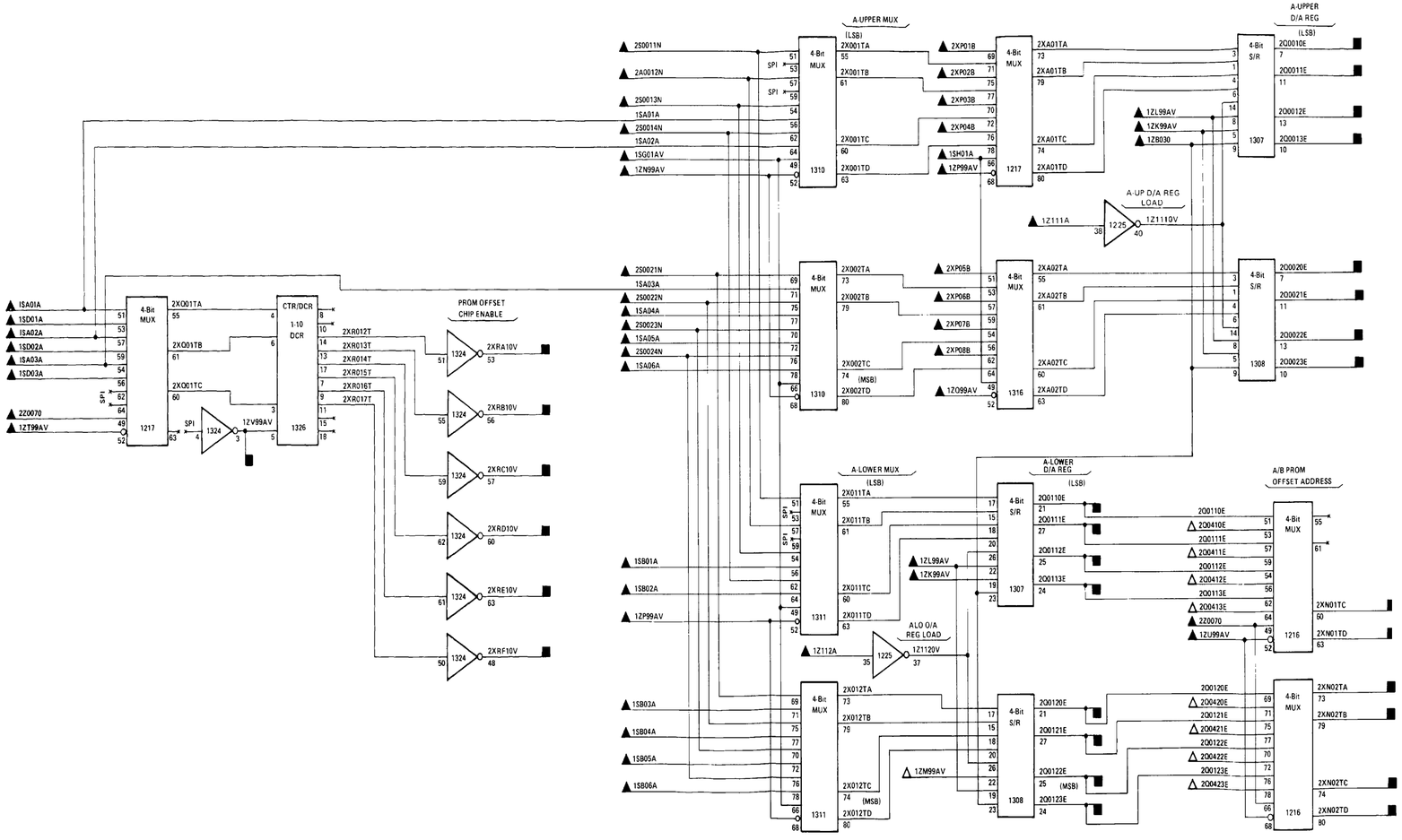
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT		OUTPUT	
	SOURCE		DESTINATION
SIGNAL	FO-SH	SIGNAL	FO-SH
1SA01A	31901	1ZV99AV	00400
1SA02A	31901	2Q0010E	00300, 01200, 26802, 29701
1SA03A	31901	2Q0011E	00300, 01200, 26802, 29701
1SA04A	31901	2Q0012E	00300, 01200, 26802, 29701
1SA05A	31901	2Q0013E	00300, 01200, 26802, 29701
1SA06A	31901	2Q0020E	00300, 01200, 26802, 29701
1SB01A	31901	2Q0021E	00300, 01200, 26802, 29701
1SB02A	31901	2Q0022E	00300, 01200, 26802, 29701
1SB03A	31901	2Q0023E	00300, 01200, 26802, 29701
1SB04A	31901	2Q0110E	00300, 01200, 26802, 29701
1SB05A	31901	2Q0111E	00300, 01200, 26802, 29701
1SB06A	31901	2Q0112E	00300, 01200, 26802, 29701
1SD01A	31901	2Q0113E	00300, 01200, 26802, 29701
1SD02A	31901	2Q0120E	00300, 01200, 26802, 29701
1SD03A	31901	2Q0121E	00300, 01200, 26802, 29701
1SG01AV	01000	2Q0122E	00300, 01200, 26802, 29701
1SH01A	01000	2Q0123E	00300, 01200, 26802, 29701
1ZB030	00100	2XN01TC	01300, 26802, 30602
1ZK99AV	00400	2XN01TD	01300, 26802, 30602
1ZL99AV	00400	2XN02TA	01300, 26802, 30602
1ZN99AV	00500	2XN02TB	01300, 26802, 30602
1ZP99AV	00500	2XN02TC	01300, 26802, 30502
1ZQ99AV	00400	2XN02TD	01300, 26802, 30502
1ZT99AV	00600	2XRA10V	01300, 26802, 30402
1ZU99AV	00600	2XRB10V	01300, 26802, 30402
1Z111A	00200	2XRC10V	01300, 26802, 30402
1Z112A	00200	2XRD10V	01300, 26802, 30402
2S0011N	01200	2XRE10V	01300, 26802, 30402
2S0012N	01200	2XRF10V	01300, 26802, 32102
2S0013N	01200		
2S0014N	01200		
2S0021N	01200		
2S0022N	01200		
2S0023N	01200		
2S0024N	01200		
2XP01B	01300		
2XP02B	01300		
2XP03B	01300		
2XP04B	01300		
2XP05B	01300		
2XP06B	01300		
2XP07B	01300		
2XP08B	01300		
2Z0070	01200		



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUITCATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

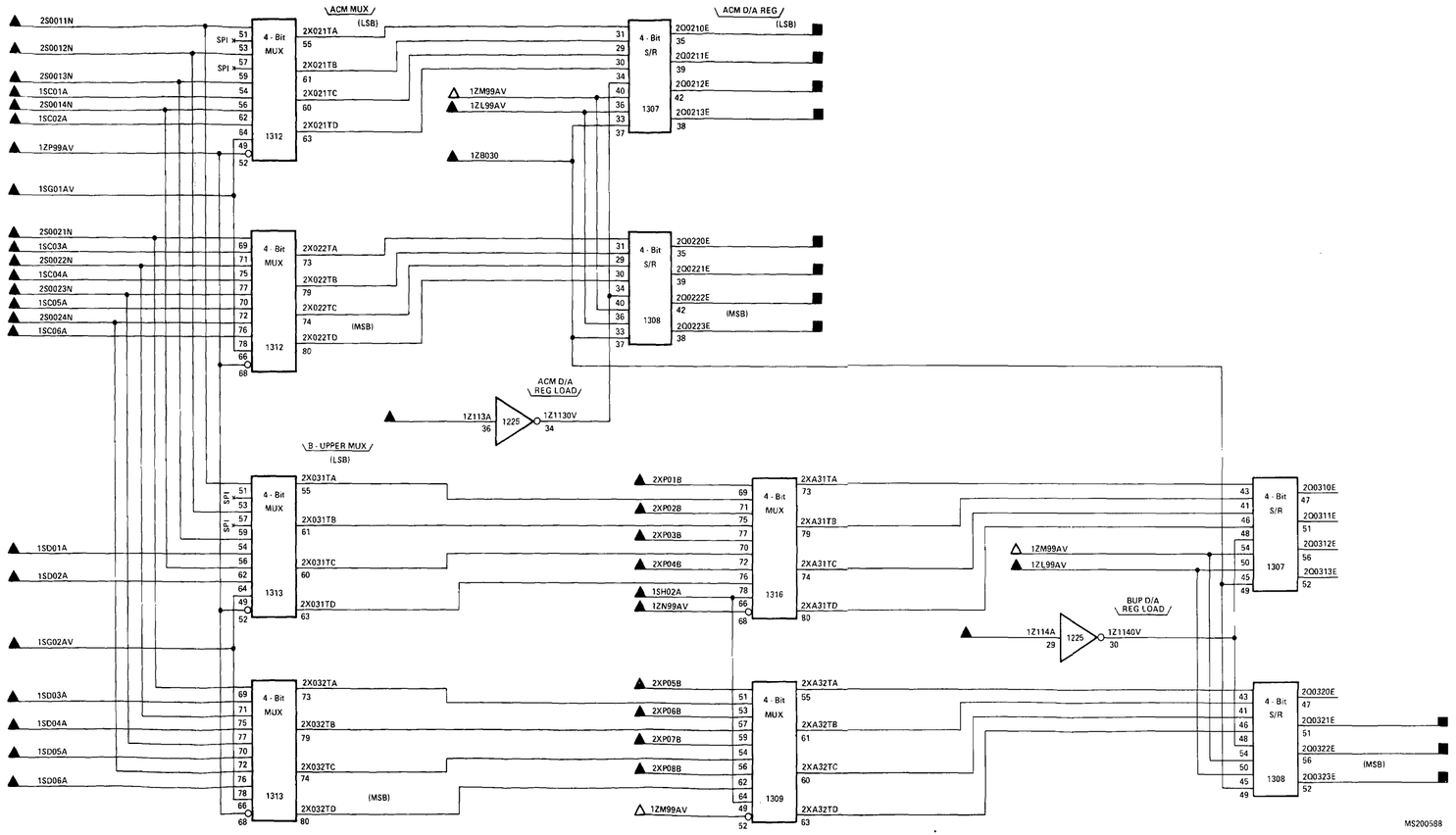
    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

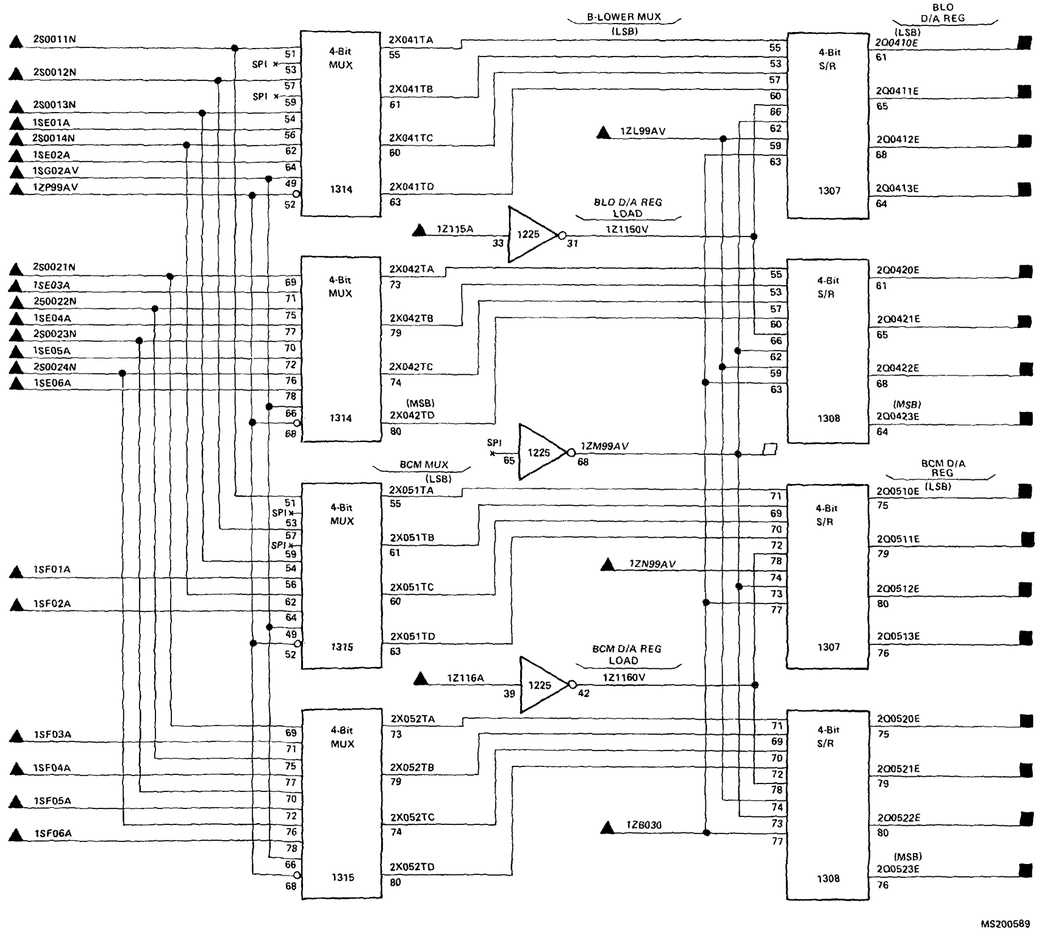
FO-11 CFAR Quantizer D/A Registers Logic Diagram (Sheet 1 of 3)

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
1SC01A	31901	2Q0210E	01200, 03800, 26802, 29701
1SC02A	31901	2Q0211E	01200, 03800, 26802, 29701
1SC03A	31901	2Q0212E	01200, 03800, 26802, 29701
1SC04A	31901	2Q0213E	01200, 03800, 26802, 29701
1SC05A	31901	2Q0220E	01200, 03800, 26802, 29701
1SC06A	31901	2Q0221E	01200, 03800, 26802, 29701
1SD01A	31901	2Q0222E	01200, 03800, 26802, 29701
1SD02A	31901	2Q0223E	01200, 03800, 26802, 29702
1SD03A	31901	2Q0310E	00300, 01200, 26802, 29702
1SD04A	31901	2Q0311E	00300, 01200, 26802, 29702
1SD05A	31901	2Q0312E	00300, 01200, 26802, 29702
1SD06A	31901	2Q0313E	00300, 01200, 26802, 29702
1SG01AV	01000	2Q0320E	00300, 01200, 26802, 29702
1SG02AV	01000	2Q0321E	00300, 01200, 26802, 29702
1SH02A	01000	2Q0322E	00300, 01200, 26802, 29702
1ZB030	00100	2Q0323E	00300, 01200, 26802, 29702
1ZL99AV	00400		
1ZN99AV	00500		
1ZP99AV	00500		
1Z113A	00200		
1Z114A	00200		
2S0011N	01200		
2S0012N	01200		
2S0013N	01200		
2S0014N	01200		
2S0021N	01200		
2S0022N	01200		
2S0023N	01200		
2S0024N	01200		
2XP01B	01300		
2XP02B	01300		
2XP03B	01300		
2XP04B	01300		
2XP05B	01300		
2XP06B	01300		
2XP07B	01300		
2XP08B	01300		



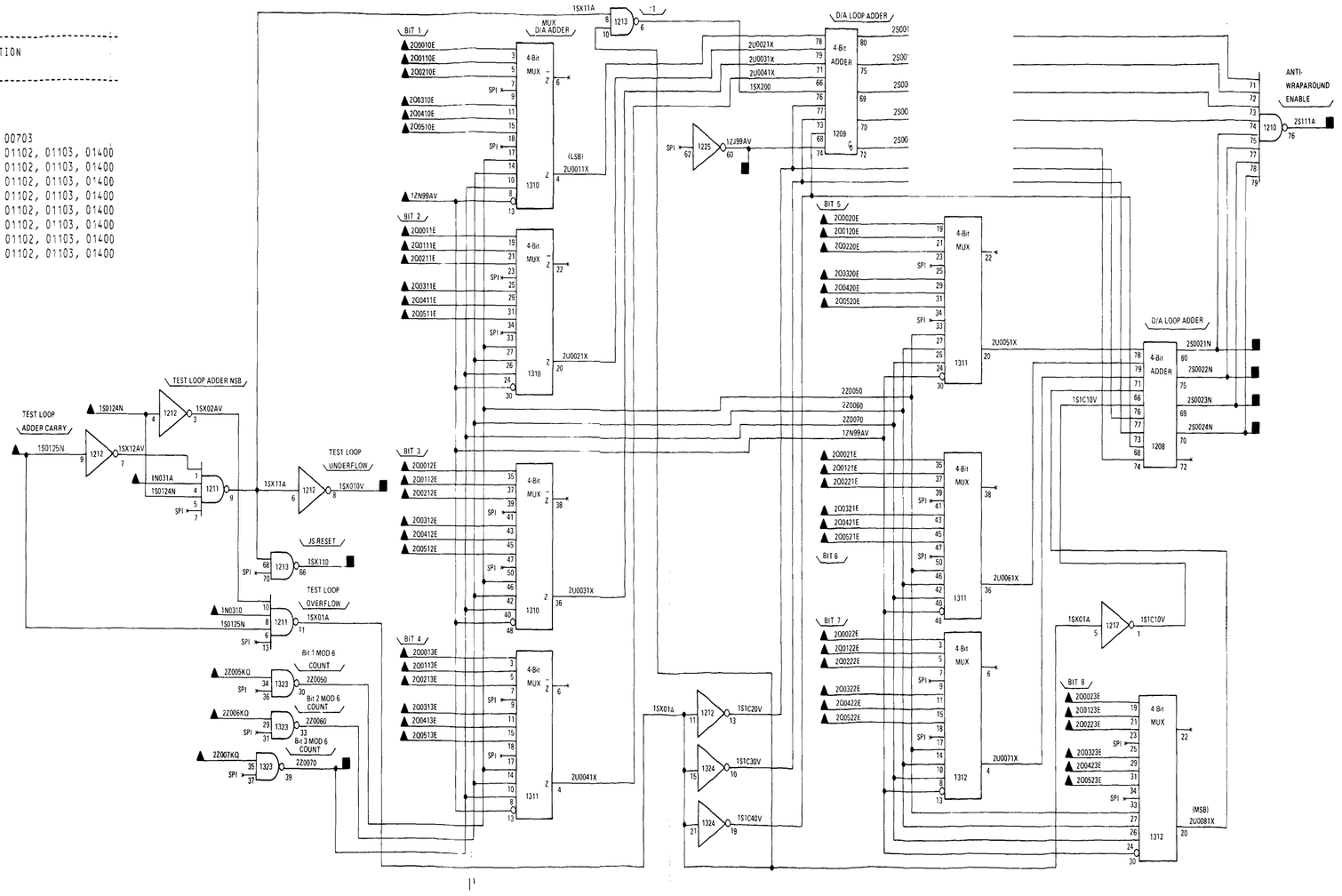
FO-11. CFAR Quantizer D/A Registers Logic Diagram (Sheet 2 of 3)

INPUT		OUTPUT	
	SOURCE		DESTINATION
SIGNAL	FO-SH	SIGNAL	FO-SH
1SE01A	31901	2Q0410E	00300, 01200, 26802, 29702
1SE02A	31901	2Q0411E	00300, 01200, 26802, 29702
1SE03A	31901	2Q0412E	00300, 01200, 26802, 29702
1SE04A	31901	2Q0413E	00300, 01200, 26802, 29702
1SE05A	31901	2Q0420E	00300, 01200, 26802, 29702
1SE06A	31901	2Q0421E	00300, 01200, 26802, 29702
1SF01A	31102	2Q0422E	00300, 01200, 26802, 29702
1SF02A	31102	2Q0423E	00300, 01200, 26802, 29702
1SF03A	31102	2Q0510E	01200, 03800, 26802, 30602
1SF04A	31102	2Q0511E	01200, 03800, 26802, 30602
1SF05A	31102	2Q0512E	01200, 03800, 26802, 30602
1SF06A	31102	2Q0513E	01200, 03800, 26802, 30602
1SG02AV	01000	2Q0520E	01200, 03800, 26802, 30602
1ZB030	00100	2Q0521E	01200, 03800, 26802, 30602
1ZL99AV	00400	2Q0522E	01200, 03800, 26802, 30602
1ZN99AV	00500	2Q0523E	01200, 03800, 26802, 30602
1ZP99AV	00500		
1Z115A	00200		
1Z116A	00200		
2S0011N	01200		
2S0012N	01200		
2S0013N	01200		
2S0014N	01200		
2S0021N	01200		
2S0022N	01200		
2S0023N	01200		
2S0024N	01200		



FO-11. CFAR Quantizer D/A Registers Logic Diagram (Sheet 3 of 3)

INPUT SIGNAL	INPUT SOURCE FO-SH	INPUT SIGNAL	INPUT SOURCE FO-SH	INPUT SIGNAL	OUTPUT SOURCE FO-SH	OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
1N031A	00400	2Q0220E	01102	2Q0513E	01103	1SX010V	01400
1N031A	30602	2Q0220E	29701	2Q0513E	30602	1SX110	01400
1N0310	00400	2Q0221E	01102	2Q0520E	01103	1ZJ99AV	00702, 00703
1S0124N	00800	2Q0221E	29701	2Q0520E	30602	2S0011N	01101, 01102, 01103, 01400
1S0125N	00800	2Q0222E	01102	2Q0521E	01103	2S0012N	01101, 01102, 01103, 01400
1ZN99AV	00500	2Q0222E	29701	2Q0521E	30602	2S0013N	01101, 01102, 01103, 01400
2Q0010E	01101	2Q0223E	01102	2Q0522E	01103	2S0014N	01101, 01102, 01103, 01400
2Q0010E	29701	2Q0223E	29702	2Q0522E	30602	2S0021N	01101, 01102, 01103, 01400
2Q0011E	01101	2Q0310E	01102	2Q0523E	01103	2S0022N	01101, 01102, 01103, 01400
2Q0011E	29701	2Q0310E	29702	2Q0523E	30602	2S0023N	01101, 01102, 01103, 01400
2Q0012E	01101	2Q0311E	01102	2Z005KQ	00200	2S0024N	01101, 01102, 01103, 01400
2Q0012E	29701	2Q0311E	29702	2Z005KQ	30502	2S111A	00200
2Q0013E	01101	2Q0312E	01102	2Z006KQ	00200	2Z0070	01101
2Q0013E	29701	2Q0312E	29702	2Z006KQ	30502		
2Q0020E	01101	2Q0313E	01102	2Z007KQ	00200		
2Q0020E	29701	2Q0313E	29702	2Z007KQ	30502		
2Q0021E	01101	2Q0320E	01102				
2Q0021E	29701	2Q0320E	29702				
2Q0022E	01101	2Q0321E	01102				
2Q0022E	29701	2Q0321E	29702				
2Q0023E	01101	2Q0322E	01102				
2Q0023E	29701	2Q0322E	29702				
2Q0110E	01101	2Q0323E	01102				
2Q0110E	29701	2Q0323E	29702				
2Q0111E	01101	2Q0410E	01103				
2Q0111E	29701	2Q0410E	29702				
2Q0112E	01101	2Q0411E	01103				
2Q0112E	29701	2Q0411E	29702				
2Q0113E	01101	2Q0412E	01103				
2Q0113E	29701	2Q0412E	29702				
2Q0120E	01101	2Q0413E	01103				
2Q0120E	29701	2Q0413E	29702				
2Q0121E	01101	2Q0420E	01103				
2Q0121E	29701	2Q0420E	29702				
2Q0122E	01101	2Q0421E	01103				
2Q0122E	29701	2Q0421E	29702				
2Q0123E	01101	2Q0422E	01103				
2Q0123E	29701	2Q0422E	29702				
2Q0210E	01102	2Q0423E	01103				
2Q0210E	29701	2Q0423E	29702				
2Q0211E	01102	2Q0510E	01103				
2Q0211E	29701	2Q0510E	30602				
2Q0212E	01102	2Q0511E	01103				
2Q0212E	29701	2Q0511E	30602				
2Q0213E	01102	2Q0512E	01103				
2Q0213E	29701	2Q0512E	30602				

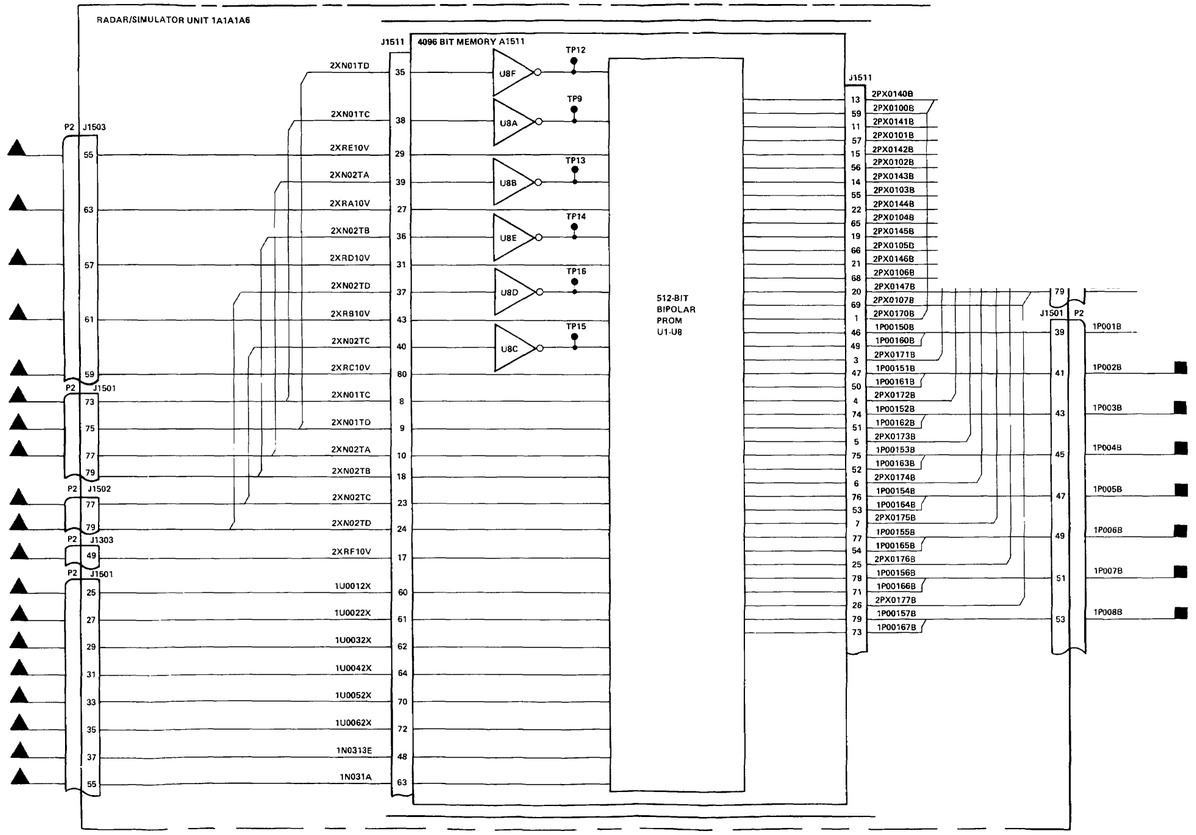


NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN . FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INPUT FROM ANOTHER FIGURE
   - △ INPUT FROM SAME FIGURE
   - ■ OUTPUT TO ANOTHER FIGURE
   - □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   - ◪ OUTPUT TO SAME FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUITCATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.
    - B. REFER TO TABLE 5037 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FORMTS TESTABLE CARDS.
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

FO-12. CFAR D/A Loop Adder Logic Diagram

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
1N031A	00400	2XP01B	01101, 01102, 01400, 26802
1N031A	30602	2XP02B	01101, 01102, 01400, 26802
1N0313E	00400	2XP03B	01101, 01102, 01400, 26802
1N0313E	30601	2XP04B	01101, 01102, 01400, 26802
1U0012X	01000	2XP05B	01101, 01102, 01400, 26802
1U0012X	30601	2XP06B	01101, 01102, 01400, 26802
1U0022X	01000	2XP07B	01101, 01102, 01400, 26802
1U0022X	30601	2XP08B	01101, 01102, 01400, 26802
1U0032X	01000		
1U0032X	30601		
1U0042X	01000		
1U0042X	30601		
1U0052X	01000		
1U0052X	30601		
1U0062X	01000		
1U0062X	30601		
2XN01TC	01101		
2XN01TC	30602		
2XN01TD	01101		
2XN01TD	30602		
2XN02TA	01101		
2XN02TA	30602		
2XN02TB	01101		
2XN02TB	30602		
2XN02TC	01101		
2XN02TC	30502		
2XN02TD	01101		
2XN02TD	30502		
2XRA10V	01101		
2XRA10V	30402		
2XRB10V	01101		
2XRB10V	30402		
2XRC10V	01101		
2XRC10V	30402		
2XRD10V	01101		
2XRD10V	30402		
2XRE10V	01101		
2XRE10V	30402		
2XRF10V	01101		
2XRF10V	28902		



FO-13. CFAR PROM Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INPUT FROM ANOTHER FIGURE
   - △ INPUT FROM SAME FIGURE
   - ■ OUTPUT TO ANOTHER FIGURE
   - □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   - ◪ OUTPUT TO SAME FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUITCATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

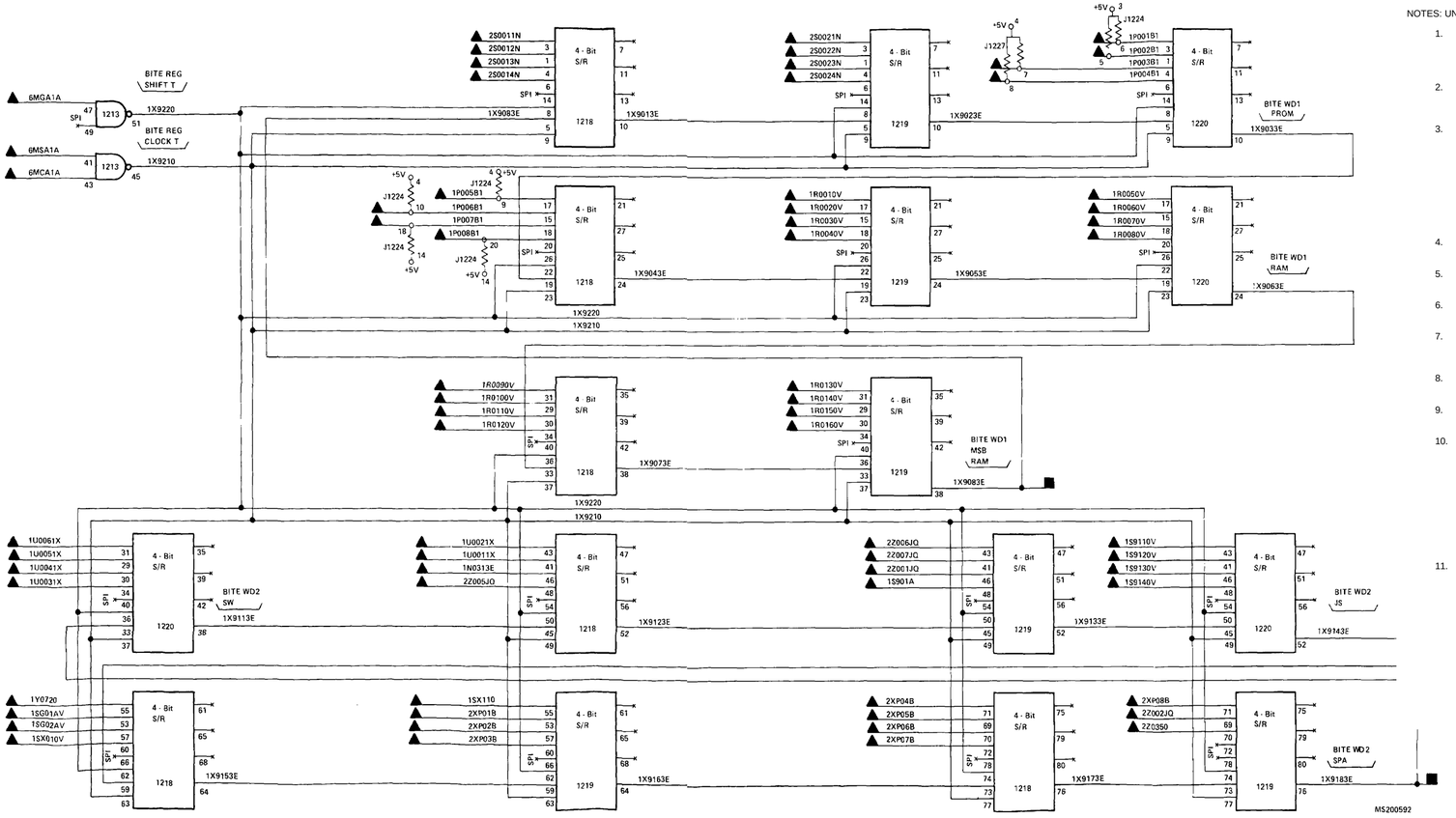
    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

7. REFER T GRAMS F
8. REFER T 20-3 FOR

INPUT SIGNAL	SOURCE FO-SH
1N0313E	00400
1N0313E	30601
1P001B1	26802
1P002B1	26802
1P003B1	26802
1P004B1	26802
1P005B1	26802
1P006B1	26802
1P007B1	26802
1P008B1	26802
1R0010V	00701
1R0020V	00701
1R0030V	00701
1R0040V	00701
1R0050V	00703
1R0060V	00703
1R0070V	00703
1R0080V	00703
1R0090V	00702
1R0100V	00702
1R0110V	00702
1R0120V	00702
1R0130V	00702
1R0140V	00702
1R0150V	00702
1R0160V	00702
1SG01AV	01000
1SG02AV	01000
1SX010V	01200
1SX110	01200
1S901A	31901
1S9110V	00500
1S9120V	00500
1S9130V	00500
1S9140V	00500
1U0011X	01000
1U0021X	01000
1U0031X	01000
1U0041X	01000
1U0051X	01000
1U0061X	01000
1Y0720	00500
2S0011N	01200
2S0012N	01200
2S0013N	01200
2S0014N	01200

INPUT SIGNAL	SOURCE FO-SH
2S0021N	01200
2S0022N	01200
2S0023N	01200
2S0024N	01200
2XP01B	01300
2XP02B	01300
2XP03B	01300
2XP04B	01300
2XP05B	01300
2XP06B	01300
2XP07B	01300
2XP08B	01300
2Z001JQ	00100
2Z002JQ	00100
2Z005JQ	00200
2Z006JQ	00200
2Z007JQ	00200
2Z0350	00600
6MCA1A	05600
6MCA1A	26803
6MCA1A	27901
6MGA1A	05600
6MGA1A	26803
6MGA1A	27201
6MSA1A	05600
6MSA1A	26803
6MSA1A	27201

OUTPUT SIGNAL	DESTINATION FO-SH
1X9083E	05400
1X9183E	05400



FO-14. CFAR BITE Register Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO THE SAME FIGURE
   ◪ OUTPUT TO THE SAME AND ANOTHER FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

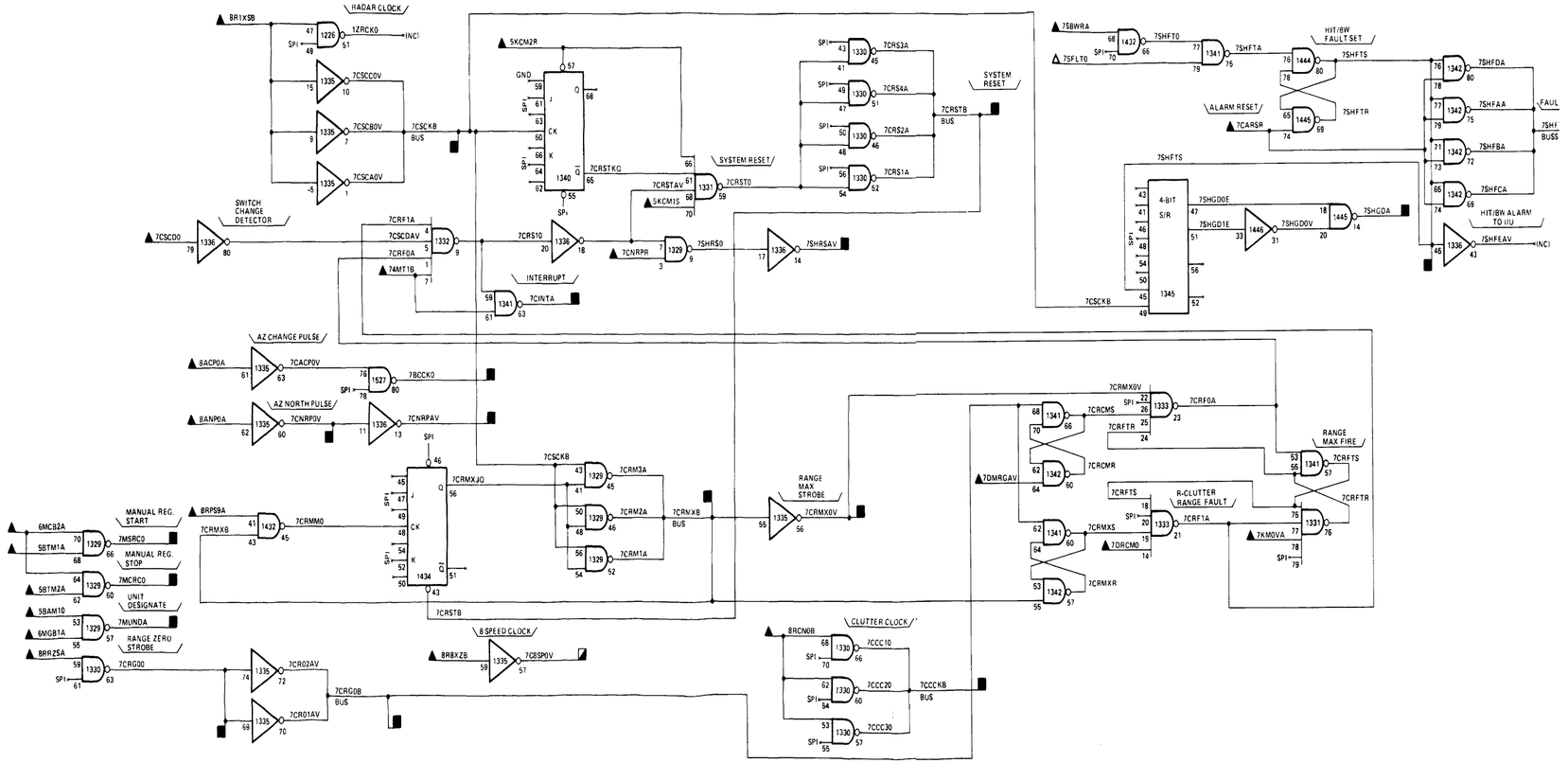
    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT SIGNAL	SOURCE FO-SH
5BAM10	13302
5BAM1D	28201
5BTM1A	13302
5BTM1A	26802
5BTM1A	28201
5BTM2A	13302
5BTM2A	26802
5BTM2A	28201
5KCM1S	13400
5KCM2R	28201
6MCB2A	05600
6MGB1A	05600
6MGB1A	27201
7CARSR	01900
7CNRPR	01800
7CSCD0	05003
7DMRGAV	02500
7DRCM0	02500
7KM0VA	02100
7SBWRA	03400
74MT1B	05101
74MT1B	28202
8ACP0A	17200
8ACP0A	28401
8ANP0A	17200
8ANP0A	28401
8RCN0B	15001
8RCN0B	27201
8RPS9A	14400
8RPS9A	26803
8RPS9A	28202
8RR2SA	14400
8RR2SA	28202
8R1XSB	00300
8R1XSB	14900
8R1XSB	26802
8R1XSB	26803
8R1XSB	27501
8R8XZB	14300
8R8XZB	27201

OUTPUT SIGNAL	DESTINATION FO-SH
7BCCK0	03300
7CCCKB	01700, 01800, 02000, 03900, 04200
7C1NTA	13400, 26802
7CNRPAV	01800
7CNRP0V	03500
7CRG0B	01600, 01700, 02100, 02400, 04200
7CRG00	01800, 02200, 02400, 02500, 03400
7CRMXB	01600, 01700, 01800, 02200, 04200, 04300, 04400, 04500, 04700
7CRMX0V	01800, 02500
7CRSTB	01700, 01800, 01900, 02100, 02200, 02300, 02400, 04200, 04300
7CSCKB	01600, 01700, 04300, 05101
7C8SP0V	03900, 04200, 04300, 04400, 04500, 04900, 05101
7MCRC0	02300
7MSRC0	02300
7MUNDA	02300
7SHFTB	26504, 26802, 31902
7SHFTS	05002
7SHGDA	26802
7SHRSAV	03400



FO-15. ACM Interface BUFFERS Logic Diagram (Sheet 1 of 2)

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INPUT FROM ANOTHER FIGURE
   - △ INPUT FROM SAME FIGURE
   - ■ OUTPUT TO ANOTHER FIGURE
   - □ OUTPUT TO THE SAME FIGURE
   - ◪ OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.
    - B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

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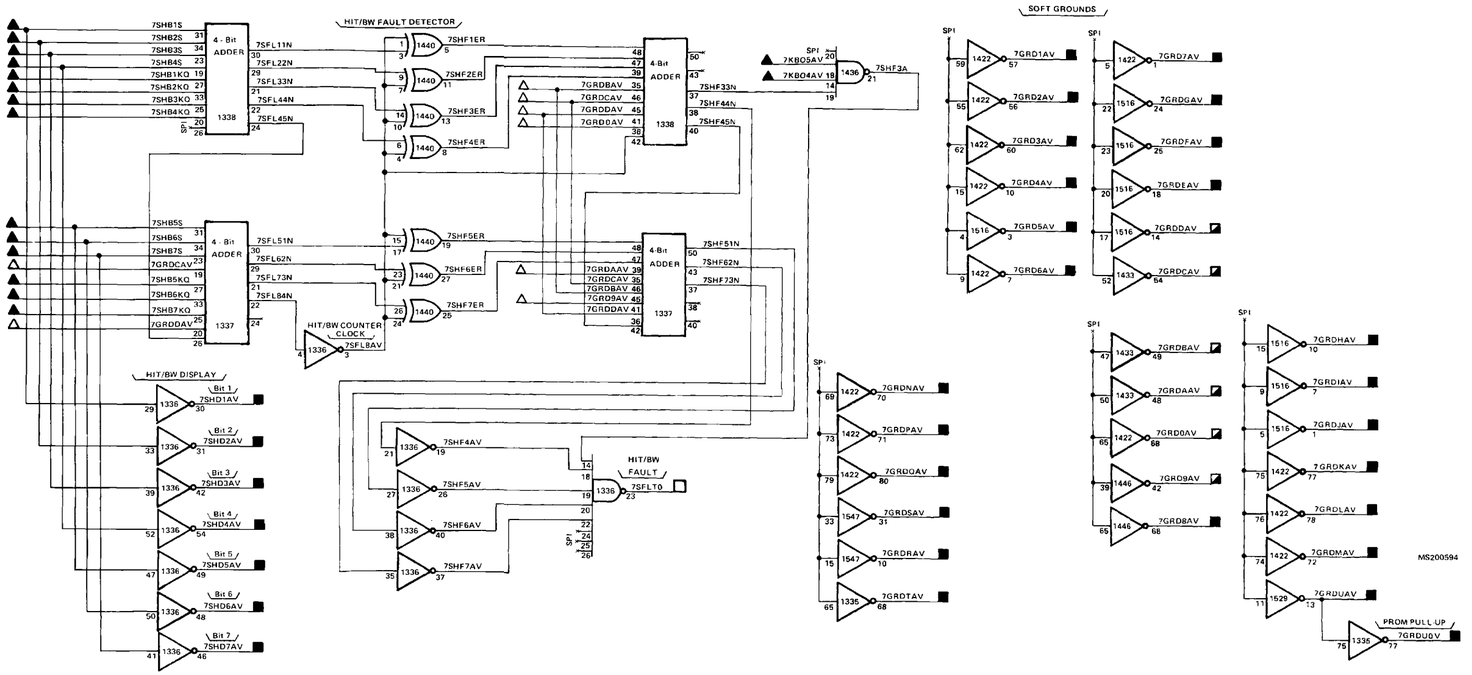
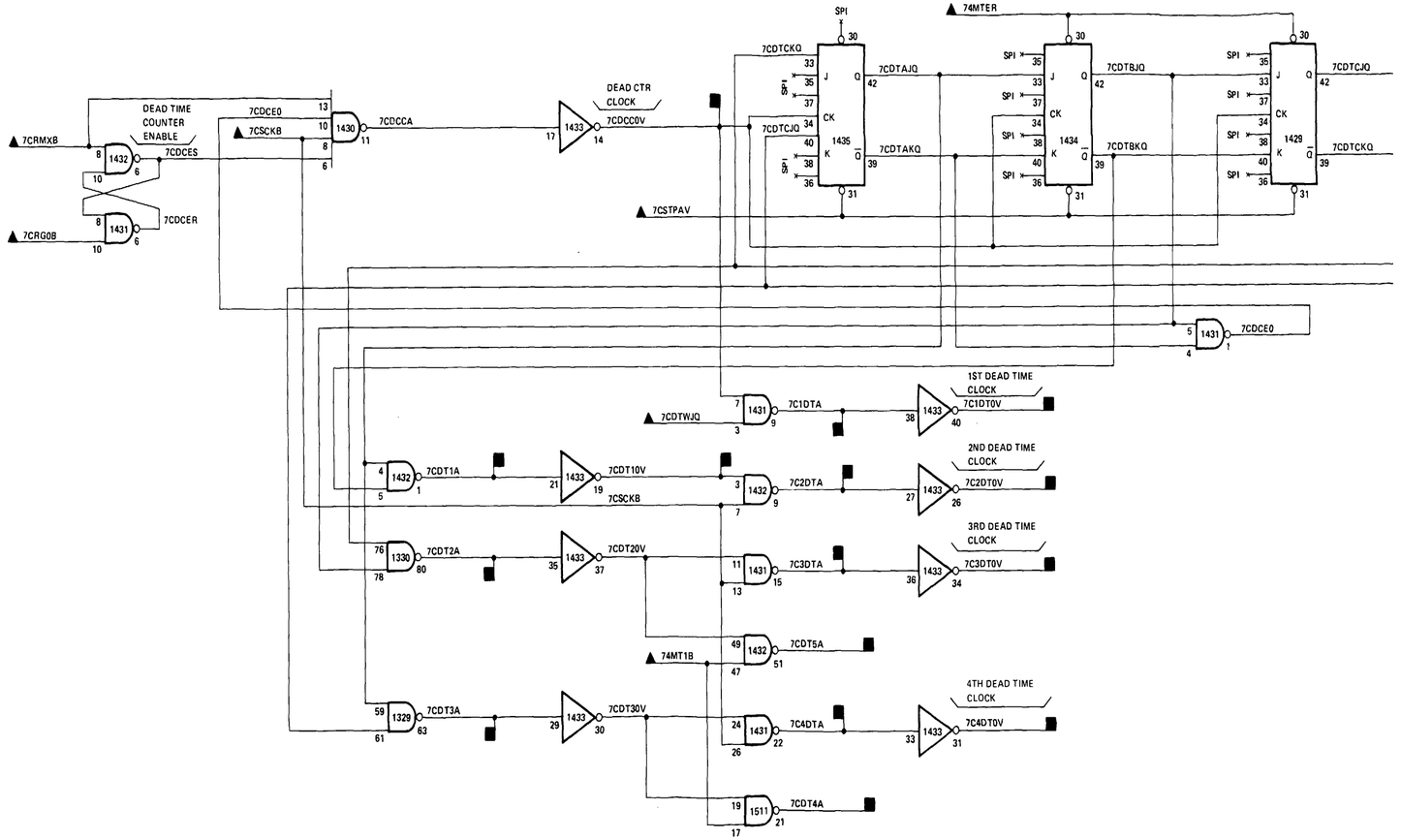
INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
7KB04AV	02000	7GRDAAV	03400, 03900
7KB05AV	02000	7GRDBAV	01800, 03900
7SHB1KQ	03400	7GRDCAV	03900, 04200
7SHB1S	03400	7GRDDAV	02600, 03000
7SHB2KQ	03400	7GRDEAV	02600
7SHB2S	03400	7GRDFAV	02600
7SHB3KQ	03400	7GRDGAV	02600
7SHB3S	03400	7GRDHAV	03000
7SHB4KQ	03400	7GRDIAV	02700
7SHB4S	03400	7GRDJAV	02700
7SHB5KQ	03400	7GRDKAV	03000
7SHB5S	03400	7GRDLAV	02000, 03000
7SHB6KQ	03400	7GRDMAV	03000
7SHB6S	03400	7GRDNAV	03000
7SHB7KQ	03400	7GRDPAV	03000
7SHB7S	03400	7GRDQAV	02100, 03000
		7GRDRAV	02500
		7GRDSAV	02400
		7GRDTAV	02400, 02900
		7GRDUAV	03600, 26802, 30401
		7GRDU0V	03600, 26802, 30401
		7GRD0AV	03400, 03900
		7GRD1AV	02000, 02400, 03400, 03500
		7GRD2AV	02000, 02200, 02500, 03400
		7GRD3AV	02000, 03300
		7GRD4AV	03300, 04100, 04900
		7GRD5AV	02100, 02200, 03300, 03900, 05102
		7GRD6AV	02000, 02200, 03300
		7GRD7AV	02000, 02200, 05101
		7GRD8AV	02100, 02200, 03400
		7GRD9AV	02500, 03400
		7SHD1AV	05002, 26802
		7SHD2AV	05002, 26802
		7SHD3AV	05002, 26802
		7SHD4AV	05002, 26802
		7SHD5AV	05002, 26802
		7SHD6AV	05002, 26802
		7SHD7AV	05002, 26802



FO-15. ACM Interface BUFFERS Logic Diagram (Sheet 2 of 2)

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
7CDTWJQ	01800	7CDCC0V	01800
7CRGOB	01501	7CDT1A	01700, 01800, 04300
7CRMXB	01501	7CDT10V	01800
7CSCKB	01501	7CDT2A	01700, 02700, 03000, 04500
7CSTPAV	01700	7CDT3A	01700, 02700, 02800, 04400
74MTER	05101	7CDT4A	01800
74MT1B	05101	7C1DTA	01800
74MT1B	28202	7C1DT0V	02000, 02900, 03400
		7C2DTA	03000, 04300
		7C2DT0V	01800, 02200, 02900, 03400, 04300
		7C3DTA	04500
		7C3DT0V	02100, 02900, 03400, 04500
		7C4DTA	04400, 04700
		7C4DT0V	04400



FO-16. ACM Dead Time Counter Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO THE SAME FIGURE
   ◪ OUTPUT TO THE SAMEAND ANOTHER FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

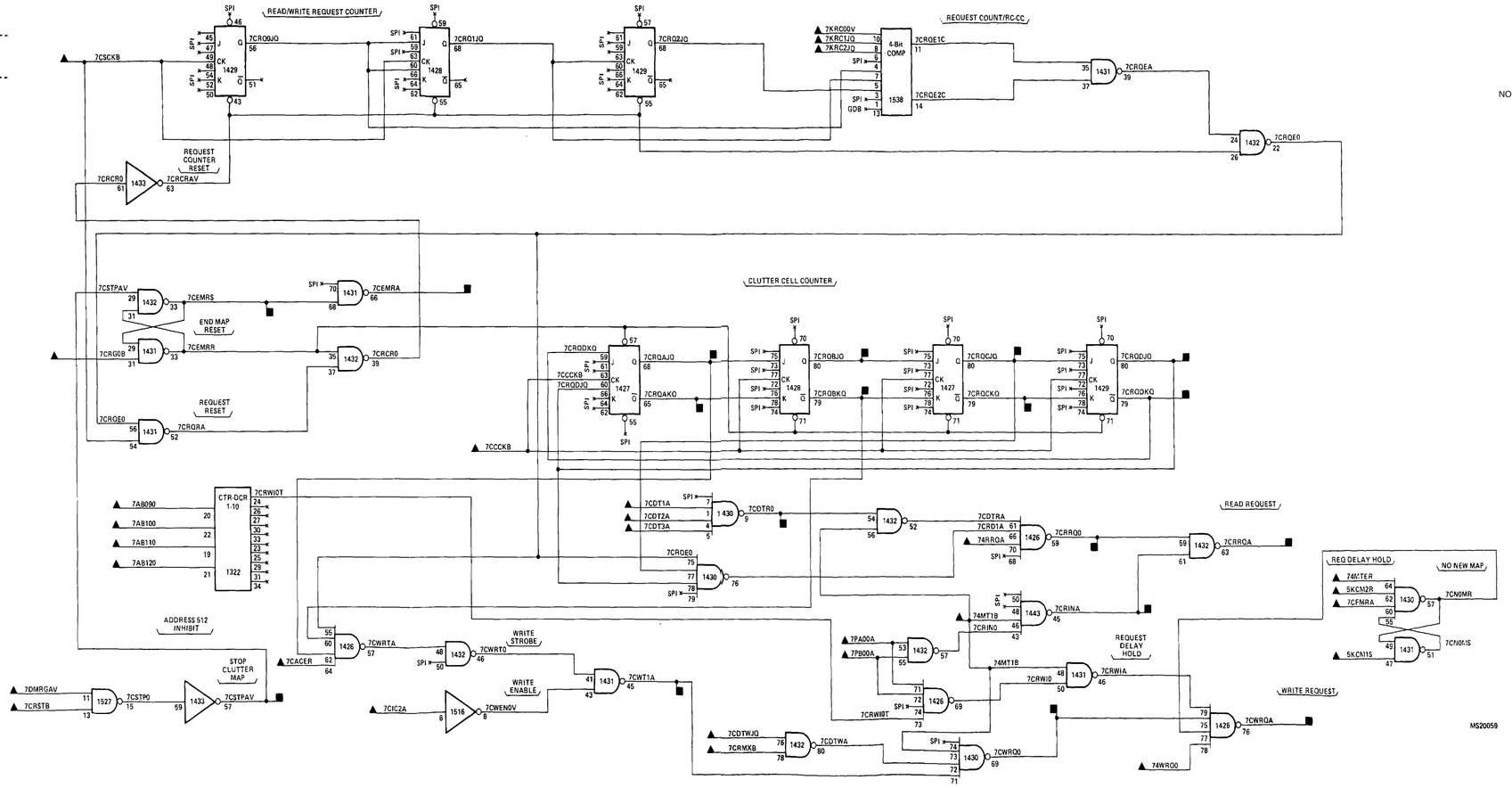
    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT SIGNAL	INPUT SOURCE FO-SH
5KCM1S	13400
5KCM2R	28201
7AB090	03100
7AB090	26802
7AB100	03100
7AB100	26802
7AB110	03100
7AB110	26802
7AB120	03100
7AB120	26802
7CACER	01800
7CCCKB	01501
7CDTWJQ	01800
7CDT1A	01600
7CDT2A	01600
7CDT3A	01600
7CFMRA	01900
7CIC2A	01800
7CRGOB	01501
7CRMXB	01501
7CRSTB	01501
7CSCKB	01501
7DMRGAV	02500
7KRC00V	02100
7KRC00V	26803
7KRC1JQ	02100
7KRC2JQ	02100
7PA00A	32102
7PB00A	32102
74MTER	05101
74MT1B	05101
74MT1B	28202
74RRQA	05101
74WRQO	05101

OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
7CDTRO	01800
7CEMRA	02000, 02500, 04900
7CEMRS	05002
7CRINA	04100
7CRQAJQ	01800
7CRQAKQ	01800
7CRQBJQ	01800
7CRQBKQ	01800
7CRQCJQ	01800
7CRQCKQ	01800, 02500
7CRQDJQ	01800
7CRQDKQ	02500
7CRRQA	26802
7CRRQO	05002
7CSTPAV	01600, 01800, 02600
7CWRQA	06001, 26802
7CWRQO	05002
7CWT1A	04700



FO-17. ACM Read/Write Request and Clutter Cell Counter Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN; FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEM-

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO THE SAME FIGURE
   ◪ OUTPUT TO THE SAME AND ANOTHER FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

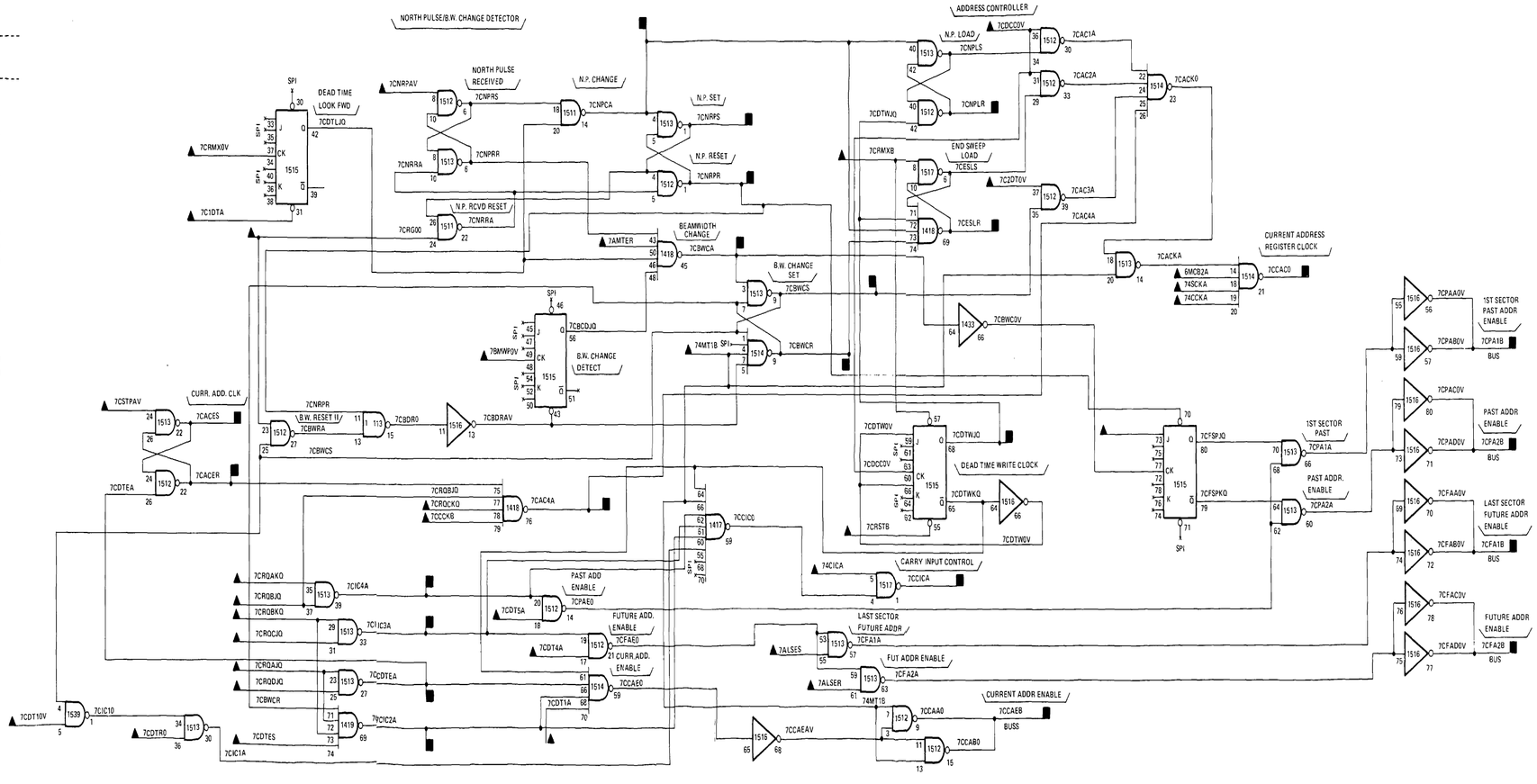
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT		OUTPUT	
	SOURCE		DESTINATION
SIGNAL	FO-SH	SIGNAL	FO-SH
6MCB2A	05600	7CACER	01700, 03000
7ALSER	02900	7CACES	03000
7ALSER	26802	7CAC4A	02200, 03000
7ALSES	02900	7CBWCA	02600
7BMWP0V	03300	7CBWCR	02800
7CCCKB	01501	7CBWCS	03400, 05002
7CDCC0V	01600	7CCAC0	02600, 02800
7CDTES	02600	7CCAEB	03100
7CDTR0	01700	7CC1CA	02600
7CDT1A	01600	7CDTEA	02600, 04300
7CDT10V	01600	7CDTWJQ	01600, 01700
7CDT4A	01600	7CESLR	02600, 02800
7CDT5A	01600	7CFA1B	03100
7CNRPAV	01501	7CFA2B	03100
7CRG00	01501	7CIC2A	01700
7CRMXB	01501	7CIC3A	04400
7CRMX0V	01501	7CIC4A	04500
7CRQAJQ	01700	7CNPCA	01900
7CRQAKQ	01700	7CNPLR	02600, 02800
7CRQBJQ	01700	7CNRPR	01501, 02900
7CRQBKQ	01700	7CNRPS	03400
7CRQCJQ	01700	7CPA1B	03100
7CRQCKQ	01700	7CPA2B	03100
7CRQDJQ	01700		
7CRSTB	01501		
7CSTPAV	01700		
7C1DTA	01600		
7C20T0V	01600		
7GRDBAV	01502		
74CCKA	05101		
74CICA	05101		
74MTER	05101		
74MT1B	05101		
74MT1B	28202		
74SCKA	05101		



FO-18. ACM Address Controller Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   ▲ INPUT FROM THE SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO THE SAME FIGURE
   ◪ OUTPUT TO THE SAME AND ANOTHER FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

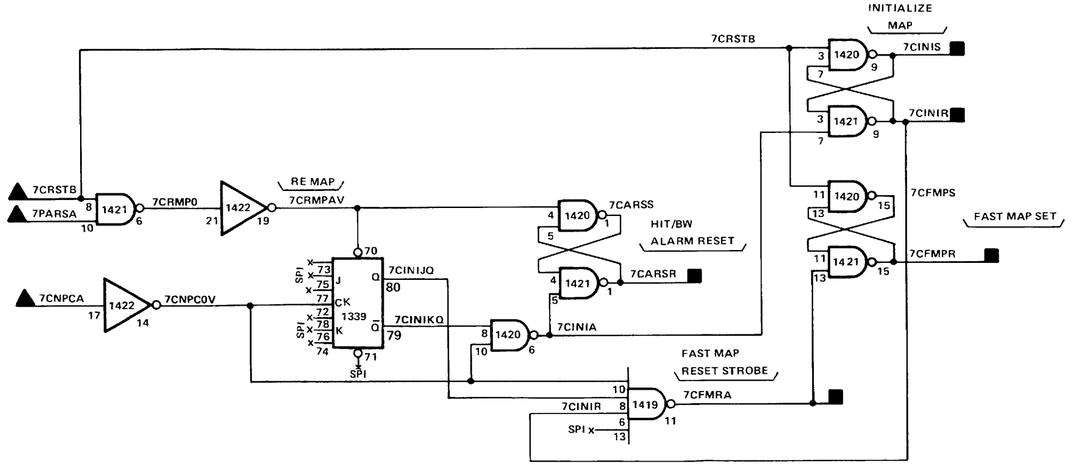
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
7CNPCA	01800	7CARSR	01501
7CRSTB	01501	7CFMPR	03500, 03900, 06500, 26802
7PARSA	26802	7CFMRA	01700
7PARSA	30001	7CINIR	04000, 04100
7PARSA	32101	7CINIS	03400



FO-19. Acm Initialize Map Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

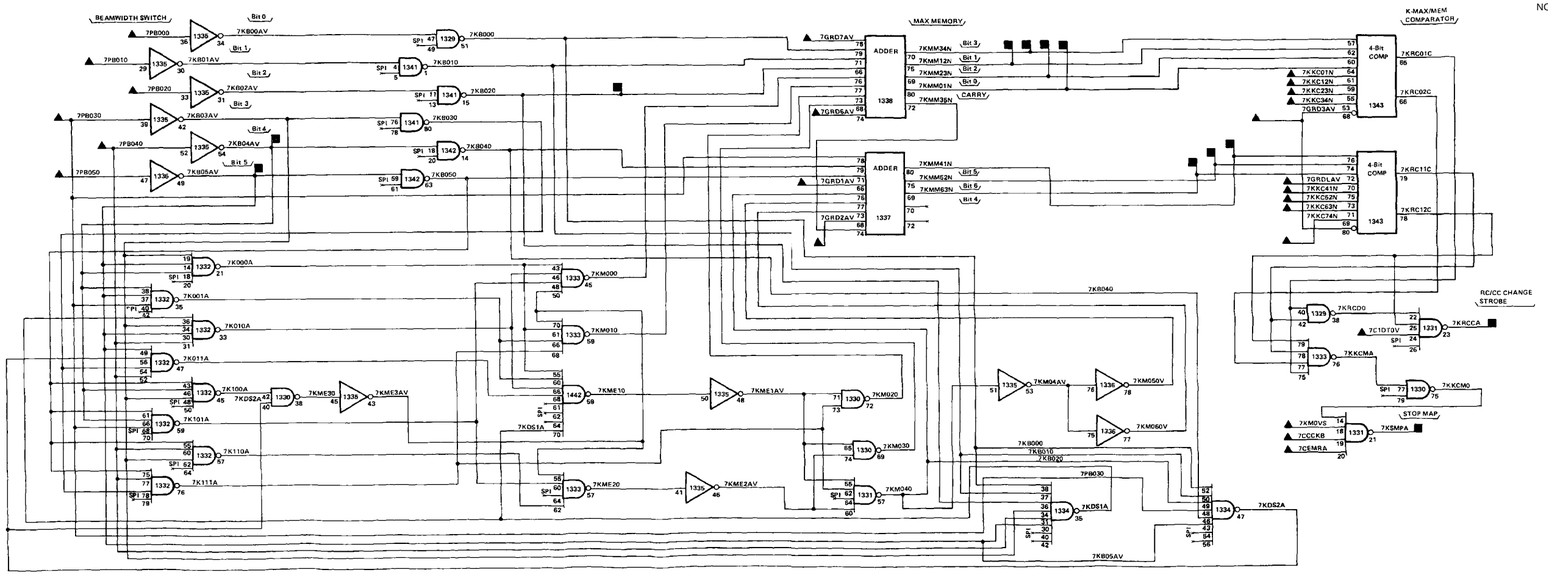
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
7CCCKB	01501	7KB020	03300
7CEMRA	01700	7KB04AV	01502
7C1D10V	01600	7KB05AV	01502
7GRD1AV	01502	7KMM01N	02700
7GRD2AV	01502	7KMM12N	02700
7GRD3AV	01502	7KMM23N	02700
7GRD6AV	01502	7KMM34N	02700
7GRD7AV	01502	7KMM41N	02700
7KKC01N	02200	7KMM52N	02700
7KKC12N	02200	7KMM63N	02700
7KKC23N	02200	7KRCCA	02100
7KKC34N	02200	7KSMPA	02100, 02500
7KKC41N	02200		
7KKC52N	02200		
7KKC63N	02200		
7KKC74N	02200		
7KMOVS	02100		
7PB000	32101		
7PB010	32101		
7PB020	32101		
7PB030	32101		
7PB040	26802		
7PB040	32000		
7PB040	32101		
7PB050	32000		
7PB050	32101		



FO-20. ACM Maximum K Calculator Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

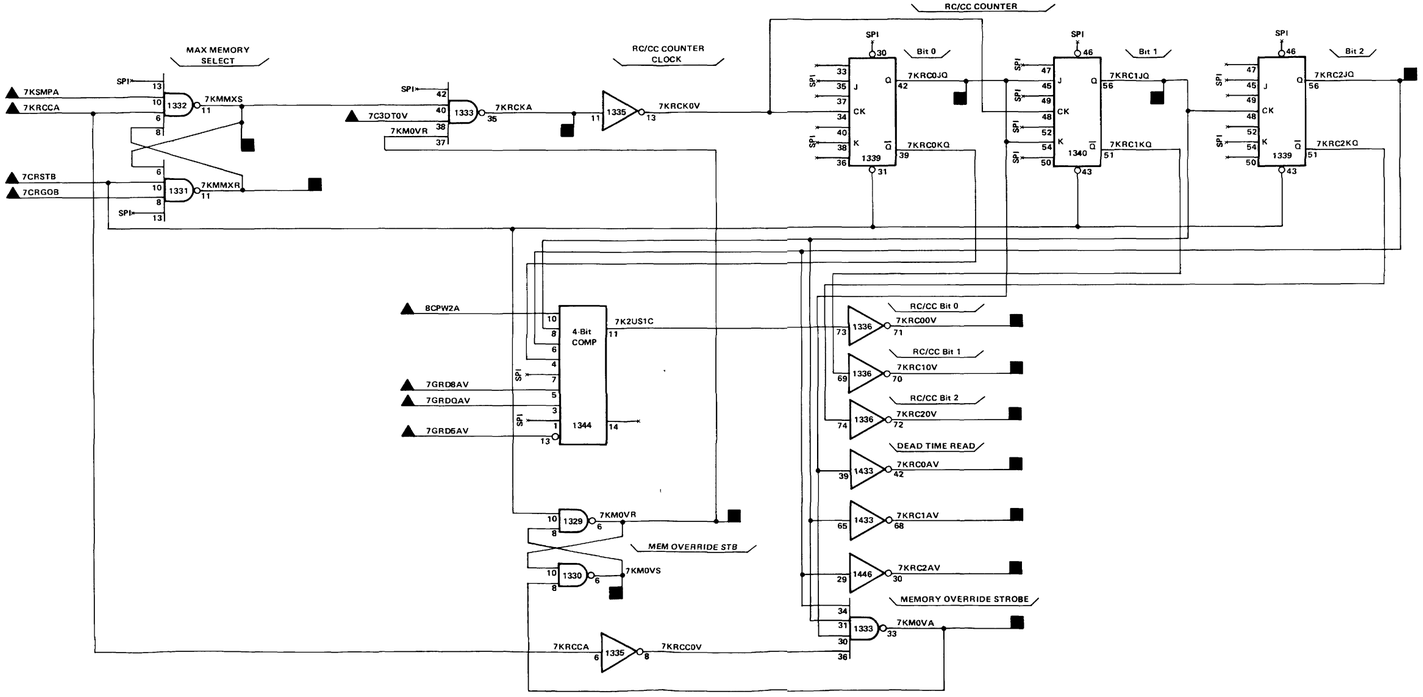
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
7CRGOB	01501	7KMMXR	02200
7CRSTB	01501	7KMMXS	02200
7C3DT0V	01600	7KM0VA	01501
7GRDQAV	01502	7KM0VR	02200
7GRD5AV	01502	7KM0VS	02000, 02700
7GRD8AV	01502	7KRCKA	02200
7KRCCA	02000	7KRCOAV	03600, 26802, 27202
7KSMPA	02000	7KRCOJQ	05002
8CPW2A	26802	7KRCOOV	01700, 15001, 26802
8CPW2A	26803	7KRC1AV	03600, 26802, 30401
8CPW2A	27101	7KRC1JQ	01700, 05002
8CPW2A	28901	7KRC1OV	26802
8CPW2A	32102	7KRC2AV	26802, 30401
		7KRC2JQ	01700, 05002
		7KRC2OV	03600, 26802



FO-21. ACM RC/CC Counter Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

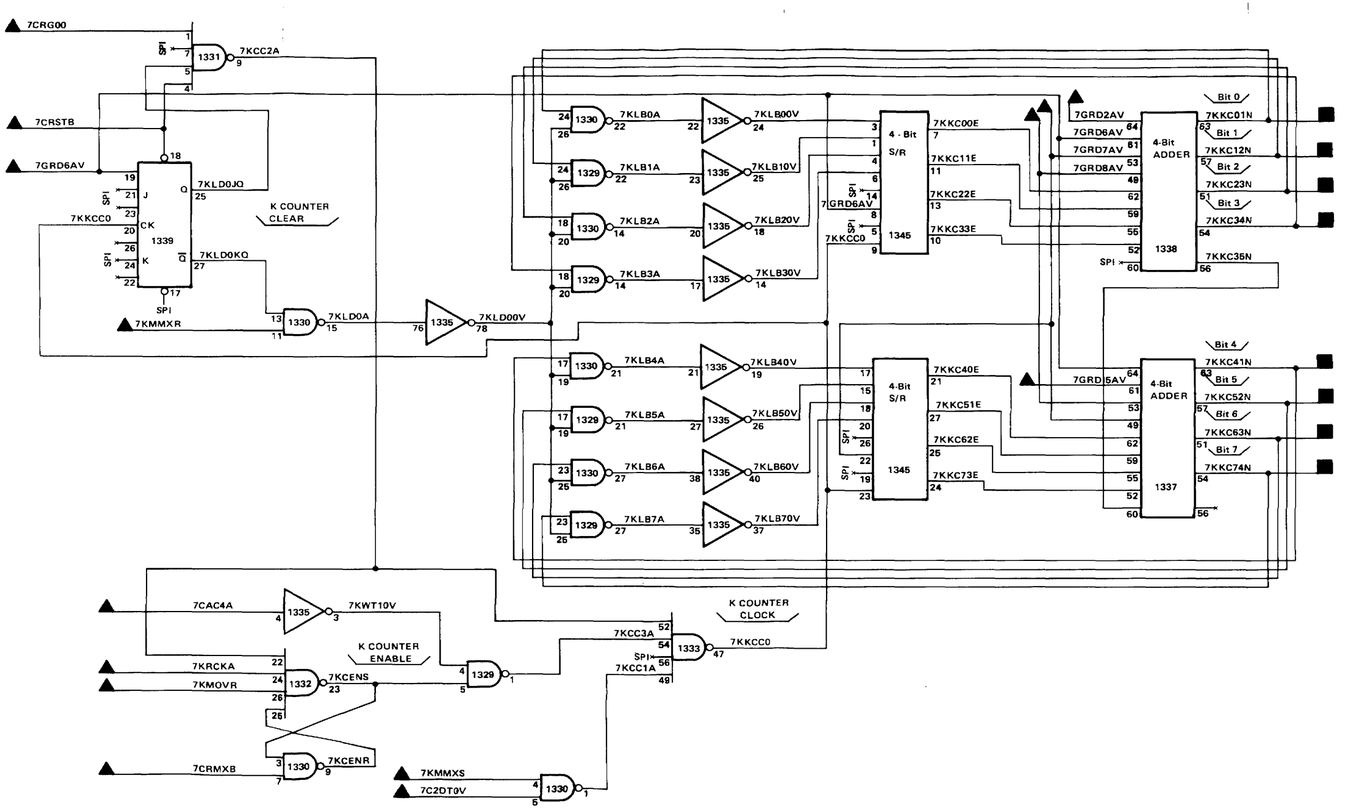
    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT	
SIGNAL	SOURCE FO-SH
7CAC4A	01800
7CRG00	01501
7CRMXB	01501
7CRSTB	01501
7C2DT0V	01600
7GRD2AV	01502
7GRD5AV	01502
7GRD6AV	01502
7GRD7AV	01502
7GRD8AV	01502
7KMMXR	02100
7KMMXS	02100
7KMOVR	02100
7KRCKA	02100

OUTPUT	
SIGNAL	DESTINATION FO-SH
7KKC01N	02000, 02700, 05002
7KKC12N	02000, 02700, 05002
7KKC23N	02000, 02700, 05002
7KKC34N	02000, 02700, 05002
7KKC41N	02000, 02700, 05002
7KKC52N	02000, 02700, 05002
7KKC63N	02000, 02700, 05002
7KKC74N	02000, 02700, 05002



FO-22. ACM K-Counter Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

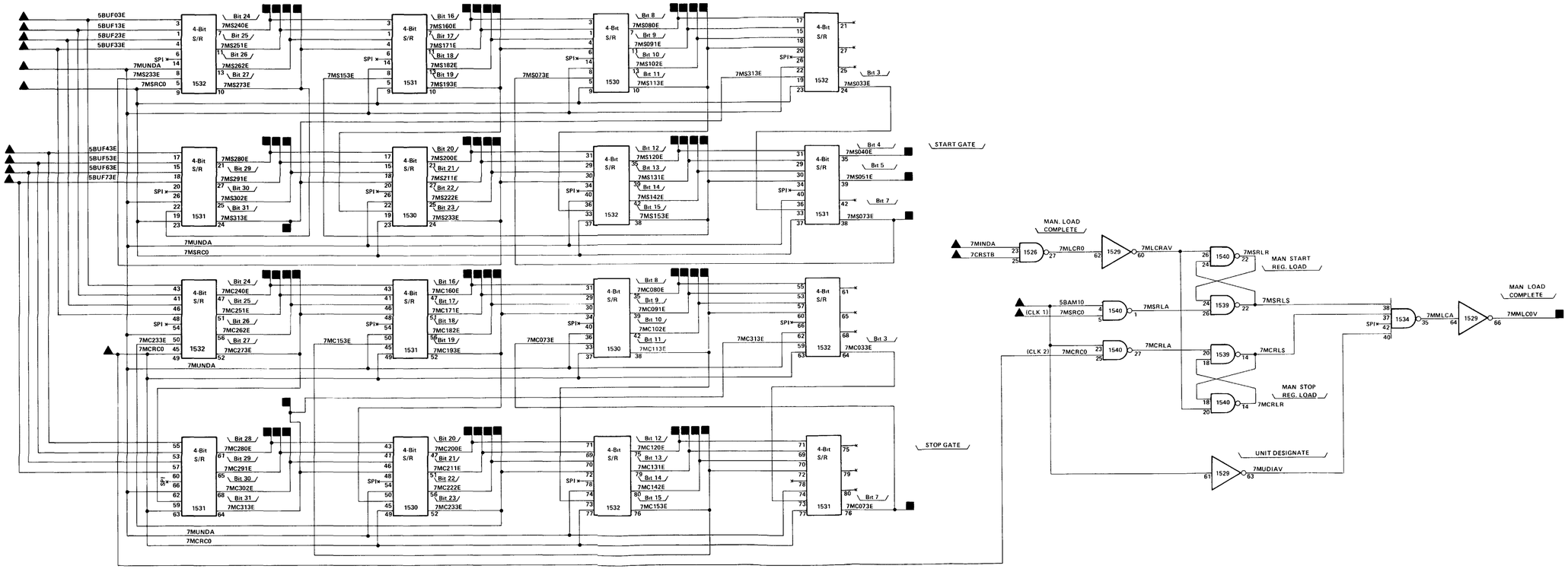
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

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5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

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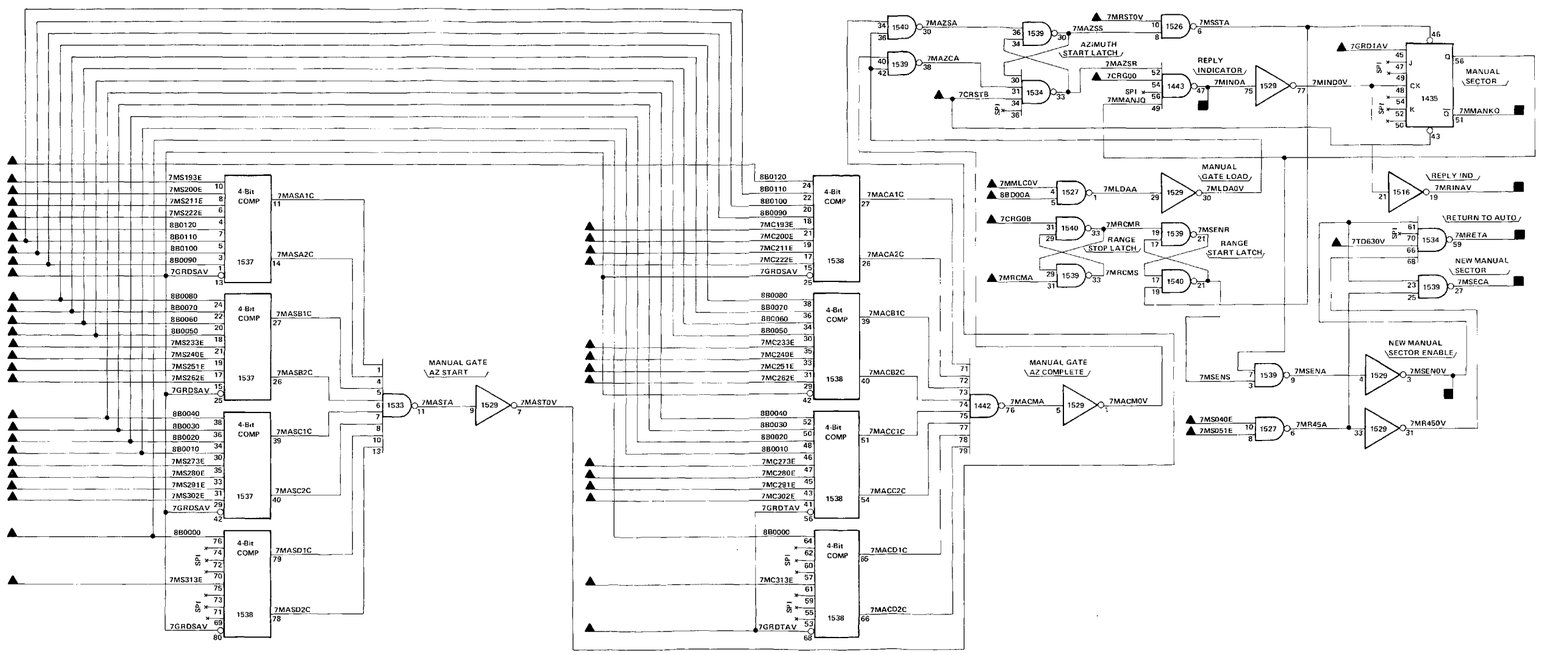
FO-23. ACM Manual Gate Register Logic Diagram

INPUT		INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
7CRG0B	01501	8B0020	17102	7MINDA	02300
7CRG00	01501	8B0030	28402	7MMANKQ	05002
7CRSTB	01501	8B0035	17102	7MRETA	04000, 04100
7GRD5AV	01502	8B0038	28402	7MRINAV	13101, 26802, 28201
7GRDTAV	01502	8B0040	28402	7MSECA	04000, 04100
7GRD1AV	01502	8B0040	28402	7MSENDV	05002, 05900
7MC193E	02300	8B0050	17102		
7MC200E	02300	8B0050	28402		
7MC211E	02300	8B0060	17102		
7MC222E	02300	8B0060	28402		
7MC233E	02300	8B0070	17102		
7MC240E	02300	8B0070	28402		
7MC251E	02300	8B0080	17102		
7MC262E	02300	8B0080	28402		
7MC273E	02300	8B0090	17102		
7MC280E	02300	8B0090	28402		
7MC291E	02300	8B0100	17102		
7MC302E	02300	8B0100	28402		
7MC313E	02300	8B0110	17102		
7MMLC0V	02300	8B0110	28402		
7MRCMA	02500	8B0120	17102		
7MRST0V	02500	8B0120	28402		
7MS040E	02300				
7MS051E	02300				
7MS193E	02300				
7MS200E	02300				
7MS211E	02300				
7MS222E	02300				
7MS233E	02300				
7MS240E	02300				
7MS251E	02300				
7MS262E	02300				
7MS273E	02300				
7MS280E	02300				
7MS291E	02300				
7MS302E	02300				
7MS313E	02300				
7TD630V	04100				
8B0D0A	17200				
8B0D0A	26802				
8B0D0A	28401				
8B0D0C	17102				
8B0000	26802				
8B0000	28401				
8B0010	17102				
8B0010	28402				



FO-24. ACM Azimuth Manual Gate Comparator Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

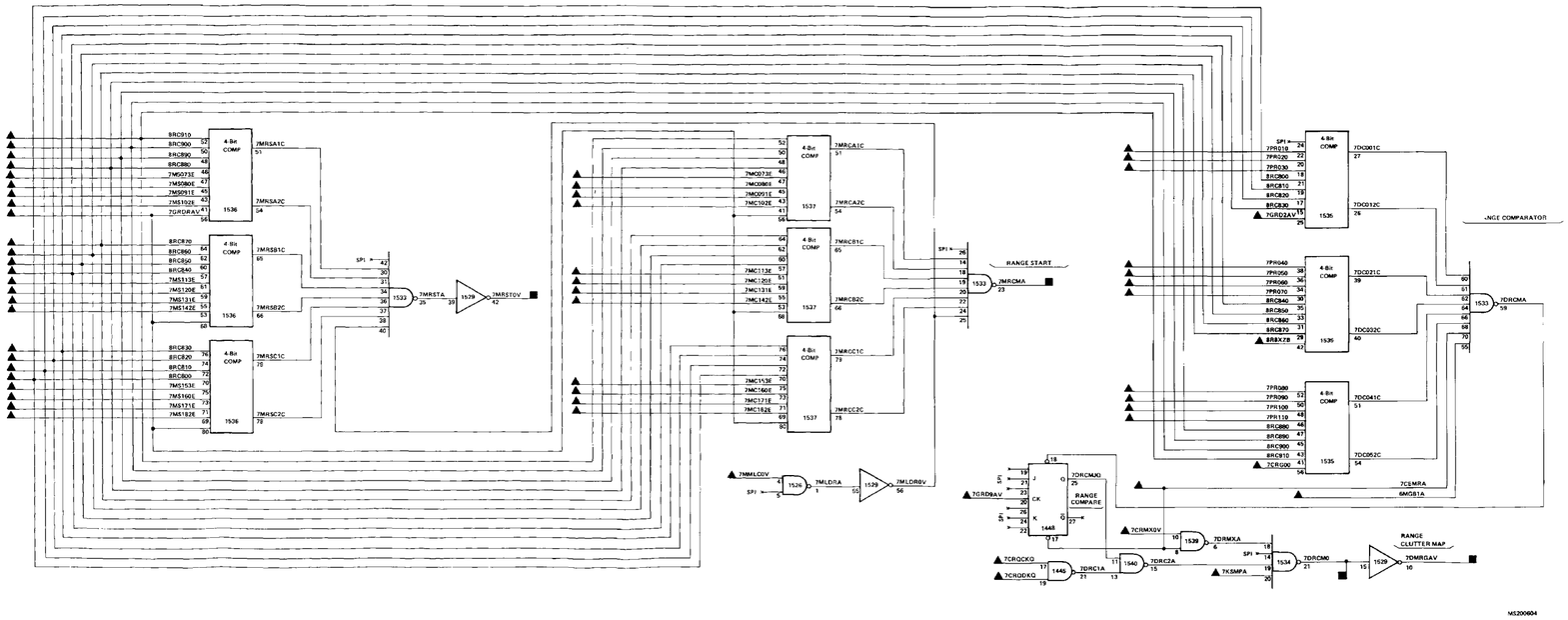
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT		INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
6MGB1A	05600	7PR110	32101	7DMRGAV	J 01501, 01700, 04200
6MGB1A	27201	8RC800	14900	7DRCM0	01501, 04900
7CEMRA	01700	8RC800	26802	7MRCMA	02400
7CRG00	01501	8RC800	27202	7MRSTOV	02400
7CRMXOV	01501	8RC810	14900		
7CRQCKQ	01700	8RC810	26802		
7CRQDKQ	01700	8RC810	27202		
7GRDRAV	01502	8RC820	14900		
7GRD2AV	01502	8RC820	26802		
7GRD9AV	01502	8RC820	27202		
7KSMPA	02000	8RC830	14900		
7MC073E	02300	8RC830	26802		
7MC080E	02300	8RC830	27202		
7MC091E	02300	8RC840	14900		
7MC102E	02300	8RC840	26802		
7MC113E	02300	8RC840	27202		
7MC120E	02300	8RC850	14900		
7MC131E	02300	8RC850	26802		
7MC142E	02300	8RC850	27202		
7MC153E	02300	8RC860	14900		
7MC160E	02300	8RC860	26802		
7MC171E	02300	8RC860	27202		
7MC182E	02300	8RC870	14600		
7MMLCOV	02300	8RC870	26802		
7MS073E	02300	8RC870	27202		
7MS080E	02300	8RC880	14600		
7MS091E	02300	8RC880	26802		
7MS102E	02300	8RC880	27202		
7MS113E	02300	8RC890	14600		
7MS120E	02300	8RC890	26802		
7MS131E	02300	8RC890	27202		
7MS142E	02300	8RC900	14600		
7MS153E	02300	8RC900	26802		
7MS160E	02300	8RC900	27202		
7MS171E	02300	8RC910	14600		
7MS182E	02300	8RC910	26802		
7PR010	32101	8RC910	27202		
7PR020	32101	8RBXZB	14300		
7PR030	32101	8RBXZB	27201		
7PR040	32101				
7PR050	32101				
7PR060	32101				
7PR070	32101				
7PR080	32101				
7PR090	32101				
7PR100	32101				



FO-25. ACM Range Manual Gate Comparator Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

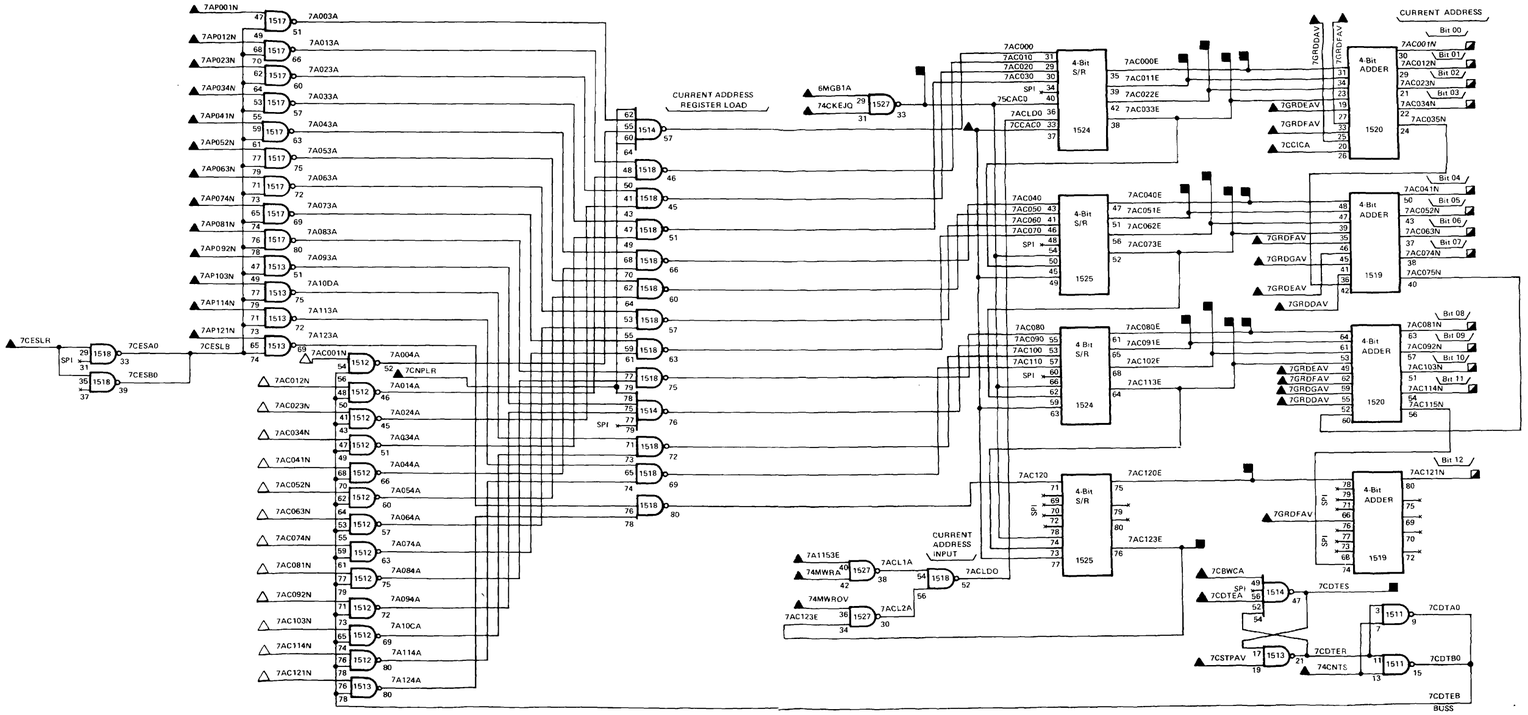
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT		OUTPUT	
	SOURCE		DESTINATION
SIGNAL	FO-SH	SIGNAL	FO-SH
6MGB1A	05600	7AC000E	02900
6MGB1A	27201	7AC001N	02700, 02900, 03100
7AP001N	02700	7AC011E	02900
7AP012N	02700	7AC012N	02700, 02900, 03100
7AP023N	02700	7AC022E	02900
7AP034N	02700	7AC023N	02700, 02900, 03100
7AP041N	02700	7AC033E	02900
7AP052N	02700	7AC034N	02700, 02900, 03100
7AP063N	02700	7AC040E	02900
7AP074N	02700	7AC041N	02700, 02900, 03100
7AP081N	02700	7AC051E	02900
7AP092N	02700	7AC052N	02700, 02900, 03100
7AP103N	02700	7AC062E	02900
7AP114N	02700	7AC063N	02700, 02900, 03100
7AP121N	02700	7AC073E	02900, 05102
7A1153E	03000	7AC074N	02700, 02900, 03100
7CBWCA	01800	7AC080E	02900
7CCACO	01800	7AC081N	02700, 02900, 03100
7CCICA	01800	7AC091E	02900
7CDTEA	01800	7AC092N	02700, 02900, 03100
7CESLR	01800	7AC102E	02900
7CNPLR	01800	7AC103N	02700, 02900, 03100
7CSTPAV	01700	7AC113E	02900
7GRDDAV	01502	7AC114N	02700, 02900, 03100
7GRDEAV	01502	7AC120E	02900
7GRDFAV	01502	7AC121N	02700, 02900, 03100
7GRDGAV	01502	7AC123E	03000, 05102
74CKEJQ	05101	7CDTES	01800
74CNTS	05101	75CACO	03000
74MWRA	04200		
74MWROV	04200		



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

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8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

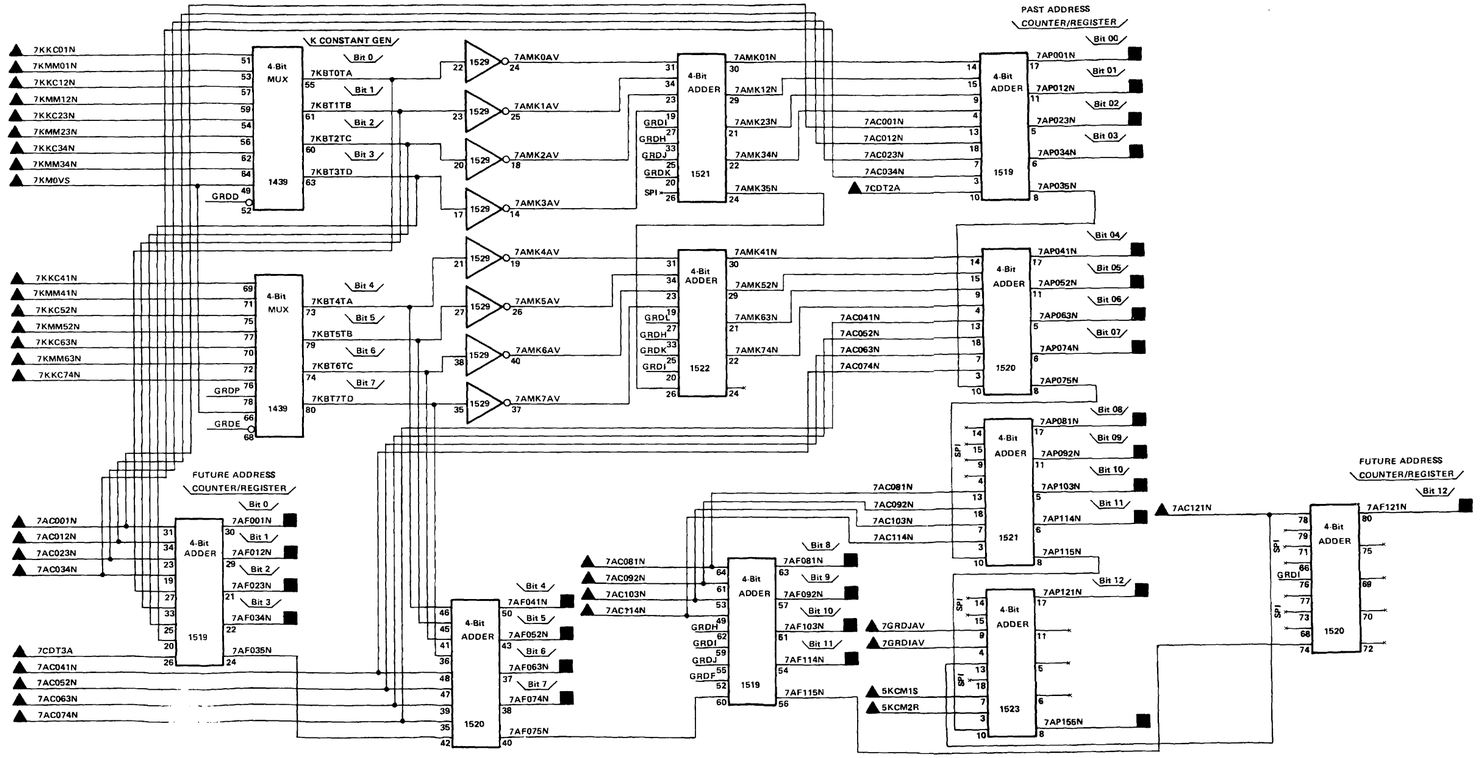
    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

FO-26. ACM Current Address Counter Logic Diagram

INPUT SIGNAL	SOURCE FO-SH
5KCM1S	13400
5KCM2R	28201
7AC001N	02600
7AC012N	02600
7AC023N	02600
7AC034N	02600
7AC041N	02600
7AC052N	02600
7AC063N	02600
7AC074N	02600
7AC081N	02600
7AC092N	02600
7AC103N	02600
7AC114N	02600
7AC121N	02600
7CDT2A	01600
7CDT3A	01600
7GRDJAV	01502
7KKC01N	02200
7KKC12N	02200
7KKC23N	02200
7KKC34N	02200
7KKC41N	02200
7KKC52N	02200
7KKC63N	02200
7KKC74N	02200
7KMM01N	02000
7KMM12N	02000
7KMM23N	02000
7KMM34N	02000
7KMM41N	02000
7KMM52N	02000
7KMM63N	02000
7KM0VS	02100

OUTPUT SIGNAL	DESTINATION FO-SH
7AF001N	03100
7AF012N	03100
7AF023N	03100
7AF034N	03100
7AF041N	03100
7AF052N	03100
7AF063N	03100
7AF074N	03100
7AF081N	03100
7AF092N	03100
7AF103N	03100
7AF114N	03100
7AF121N	03100
7AP001N	02600, 03100
7AP012N	02600, 03100
7AP023N	02600, 03100
7AP034N	02600, 03100
7AP041N	02600, 03100
7AP052N	02600, 03100
7AP063N	02600, 03100
7AP074N	02600, 03100
7AP081N	02600, 03100
7AP092N	02600, 03100
7AP103N	02600, 03100
7AP114N	02600, 03100
7AP121N	02600, 03100
7AP155N	05102



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2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

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4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

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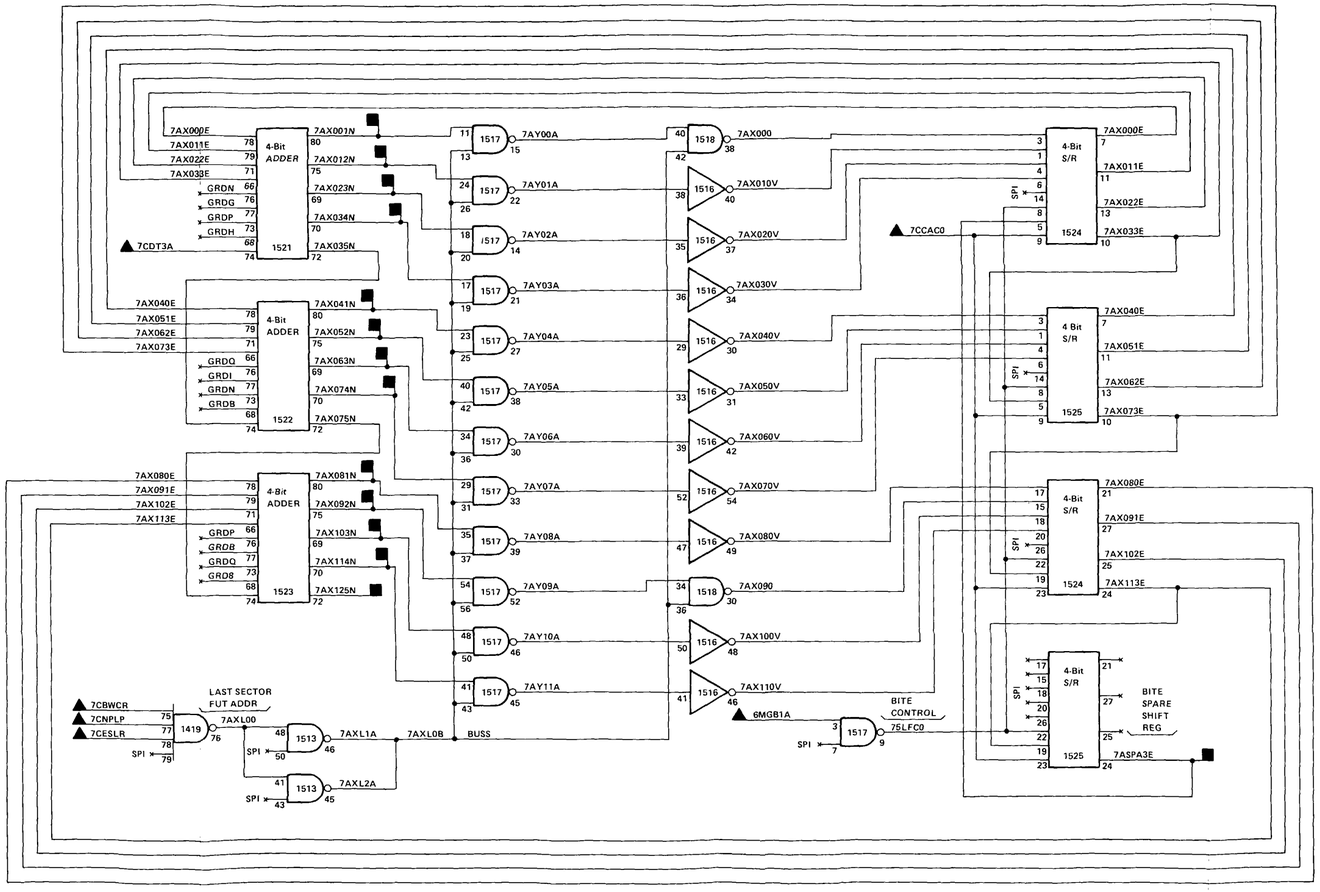
FO-27. ACM Past and Future Address Generation Logic Diagram

INPUT	
SIGNAL	SOURCE FO-SH
6MGB1A	05600
6MGB1A	27201
7CBWCR	01800
7CCACO	01800
7CDT3A	01600
7CESLR	01800
7CNPLR	01800

OUTPUT	
SIGNAL	DESTINATION FO-SH
7ASPA3E	05003
7AX001N	03100
7AX012N	03100
7AX023N	03100
7AX034N	03100
7AX041N	03100
7AX052N	03100
7AX063N	03100
7AX074N	03100
7AX081N	03100
7AX092N	03100
7AX103N	03100
7AX114N	03100
7AX125N	03100



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
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4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

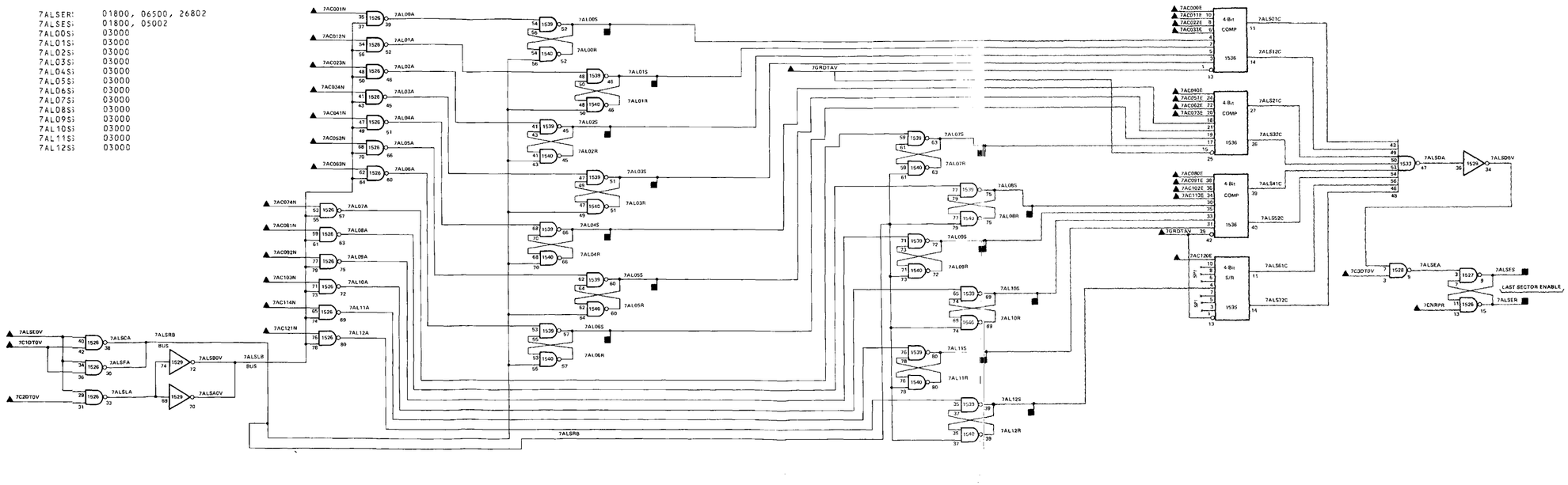
    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

FO-28. ACM Last Sector-Future Address Counter Logic Diagram

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
7AC000E	02600	7ALSER:	01800, 06500, 26802
7AC001N	02600	7ALSES:	01800, 05002
7AC011E	02600	7AL00S:	03000
7AC012N	02600	7AL01S:	03000
7AC022E	02600	7AL02S:	03000
7AC023N	02600	7AL03S:	03000
7AC033E	02600	7AL04S:	03000
7AC034N	02600	7AL05S:	03000
7AC040E	02600	7AL06S:	03000
7AC041N	02600	7AL07S:	03000
7AC051E	02600	7AL08S:	03000
7AC052N	02600	7AL09S:	03000
7AC062E	02600	7AL10S:	03000
7AC063N	02600	7AL11S:	03000
7AC073E	02600	7AL12S:	03000
7AC074N	02600		
7AC080E	02600		
7AC081N	02600		
7AC091E	02600		
7AC092N	02600		
7AC102E	02600		
7AC103N	02600		
7AC113E	02600		
7AC114N	02600		
7AC120E	02600		
7AC121N	02600		
7ALSE0V	03300		
7CNRPR	01800		
7C1DT0V	01600		
7C2DT0V	01600		
7C3DT0V	01600		
7GRDTAV	01502		



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
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4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

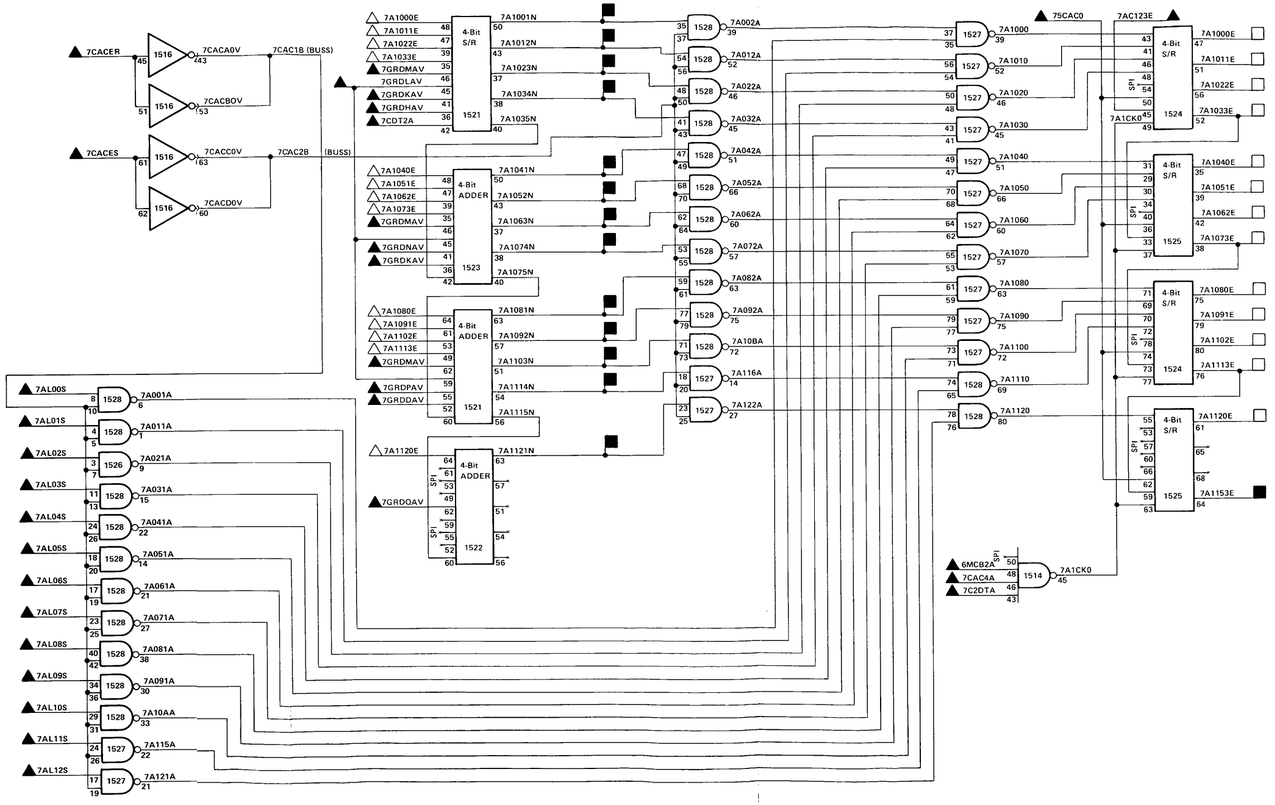
    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

FO-29. ACM Last Sector Storage Logic Diagram

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
6MCB2A	05600	7A1001N	03100
7AC123E	02600	7A1012N	03100
7AL00S	02900	7A1023N	03100
7AL01S	02900	7A1034N	03100
7AL02S	02900	7A1041N	03100
7AL03S	02900	7A1052N	03100
7AL04S	02900	7A1063N	03100
7AL05S	02900	7A1074N	03100
7AL06S	02900	7A1081N	03100
7AL07S	02900	7A1092N	03100
7AL08S	02900	7A1103N	03100
7AL09S	02900	7A1114N	03100
7AL10S	02900	7A1121N	03100
7AL11S	02900	7A1153E	02600, 05003
7AL12S	02900		
7CACER	01800		
7CACES	01800		
7CAC4A	01800		
7CDT2A	01600		
7C2DTA	01600		
7GRDDAV	01502		
7GRDHAV	01502		
7GRDKAV	01502		
7GRDLAV	01502		
7GRDMAV	01502		
7GRDNAV	01502		
7GRDPAV	01502		
7GRDQAV	01502		
75CAC0	02600		



FO-30. ACM First Sector-Past Address Counter/Register Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1AS).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

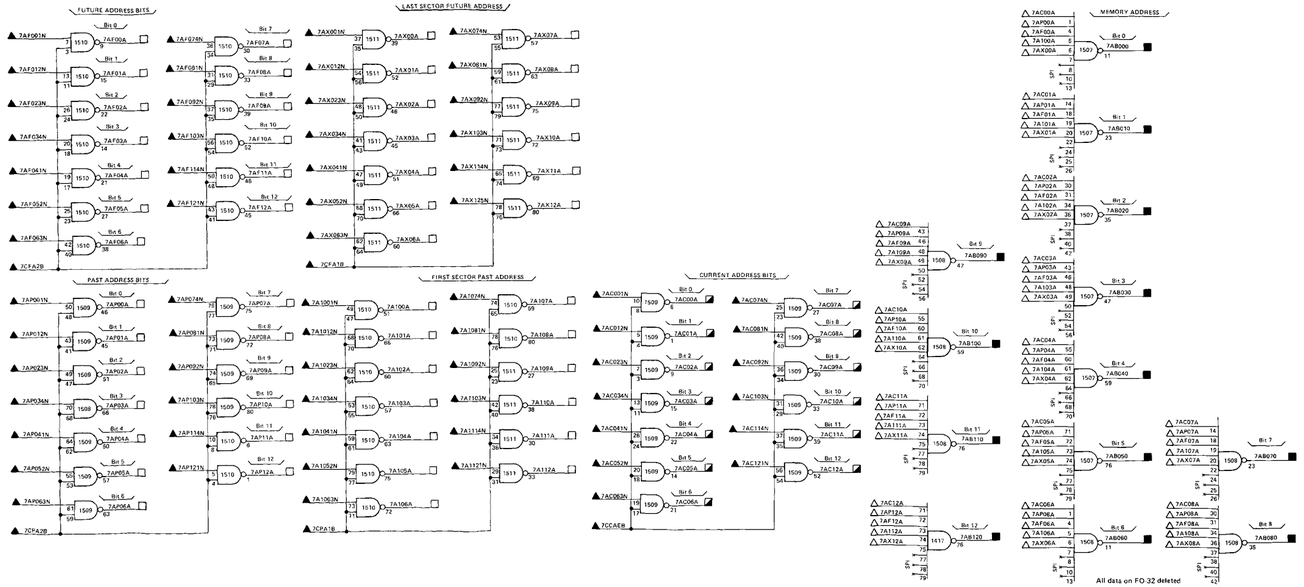
    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

MS200609

INPUT SIGNAL	INPUT SOURCE FO-SH	INPUT SIGNAL	INPUT SOURCE FO-SH	OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
AC001N	02600	7AX074N	02800	7AB000	05002, 06300, 26802
AC012N	02600	7AX081N	02800	7AB010	05002, 06300, 26802
AC023N	02600	7AX092N	02800	7AB020	05002, 06300, 26802
AC034N	02600	7AX103N	02800	7AB030	05002, 06300, 26802
AC041N	02600	7AX114N	02800	7AB040	05002, 06300, 26802
AC052N	02600	7AX125N	02800	7AB050	05002, 06300, 26802
AC063N	02600	7A1007N	03000	7AB060	05002, 06300, 26802
AC074N	02600	7A1012N	03000	7AB070	05002, 06300, 26802
AC081N	02600	7A1023N	03000	7AB080	05002, 06300, 26802
AC092N	02600	7A1034N	03000	7AB090	01700, 05002, 06300, 26802
AC103N	02600	7A1041N	03000	7AB100	01700, 05002, 06300, 26802
AC114N	02600	7A1052N	03000	7AB110	01700, 05002, 06300, 26802
AC121N	02600	7A1063N	03000	7AB120	01700, 05002, 06300, 26802
AF001N	02700	7A1074N	03000	7AC00A	05102
AF012N	02700	7A1081N	03000	7AC01A	05102
AF023N	02700	7A1092N	03000	7AC02A	05102
AF034N	02700	7A1103N	03000	7AC03A	05102
AF041N	02700	7A1114N	03000	7AC04A	05102
AF052N	02700	7A1121N	03000	7AC05A	05102
AF063N	02700	7CCAEB	01800	7AC06A	05102
AF074N	02700	7CFA1B	01800	7AC07A	05102
AF081N	02700	7CFA2B	01800	7AC08A	05102
AF092N	02700	7CPA1B	01800	7AC09A	05102
AF103N	02700	7CPA2B	01800	7AC10A	05102
AF114N	02700			7AC11A	05102
AF121N	02700			7AC12A	05102
AP001N	02700				
AP012N	02700				
AP023N	02700				
AP034N	02700				
AP041N	02700				
AP052N	02700				
AP063N	02700				
AP074N	02700				
AP081N	02700				
AP092N	02700				
AP103N	02700				
AP114N	02700				
AP121N	02700				
AX001N	02800				
AX012N	02800				
AX023N	02800				
AX034N	02800				
AX041N	02800				
AX052N	02800				
AX063N	02800				



FO-31. ACM Memory Address Gating Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

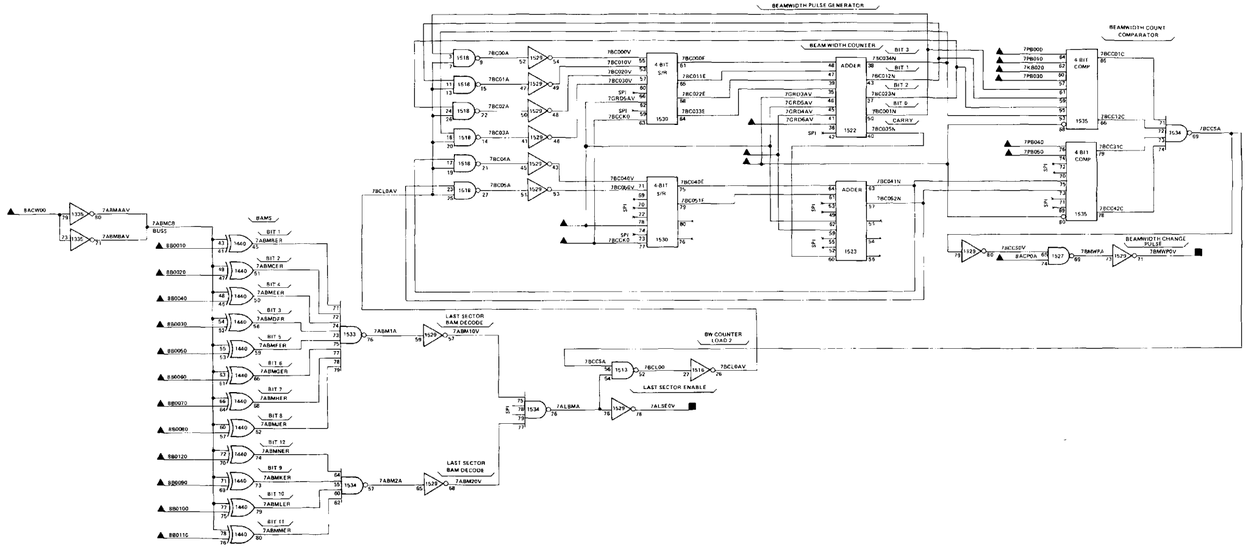
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
7BCCK0	01501	7ALSE0V	02900
7GRD3AV	01502	7BMWP0V	01800, 03400
7GRD4AV	01502		
7GRD5AV	01502		
7GRD6AV	01502		
7KB020	02000		
7PB000	32101		
7PB010	32101		
7PB030	32101		
7PB040	26802		
7PB040	32000		
7PB040	32101		
7PB050	32000		
7PB050	32101		
8ACP0A	17200		
8ACP0A	28401		
8ACW00	16900		
8ACW00	26802		
8ACW00	28401		
8B0010	17102		
8B0010	28402		
8B0020	17102		
8B0020	28402		
8B0030	17102		
8B0030	28402		
8B0040	17102		
8B0040	28402		
8B0050	17102		
8B0050	28402		
8B0060	17102		
8B0060	28402		
8B0070	17102		
8B0070	28402		
8B0080	17102		
8B0080	28402		
8B0090	17102		
8B0090	28402		
8B0100	17102		
8B0100	28402		
8B0110	17102		
8B0110	28402		
8B0120	17102		
8B0120	28402		



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

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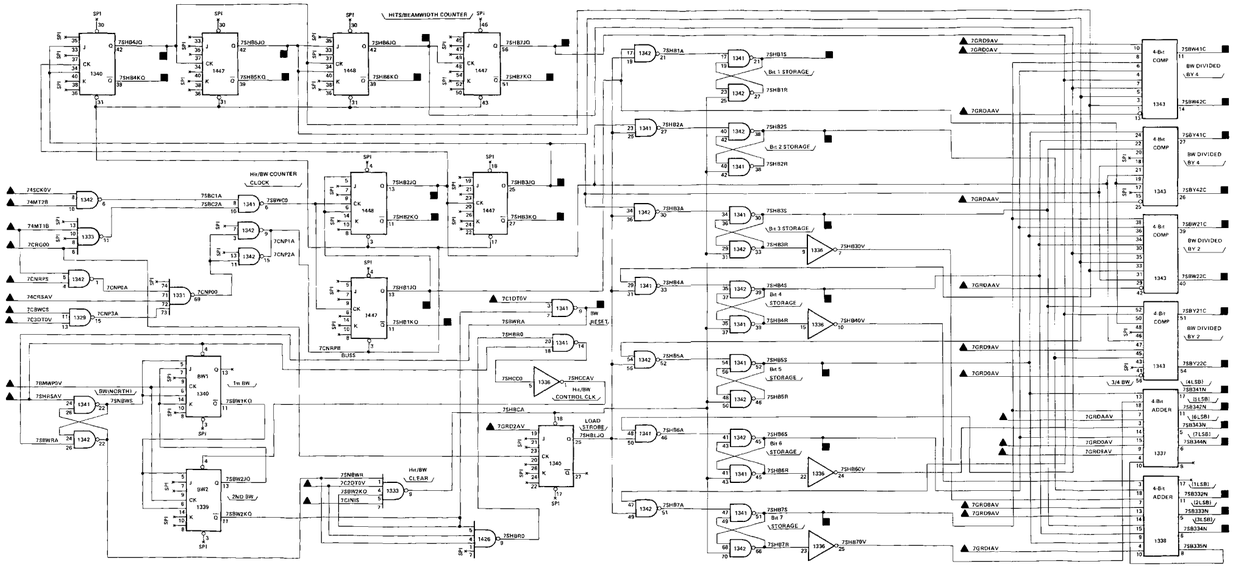
FO-33. ACM Beam width Pulse Generator Logic Diagram

INPUT SIGNAL	SOURCE FO-SH
7CFMPR	01900
7CFMPR	26802
7CNRPOV	01501
7GRD1AV	01502
7PS11A	32101
7PS12A	32101
7PS13A	32101
7PS14A	32101
7PS15A	32101
7PS22A	32101
7PS33A	32101
7SBW21C	03400
7SBW22C	03400
7SBW41C	03400
7SBW42C	03400
7SBY21C	03400
7SBY22C	03400
7SBY41C	03400
7SBY42C	03400
7SB332N	03400
7SB333N	03400
7SB334N	03400
7SB341N	03400
7SB342N	03400
7SB343N	03400
7SB344N	03400
7SHB1JQ	03400
7SHB2JQ	03400
7SHB3JQ	03400
7SHB4JQ	03400
7SHB5JQ	03400
7SHB6JQ	03400
7SHB7JQ	03400

OUTPUT SIGNAL	DESTINATION FO-SH
7SAMP0	03700, 03900, 05002



FO-34. ACM Hits/Beamwidth Calculator Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XI FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

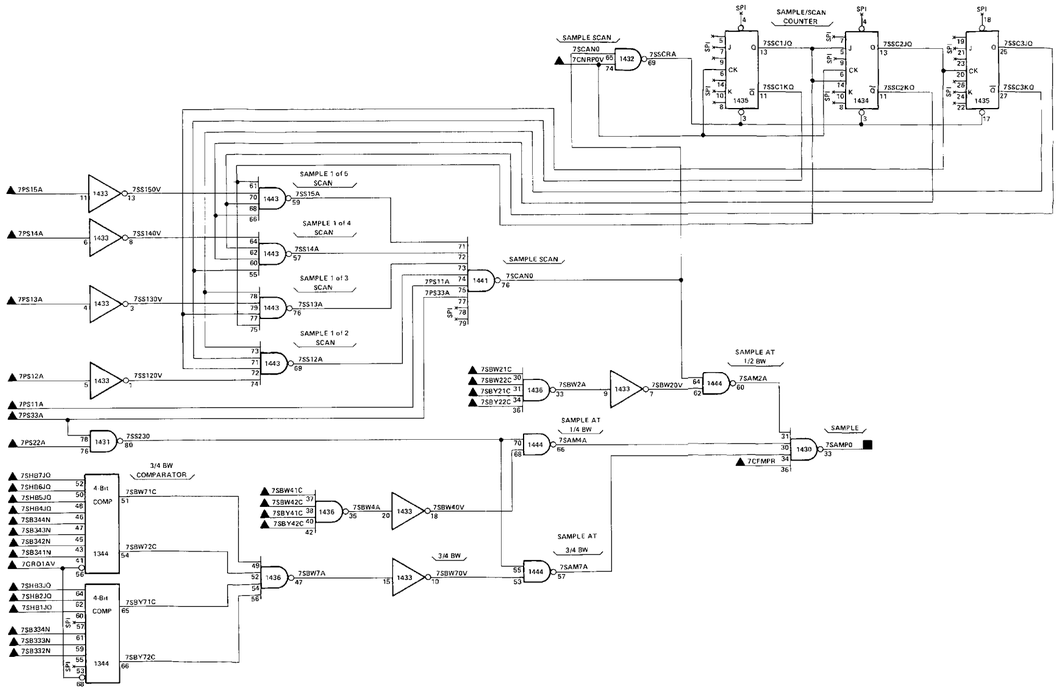
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
7BMWP0V	03300	7SBWRA	01501
7CBWCS	01800	7SBW21C	03500
7CINIS	01900	7SBW22C	03500
7CWRPS	01800	7SBW41C	03500
7CRG00	01501	7SBW42C	03500
7C1D1QV	01600	7SBY21C	03500
7C2D1QV	01600	7SBY22C	03500
7C3D1QV	01600	7SBY41C	03500
7GRDAAV	01502	7SBY42C	03500
7GRD0AV	01502	7SB332N	03500
7GRD1AV	01502	7SB333N	03500
7GRD2AV	01502	7SB334N	03500
7GRD8AV	01502	7SB341N	03500
7GRD9AV	01502	7SB342N	03500
7SHRSAV	01501	7SB343N	03500
74CRSAV	05101	7SB344N	03500
74MT1B	05101	7SHB1JQ	03500, 05101
74MT1B	28202	7SHB1KQ	01502, 05101, 05102
74MT2B	05101	7SHB1S	01502
74SCK0V	05101	7SHB2JQ	03500, 05101
		7SHB2KQ	01502, 05101, 05102
		7SHB2S	01502
		7SHB3JQ	03500, 05101
		7SHB3KQ	01502, 05101, 05102
		7SHB3S	01502
		7SHB4JQ	03500, 05101
		7SHB4KQ	01502
		7SHB4S	01502
		7SHB5JQ	03500, 05101
		7SHB5KQ	01502
		7SHB5S	01502
		7SHB6JQ	03500, 05101
		7SHB6KQ	01502
		7SHB6S	01502
		7SHB7JQ	03500
		7SHB7KQ	01502
		7SHB7S	01502



FO-35. ACM Sample/Scan Control Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTDN DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

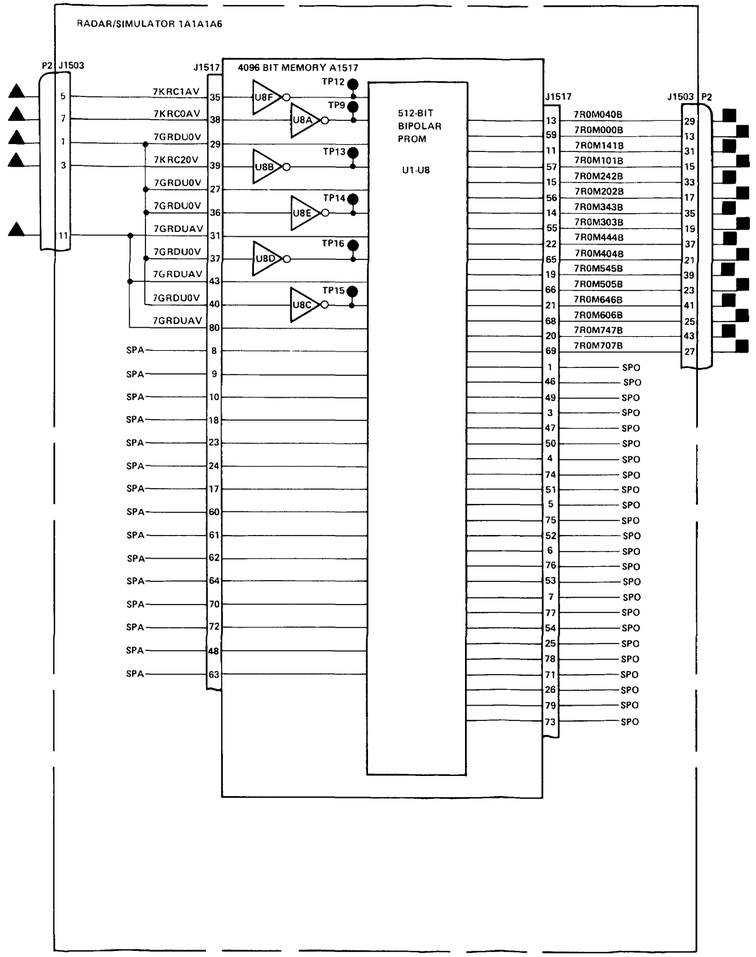
    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

MS200613

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
7GRDUAV	01502	7R0M000B	03700, 30401
7GRDUAV	30401	7R0M040B	03700, 30401
7GRDU0V	01502	7R0M101B	03700, 30401
7GRDU0V	30401	7R0M141B	03700, 30401
7KRC0AV	02100	7R0M202B	03700, 30401
7KRC0AV	27202	7R0M242B	03700, 30401
7KRC1AV	02100	7R0M303B	03700, 30401
7KRC1AV	30401	7R0M343B	03700, 30401
7KRC20V	02100	7R0M404B	03700, 30401
		7R0M444B	03700, 30401
		7R0M505B	03700, 30401
		7R0M545B	03700, 30401
		7R0M606B	26802, 30401
		7R0M646B	26802, 30401
		7R0M707B	26802, 30401
		7R0M747B	26802, 30401



NOTES: UNLESS OTHERWISE SPECIFIED

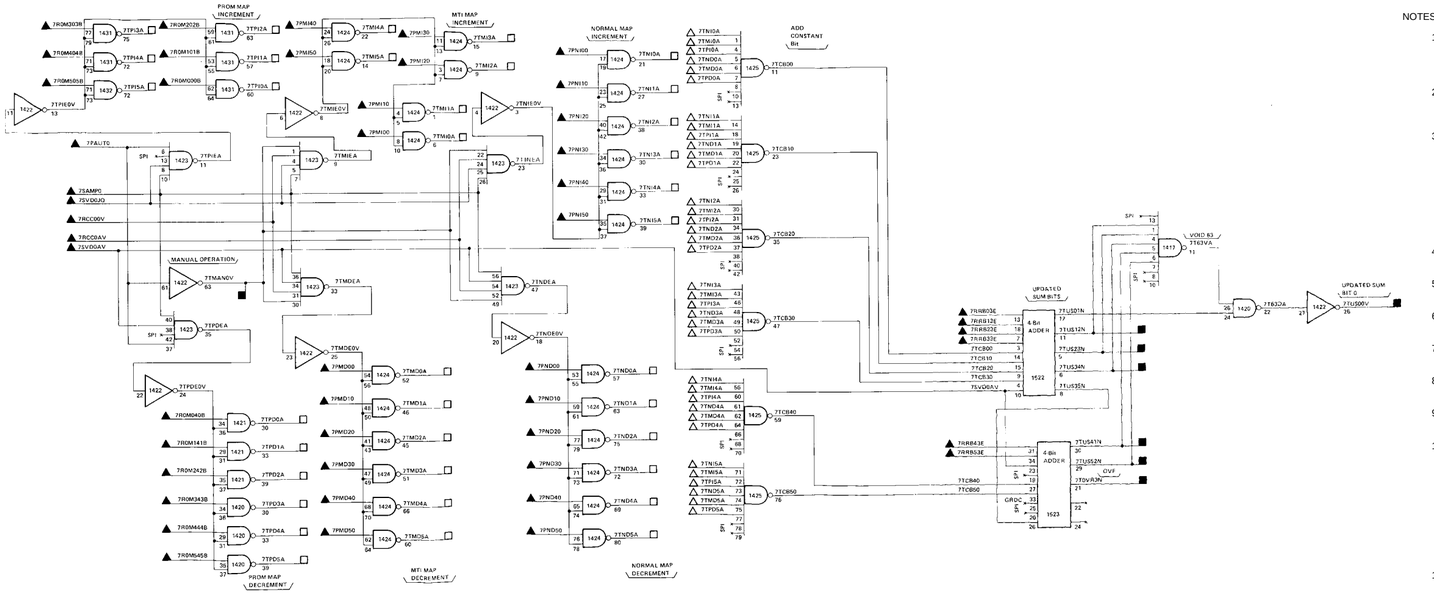
1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, RSU (1A1A1A6).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO APPROPRIATE TABLE IN TM 9-1430-655-20-3 FAR CARD PART NUMBER.

FO-36. ACM PROM Logic Diagram

INPUT SIGNAL	INPUT SOURCE FO-SH	SIGNAL	SOURCE FO-SH	OUTPUT SIGNAL	DESTINATION FO-SH
7PAUT0	32102	7ROM202B	03600	7TMANOV	04100
7PMD00	32102	7ROM202B	26802	7TUS00V	04100, 05002
7PMD10	32102	7ROM202B	30401	7TUS12N	04100, 05002
7PMD20	32102	7ROM242B	03600	7TUS23N	04100, 05002
7PMD30	32102	7ROM242B	26802	7TUS34N	04100, 05002
7PMD40	32102	7ROM242B	30401	7TUS41N	04100, 05002
7PMD50	32102	7ROM303B	03600	7TUS52N	04100, 05002
7PM100	32102	7ROM303B	26802	7TOVR3N	04000
7PM110	32102	7ROM303B	30401		
7PM120	32102	7ROM343B	03600		
7PM130	32102	7ROM343B	26802		
7PM140	32102	7ROM343B	30401		
7PM150	32102	7ROM404B	03600		
7PND00	32101	7ROM404B	26802		
7PND10	32101	7ROM404B	30401		
7PND20	32101	7ROM444B	03600		
7PND30	32101	7ROM444B	26802		
7PND40	32101	7ROM444B	30401		
7PND50	32101	7ROM505B	03600		
7PN100	32101	7ROM505B	26802		
7PN110	32101	7ROM505B	30401		
7PN120	32101	7ROM545B	03600		
7PN130	32101	7ROM545B	26802		
7PN140	32101	7ROM545B	30401		
7PN150	32101	7SAMP0	03500		
7RCC0AV	03900	7SV00AV	03900		
7RCC0AV	30401	7SV00J0	03900		
7RCC00V	03900				
7RRB03E	04600				
7RRB13E	04600				
7RRB23E	04600				
7RRB33E	04600				
7RRB43E	04600				
7RRB53E	04600				
7ROM000B	03600				
7ROM000B	26802				
7ROM000B	30401				
7ROM040B	03600				
7ROM040B	26802				
7ROM040B	30401				
7ROM101B	03600				
7ROM101B	26802				
7ROM101B	30401				
7ROM141B	03600				
7ROM141B	26802				
7ROM141B	30401				



FO-37. ACM Map Increment/Decrement Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

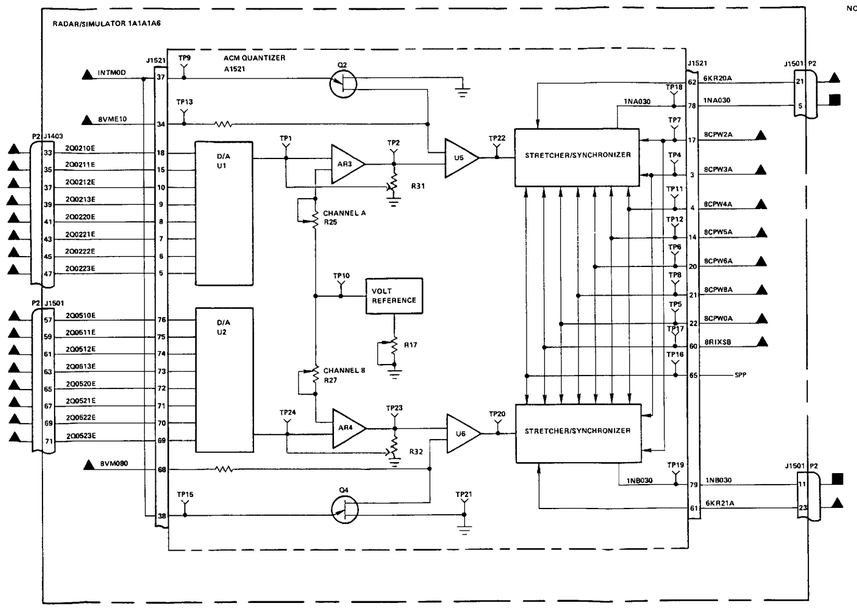
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH

INPUT		INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
INTM0D	26601	8CPW3A	27101	1NA030	00400, 03900, 26802, 30601
INTM0D	28801	8CPW3A	28901	1NB030	00400, 03900, 26802, 30601
2Q0210E	01102	8CPW4A	26803		
2Q0210E	29701	8CPW4A	27101		
2Q0211E	01102	8CPW4A	28901		
2Q0211E	29701	8CPW5A	26803		
2Q0212E	01102	8CPW5A	27101		
2Q0212E	29701	8CPW5A	28902		
2Q0213E	01102	8CPW6A	26803		
2Q0213E	29701	8CPW6A	27101		
2Q0220E	01102	8CPW6A	28902		
2Q0220E	29701	8CPW8A	26803		
2Q0221E	01102	8CPW8A	27101		
2Q0221E	29701	8CPW8A	28902		
2Q0222E	01102	8R1XSB	00300		
2Q0222E	29701	8R1XSB	14900		
2Q0223E	01102	8R1XSB	26802		
2Q0223E	29702	8R1XSB	26803		
2Q0510E	01103	8R1XSB	27501		
2Q0510E	30602	8VME70	14202		
2Q0511E	01103	8VM080	14202		
2Q0511E	30602				
2Q0512E	01103				
2Q0512E	30602				
2Q0513E	01103				
2Q0513E	30602				
2Q0520E	01103				
2Q0520E	30602				
2Q0521E	01103				
2Q0521E	30602				
2Q0522E	01103				
2Q0522E	30602				
2Q0523E	01103				
2Q0523E	30602				
6KR20A	05702				
6KR21A	05702				
8CPW0A	26504				
8CPW0A	26803				
8CPW0A	27101				
8CPW0A	28901				
8CPW2A	26802				
8CPW2A	26803				
8CPW2A	27101				
8CPW2A	28901				
8CPW2A	32102				
8CPW3A	26803				

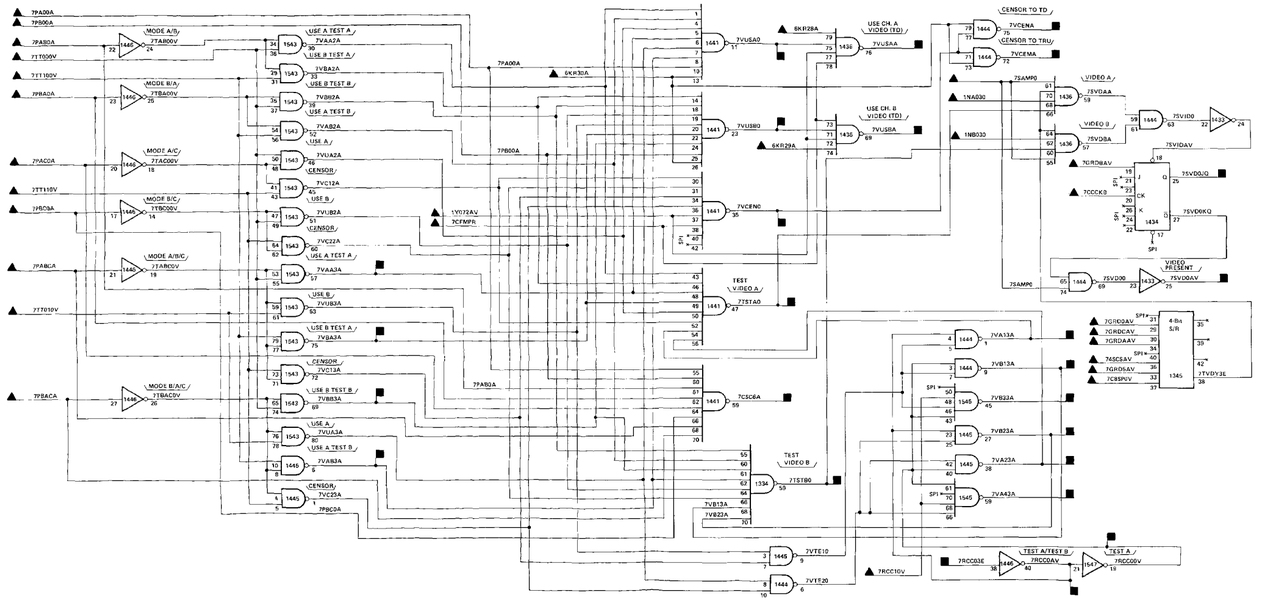


NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, RSU (1A1A1A6).
3. REFERENCE DESIGNATORS ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO SAME FIGURE
   - ◪ INDICATES OUTPUT TO SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAM INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO APPROPRIATE TABLE IN TM 9-1430-655-20-3 FOR CARD PART NUMBER.

Change 1 FO-38. ACM Quantizers Logic Diagram

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
1NA030	03800	7CSC6A	05003
1NA030	30601	7RCCOAV	03700, 04300, 04800, 26802, 30401
1NB030	03800		
1NB030	30601	7RCCOOV	03700, 04800
1VQ72AV	00500	7SVDOAV	03700, 04000
6KR28A	05702	7SVDOJQ	03700, 04000
6KR29A	05702	7TSTAO	05002
6KR30A	05702	7TSTBO	05002
7CCCKB	01501	7VAA3A	04000
7CFMPR	01900	7VAB3A	04000
7CFMPR	26802	7VA13A	04000
7CBSPOV	01501	7VA23A	04000
7GRDAAV	01502	7VA43A	04000
7GRDBAV	01502	7VBA3A	04000
7GRDCAV	01502	7VBB3A	04000
7GRDOAV	01502	7VB13A	04000
7GRD5AV	01502	7VB23A	04000
7PABCA	32102	7VB33A	04000
7PABOA	32102	7VCEMA	08502, 26802
7PACOA	32102	7VCENA	15400, 15500, 26802, 26803, 27202
7PAOOA	32102		
7PBACA	32102	7VCENO	05002
7PBAOA	32102	7VUSAA	15400, 15500, 26802, 26803, 27202
7PBCOA	32102		
7PBOOA	32102	7VUSAO	05002
7RCC03E	04300	7VUSBA	15400, 15500, 26802, 26803, 27202
7RCC10V	04800		
7SAMPO	03500	7VUSBO	05002
7TTDOOV	04900		
7TTO10V	04900		
7TT100V	04900		
7TT110V	04900		
74SCSAV	05101		



FO-39. ACM Video Gating Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

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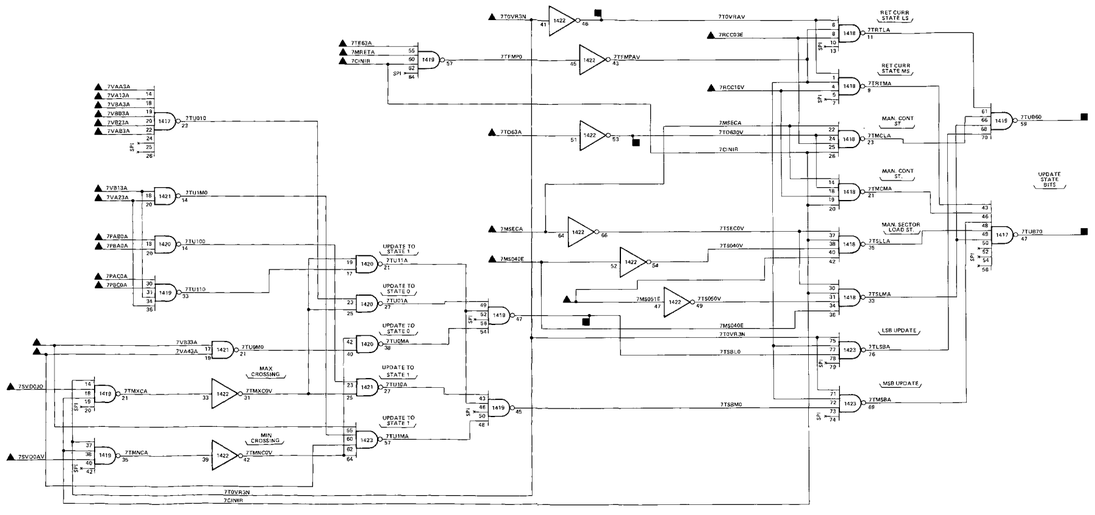
INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
7C1N1R	01900	7TSBL0	04100
7MRETA	02400	7TUB60	04200, 05002
7MSECA	02400	7TUB70	04200, 05002
7MSD4GE	02300	7TDVRAV	04100
7MS051E	02300		
7PAB0A	32102		
7PAC0A	32102		
7PBA0A	32102		
7PBC0A	32102		
7RCC03E	04300		
7RCC10V	04800		
7SVD0AV	03900		
7SVD0JG	03900		
7TE63A	04100		
7TDVR3N	03700		
7VAA3A	03900		
7VAB3A	03900		
7VA13A	03900		
7VA23A	03900		
7VA43A	03900		
7VBA3A	03900		
7VBB3A	03900		
7VB13A	03900		
7VB23A	03900		
7VB33A	03900		



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

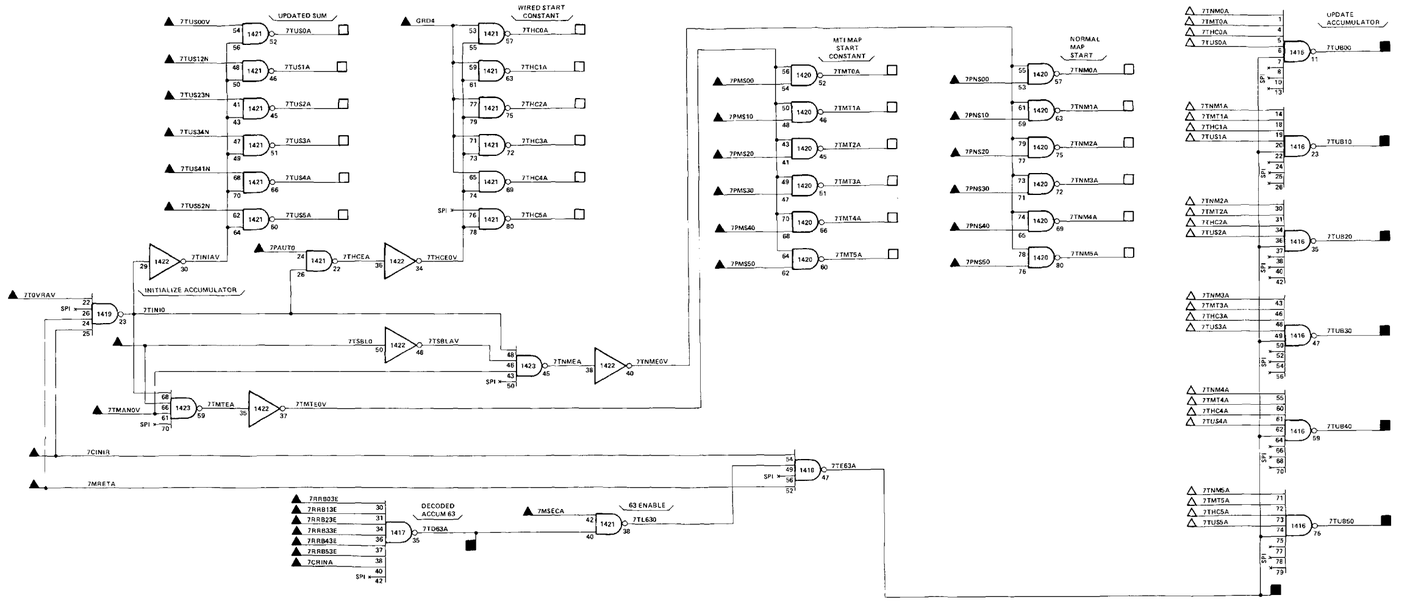
    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

FO-40. ACM State Counter Update Logic Diagram

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
7CINIR	01900	7TD63A	04100, 04900
7CRINA	01700	7TD630V	02400
7GRD4AV	01502	7TE63A	04000
7MRETA	02400	7TUB00	04200, 05002
7MSECA	02400	7TUB10	04200, 05002
7MSO40E	02300	7TUB20	04200, 05002
7PAUTO	32102	7TUB30	04200, 05002
7PMS00	32102	7TUB40	04200, 05002
7PMS10	32102	7TUB50	04200, 05002
7PMS20	32102		
7PMS30	32102		
7PMS40	32102		
7PMS50	32102		
7PNS00	32101		
7PNS10	32101		
7PNS20	32101		
7PNS30	32101		
7PNS40	32101		
7PNS50	32102		
7RRB03E	04600		
7RRB13E	04600		
7RRB23E	04600		
7RRB33E	04600		
7RRB43E	04600		
7RRB53E	04600		
7TD63A	04100		
7TMAN0V	03700		
7TSBL0	04000		
7TUS00V	03700		
7TUS12N	03700		
7TUS23N	03700		
7TUS34N	03700		
7TUS41N	03700		
7TUS52N	03700		
7TOVRAV	04000		



NOTES:

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

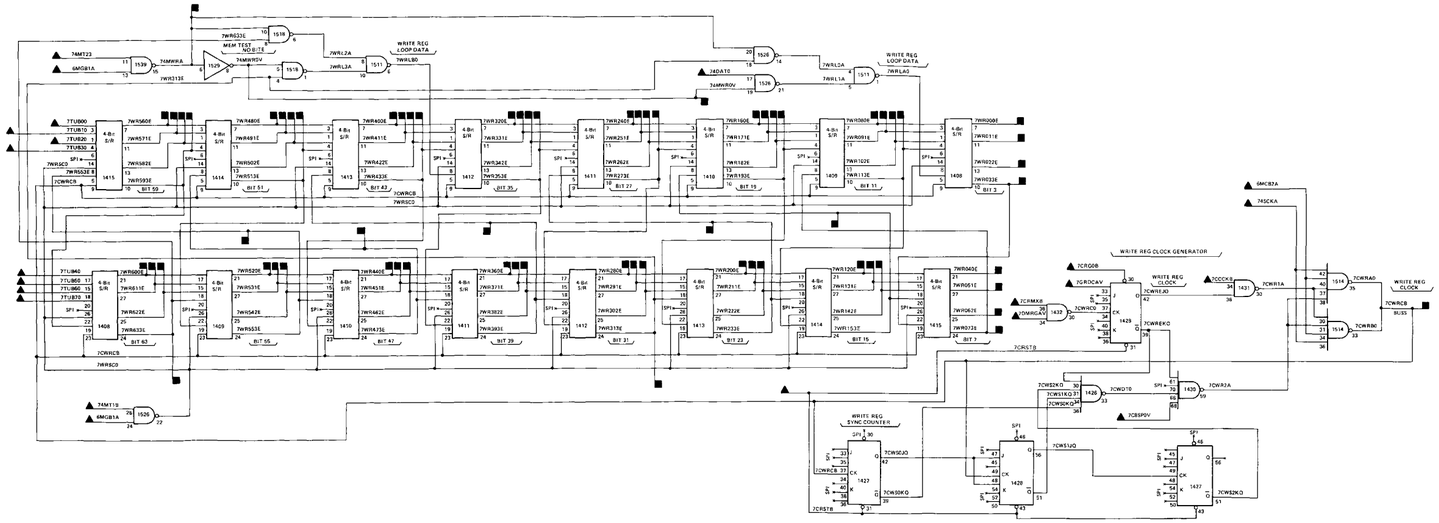
    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

MS200619

FO-41. ACM Clutter Cell Update Logic Diagram

INPUT		OUTPUT		OUTPUT	
	SOURCE	DESTINATION			DESTINATION
SIGNAL	FO-SH	SIGNAL	FO-SH	SIGNAL	FO-SH
6MCB2A	05600	7CWRCB	05102	7WR431E	06104, 26802
6MGB1A	05600	7WR000E	06101, 26802	7WR462E	06104, 26802
6MGB1A	27201	7WR011E	06101, 26802	7WR473E	06104, 26802
7CCCKB	01501	7WR022E	06101, 26802	7WR480E	06104, 26802
7CRSDB	01501	7WR033E	06101, 26802	7WR491E	06104, 26802
7CRMXB	01501	7WR040E	06101, 26802	7WR502E	06104, 26802
7CRSTB	01501	7WR051E	06101, 26802	7WR513E	06104, 26802
7CBSPDV	01501	7WR062E	06101, 26802	7WR520E	06104, 26802
7DMRGAV	02500	7WR073E	06101, 26802	7WR531E	06104, 26802
7GRDCAV	01502	7WR080E	06101, 26802	7WR542E	06104, 26802
7TUB00	04100	7WR091E	06101, 26802	7WR553E	06104, 26802
7TUB10	04100	7WR102E	06101, 26802	7WR560E	06105, 26802
7TUB20	04100	7WR113E	06101, 26802	7WR571E	06105, 26802
7TUB30	04100	7WR120E	06101, 26802	7WR582E	06105, 26802
7TUB40	04100	7WR131E	06101, 26802	7WR593E	06105, 26802
7TUB50	04100	7WR142E	06102, 26802	7WR600E	06105, 26802
7TUB60	04000	7WR153E	06102, 26802	7WR611E	06105, 26802
7TUB70	04000	7WR160E	06102, 26802	7WR622E	06105, 26802
7WRSCO	05102	7WR171E	06102, 26802	7WR633E	05003, 05102, 06105,
7WRSCO	05102	7WR182E	06102, 26802	74MWRA	02600, 04300
74MT1B	05101	7WR193E	06102, 26802	74MWRV	02600, 05102
74MT2B	05101	7WR200E	06102, 26802		
74SCKA	05101	7WR211E	06102, 26802		
		7WR222E	06102, 26802		
		7WR233E	06102, 26802		
		7WR240E	06102, 26802		
		7WR251E	06102, 26802		
		7WR262E	06102, 26802		
		7WR273E	06102, 26802		
		7WR280E	06103, 26802		
		7WR291E	06103, 26802		
		7WR302E	06103, 26802		
		7WR313E	05003, 06103, 26802		
		7WR320E	06103, 26802		
		7WR331E	06103, 26802		
		7WR342E	06103, 26802		
		7WR353E	06103, 26802		
		7WR360E	06103, 26802		
		7WR371E	06103, 26802		
		7WR382E	06103, 26802		
		7WR393E	06103, 26802		
		7WR400E	06103, 26802		
		7WR411E	06103, 26802		
		7WR422E	06104, 26802		
		7WR433E	06104, 26802		
		7WR440E	06104, 26802		



FO-42. ACM Memory Write Register Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

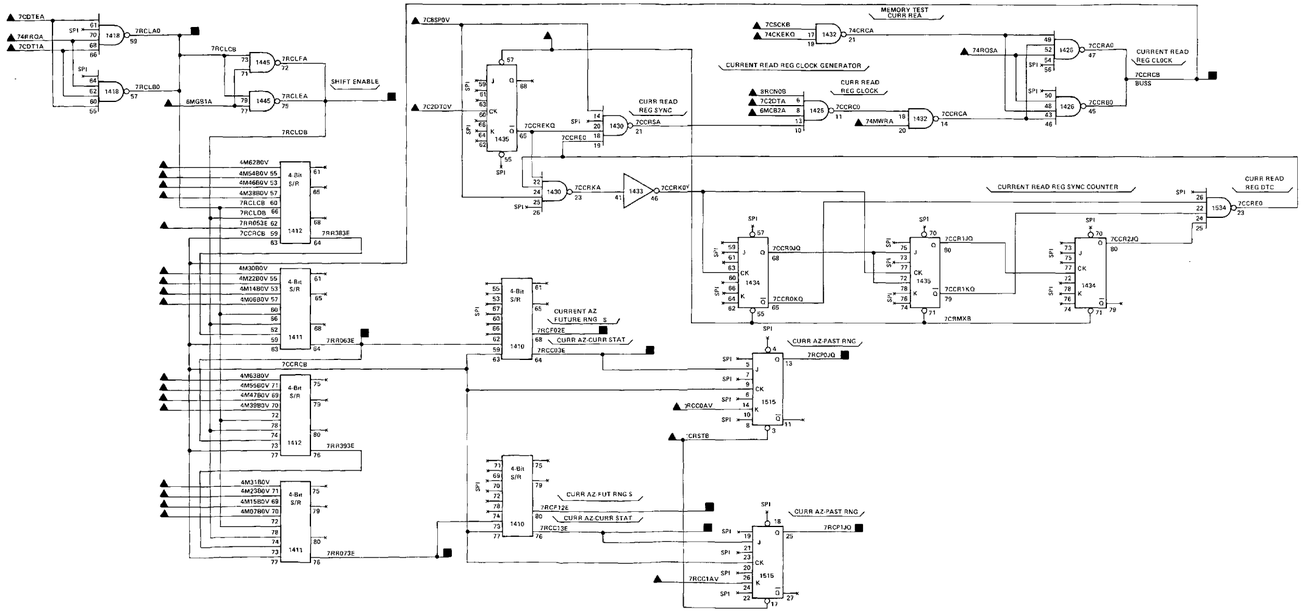
    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT	
SIGNAL	SOURCE FO-SH
4M06B0V	06401
4M07B0V	06401
4M14B0V	06401
4M15B0V	06401
4M22B0V	06401
4M23B0V	06401
4M30B0V	06401
4M31B0V	06401
4M38B0V	06402
4M39B0V	06402
4M46B0V	06402
4M47B0V	06402
4M54B0V	06402
4M55B0V	06402
4M62B0V	06402
4M63B0V	06402
6MCB2A	05600
6MGB1A	05600
6MGB1A	27201
7CDTEA	01800
7CDTIA	01600
7CRMXB	01501
7CRSTB	01501
7CSCKB	01501
7CZBTA	01600
7CZBTOV	01600
7CBSP0V	01501
7RCC0AV	03900
7RCCDAV	30401
7RCC1AV	04800
7RR053E	04600
74CKEKO	05101
74MWRA	04200
74RQSA	05101
74RRQA	05101
8RCN0B	15001
8RCN0B	27201

OUTPUT	
SIGNAL	DESTINATION FO-SH
7CCRCB	04600
7RCCO3E	03900, 04000
7RCC13E	04700, 04800
7RCF02E	04800
7RCF12E	04700, 04800
7RCLCB	04600
7RCLDB	04600
7RCP0JQ	04800
7RCP1JQ	04700, 04800
7RR063E	05102
7RR073E	04600, 05003, 05102



FO-43. ACM Current State Register Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

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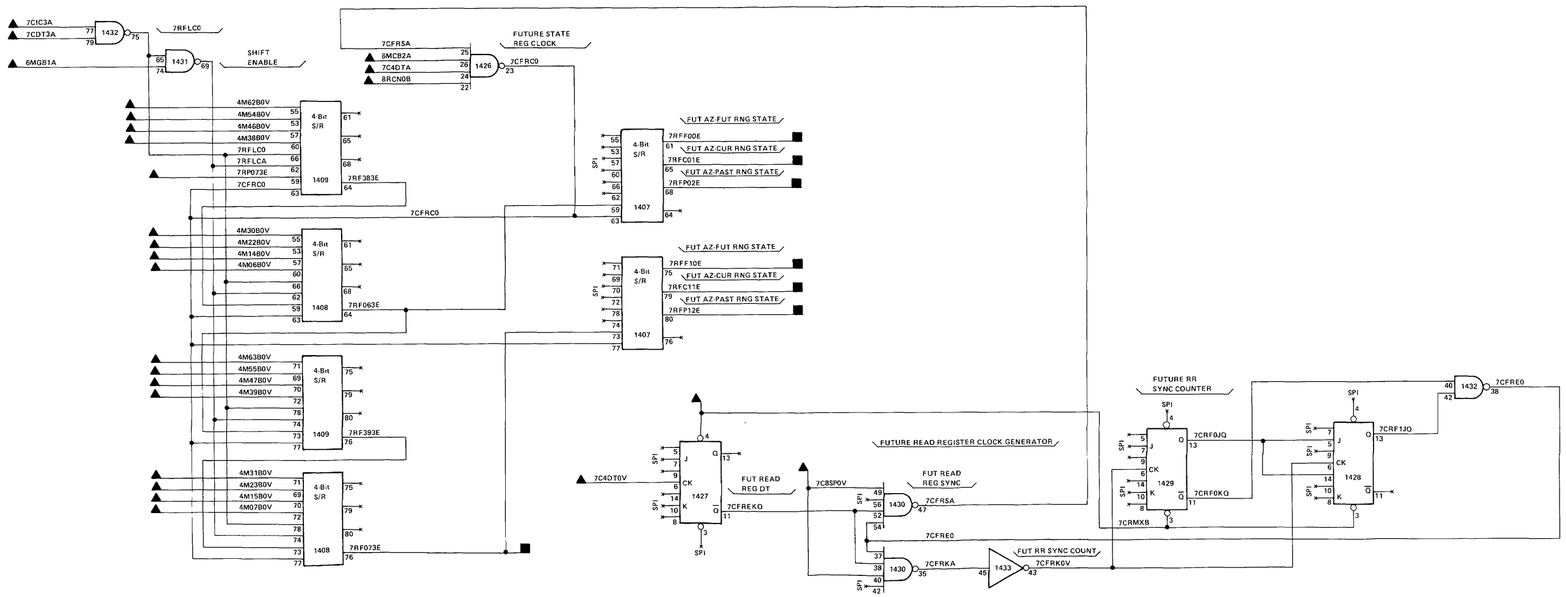
INPUT SIGNAL	SOURCE FO-SH
4M06B0V	06401
4M07B0V	06401
4M14B0V	06401
4M15B0V	06401
4M22B0V	06401
4M23B0V	06401
4M30B0V	06401
4M31B0V	06401
4M38B0V	06402
4M39B0V	06402
4M46B0V	06402
4M47B0V	06402
4M54B0V	06402
4M55B0V	06402
4M62B0V	06402
4M63B0V	06402
6MCB2A	05600
6MGB1A	27201
7CDT3A	01600
7CIC3A	01800
7CRMXB	01501
7C4DTA	01600
7C4DT0V	01600
7C8SP0V	01501
7RP073E	04500
8RCN0B	15001
8RCN0B	27201

OUTPUT SIGNAL	DESTINATION FC-SH
7RFC01E	04800
7RFC11E	04700, 04800
7RFF00E	04800
7RFF10E	04700, 04800
7RFP02E	04800
7RFP12E	04700, 04800
7RF073E	04500, 05003



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

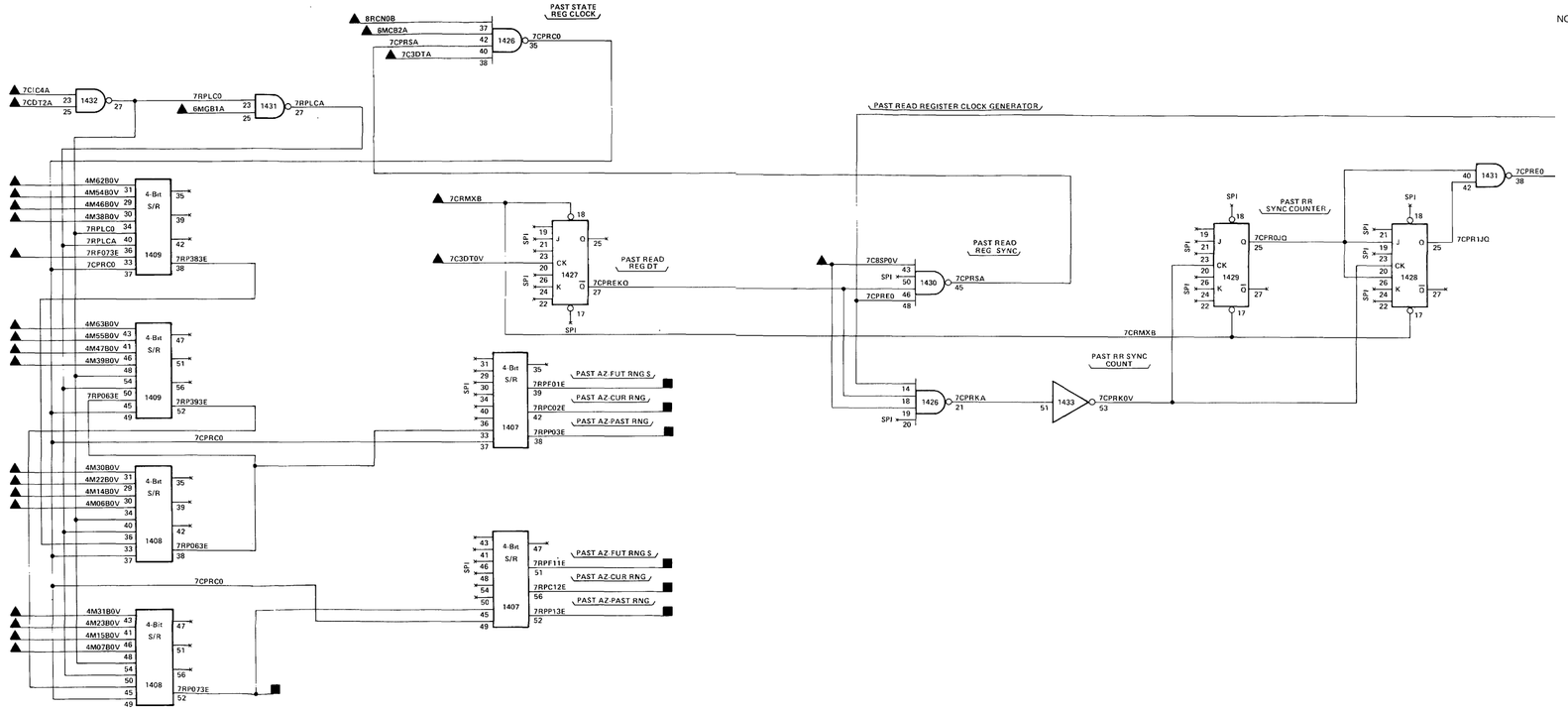
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

MS200622

FO-44. ACM Future State Register Logic Diagram

INPUT SIGNAL	SOURCE FO-SH
4M06B0V	06401
4M07B0V	06401
4M14B0V	06401
4M15B0V	06401
4M22B0V	06401
4M23B0V	06401
4M30B0V	06401
4M31B0V	06401
4M38B0V	06402
4M39B0V	06402
4M46B0V	06402
4M47B0V	06402
4M54B0V	06402
4M55B0V	06402
4M62B0V	06402
4M63B0V	06402
6MCB2A	05600
6MGB1A	05600
6MGB1A	27201
7CDT2A	01600
7C1C4A	01800
7CRMXB	01501
7C3DTA	01600
7C3DTOV	01600
7C8SP0V	01501
7RF073E	04400
8RCN0B	15001
8RCN0B	27201

OUTPUT SIGNAL	DESTINATION FO-SH
7RPC02E	04800
7RPC12E	04700, 04800
7RPF01E	04800
7RPF11E	04700, 04800
7RPP03E	04800
7RPP13E	04700, 04800
7RP073E	04400



FO-45. ACM Past State Register Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INPUT FROM ANOTHER FIGURE
   - △ INPUT FROM SAME FIGURE
   - ■ OUTPUT TO ANOTHER FIGURE
   - □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   - ◪ OUTPUT TO SAME FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.
    - B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

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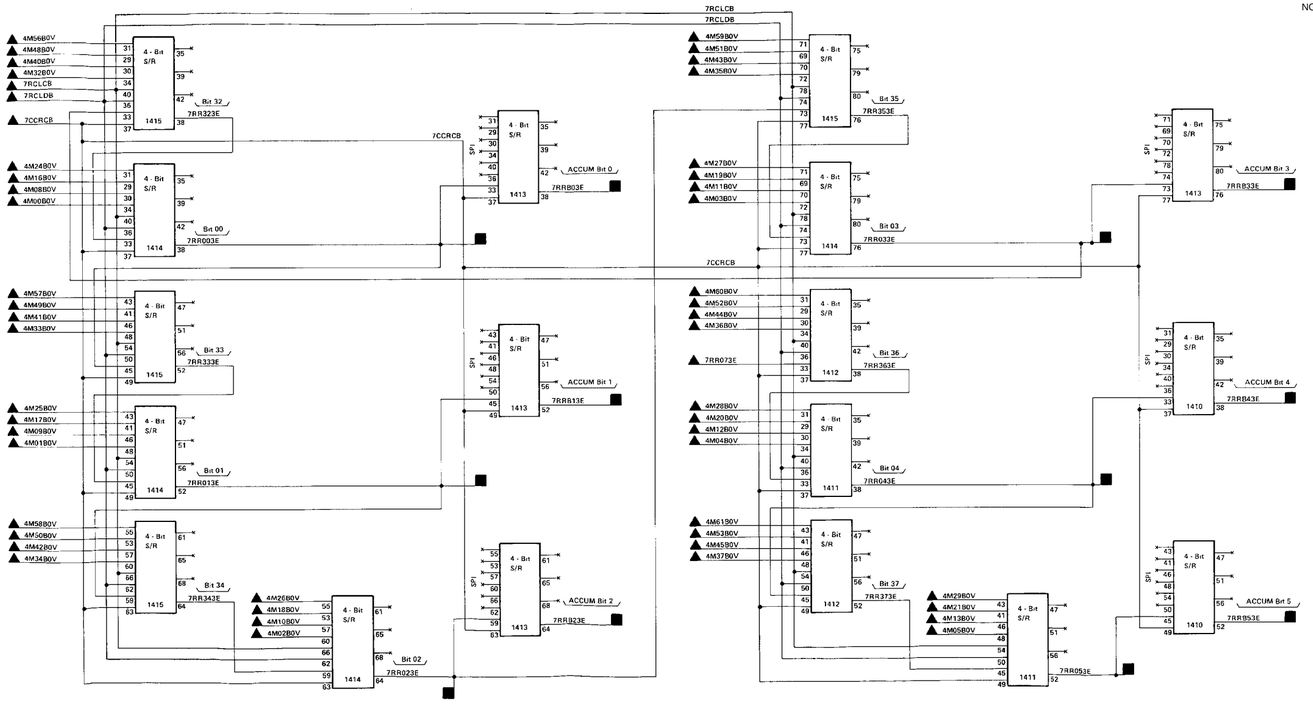
INPUT		INPUT		INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	SOURCE FO-SH	SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
4M00B0V	06401	4M29B0V	06401	4M60B0V	06402	7RRB03E	03700, 04100
4M00B0V	26802	4M29B0V	26802	4M60B0V	26802	7RRB13E	03700, 04100
4M01B0V	06401	4M32B0V	06402	4M61B0V	06402	7RRB23E	03700, 04100
4M01B0V	26802	4M32B0V	26802	4M61B0V	26802	7RRB33E	03700, 04100
4M02B0V	06401	4M33B0V	06402	7CCRCB	04300	7RRB43E	03700, 04100
4M02B0V	26802	4M33B0V	26802	7RCLCB	04300	7RRB53E	03700, 04100
4M03B0V	06401	4M34B0V	06402	7RCLDB	04300	7RR003E	05102
4M03B0V	26802	4M34B0V	26802	7RR073E	04300	7RR013E	05102
4M04B0V	06401	4M35B0V	06402			7RR023E	05102
4M04B0V	26802	4M35B0V	26802			7RR033E	05003, 05102
4M05B0V	06401	4M36B0V	06402			7RR043E	05102
4M05B0V	26802	4M36B0V	26802			7RR053E	04300, 05102
4M08B0V	06401	4M37B0V	06402				
4M08B0V	26802	4M37B0V	26802				
4M09B0V	06401	4M40B0V	06402				
4M09B0V	26802	4M40B0V	26802				
4M10B0V	06401	4M41B0V	06402				
4M10B0V	26802	4M41B0V	26802				
4M11B0V	06401	4M42B0V	06402				
4M11B0V	26802	4M42B0V	26802				
4M12B0V	06401	4M43B0V	06402				
4M12B0V	26802	4M43B0V	26802				
4M13B0V	06401	4M44B0V	06402				
4M13B0V	26802	4M44B0V	26802				
4M16B0V	06401	4M45B0V	06402				
4M16B0V	26802	4M45B0V	26802				
4M17B0V	06401	4M48B0V	06402				
4M17B0V	26802	4M48B0V	26802				
4M18B0V	06401	4M49B0V	06402				
4M18B0V	26802	4M49B0V	26802				
4M19B0V	06401	4M50B0V	06402				
4M19B0V	26802	4M50B0V	26802				
4M20B0V	06401	4M51B0V	06402				
4M20B0V	26802	4M51B0V	26802				
4M21B0V	06401	4M52B0V	06402				
4M21B0V	26802	4M52B0V	26802				
4M24B0V	06401	4M53B0V	06402				
4M24B0V	26802	4M53B0V	26802				
4M25B0V	06401	4M56B0V	06402				
4M25B0V	26802	4M56B0V	26802				
4M26B0V	06401	4M57B0V	06402				
4M26B0V	26802	4M57B0V	26802				
4M27B0V	06401	4M58B0V	06402				
4M27B0V	26802	4M58B0V	26802				
4M28B0V	06401	4M59B0V	06402				
4M28B0V	26802	4M59B0V	26802				



FO-46. ACM Read Register Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

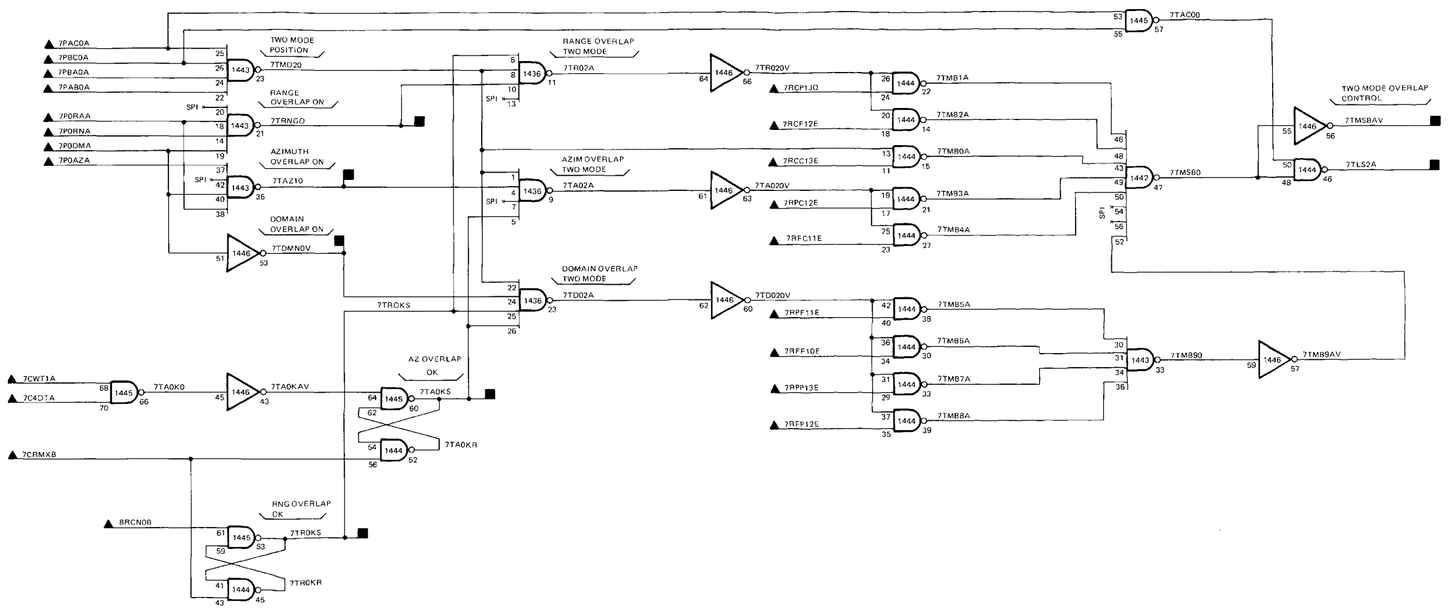
    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

MS200624

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
7CRMXB	01501	7TAZ1D	04800
7CWT1A	01700	7TAOKS	04800
7C4DTA	01600	7TDMNOV	04800
7PAB0A	32102	7TLS2A	04900, 05002
7PAC0A	32102	7TMSBAV	04900, 05002
7PBA0A	32102	7TRNGO	04800
7PBC0A	32102	7TROKS	04800
7PDAZA	26802		
7PDDMA	26802		
7PDRAA	26802		
7PDRNA	26802		
7RCC13E	04300		
7RCF12E	04300		
7RCP1JQ	04300		
7RFC11E	04400		
7RFF10E	04400		
7RFP12E	04400		
7RPC12E	04500		
7RPF11E	04500		
7RPP13E	04500		
BRCN0B	15001		
BRCN0B	27201		



FO-47. ACM Two-Mode Overlap Controller Logic Diagram.

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

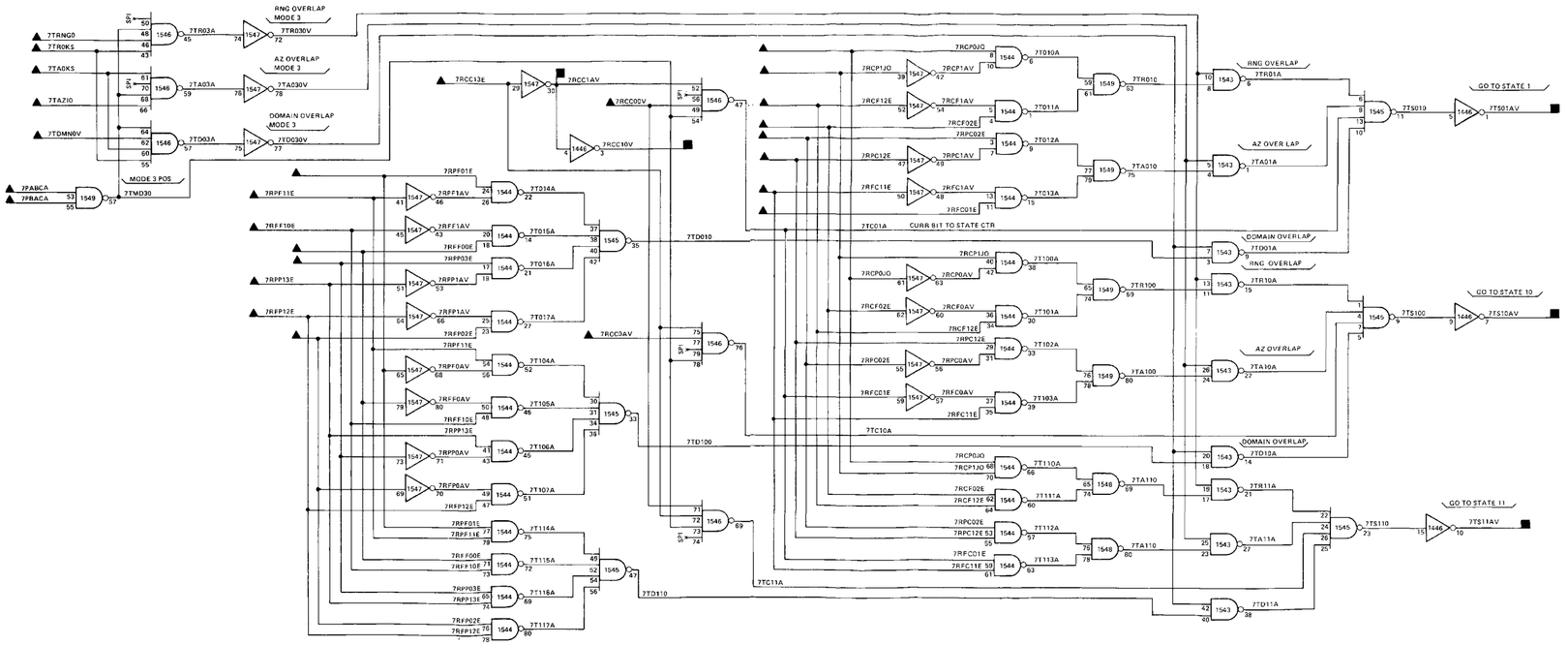
    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT	
SIGNAL	SOURCE FO-SH
7PABCA	32102
7PBACA	32102
7RCC0AV	03900
7RCC0AV	30401
7RCC00V	03900
7RCC13E	04300
7RCF02E	04300
7RCF12E	04300
7RCP0JQ	04300
7RCP1JQ	04300
7RFC01E	04400
7RFC11E	04400
7RFF00E	04400
7RFF10E	04400
7RFP02E	04400
7RFP12E	04400
7RPC02E	04500
7RPC12E	04500
7RPF01E	04500
7RPF11E	04500
7RPP03E	04500
7RPP13E	04500
7TAZ10	04700
7TADKS	04700
7TDMN0V	04700
7TRNG0	04700
7TROKS	04700

OUTPUT	
SIGNAL	DESTINATION FO-SH
7RCC1AV	04300
7RCC10V	03900, 04000
7TS01AV	04900, 05002
7TS10AV	04900, 05002
7TS11AV	04900, 05002



FO-48. ACM Three-Mode Overlap Controller Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

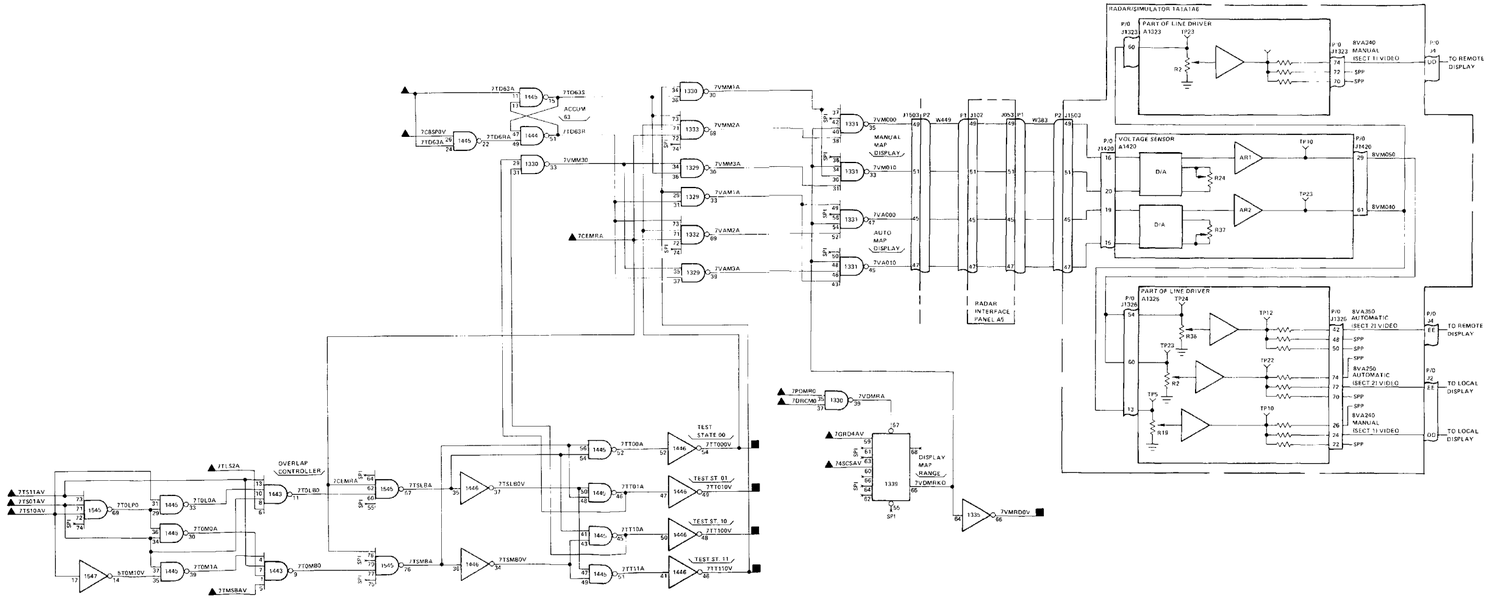
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
7CEMRA	01700	7TT000V	03900
7C85P0V	01501	7TT010V	03900
7DRCM0	02500	7TT100V	03900
7GRD4AV	01502	7TT110V	03900
7P0MR0	32101	7VMR00V	14202, 26802, 30402
7TD63A	04100		
7TLS2A	04700		
7TM58AV	04700		
7TS01AV	04800		
7TS10AV	04800		
7TS11AV	04800		
74SC5AV	05101		



FO-49. ACM Overlap Controller Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

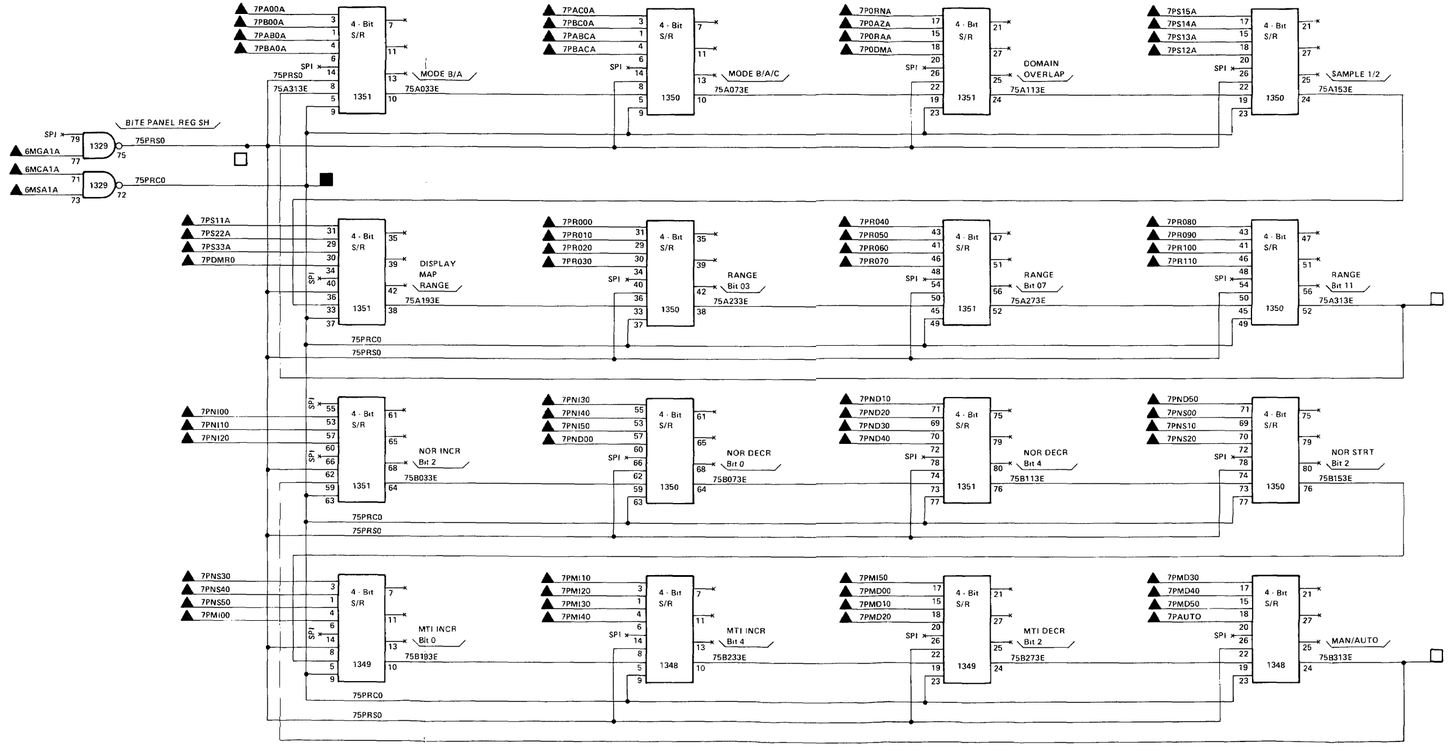
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT SIGNAL	SOURCE FO-SH	INPUT SIGNAL	SOURCE FO-SH
6MCA1A	05600	7PNS30	32101
6MCA1A	26803	7PNS40	32101
6MCA1A	27901	7PNS50	32102
6MGA1A	05600	7PR000	32101
6MGA1A	26803	7PR010	32101
6MGA1A	27201	7PR020	32101
6MSA1A	05600	7PR030	32101
6MSA1A	26803	7PR040	32101
6MSA1A	27201	7PR050	32101
7PABCA	32102	7PR060	32101
7PABOA	32102	7PR070	32101
7PACOA	32102	7PR080	32101
7PAUTO	32102	7PR090	32101
7PA00A	32102	7PR100	32101
7PBACA	32102	7PR110	32101
7PBAOA	32102	7PS11A	32101
7PBCOA	32102	7PS12A	32101
7PB00A	32102	7PS13A	32101
7PDMR0	32101	7PS14A	32101
7PMD00	32102	7PS15A	32101
7PMD10	32102	7PS22A	32101
7PMD20	32102	7PS33A	32101
7PMD30	32102	7POAZA	26802
7PMD40	32102	7PODMA	26802
7PMD50	32102	7PORAA	26802
7PM100	32102	7PORNA	26802
7PMI10	32102		
7PMI20	32102		
7PMI30	32102		
7PMI40	32102		
7PMI50	32102		
7PND00	32101		
7PND10	32101		
7PND20	32101		
7PND30	32101		
7PND40	32101		
7PND50	32101		
7PN100	32101		
7PNI10	32101		
7PNI20	32101		
7PNI30	32101		
7PNI40	32101		
7PNI50	32101		
7PNS00	32101		
7PNS10	32101		
7PNS20	32101		



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

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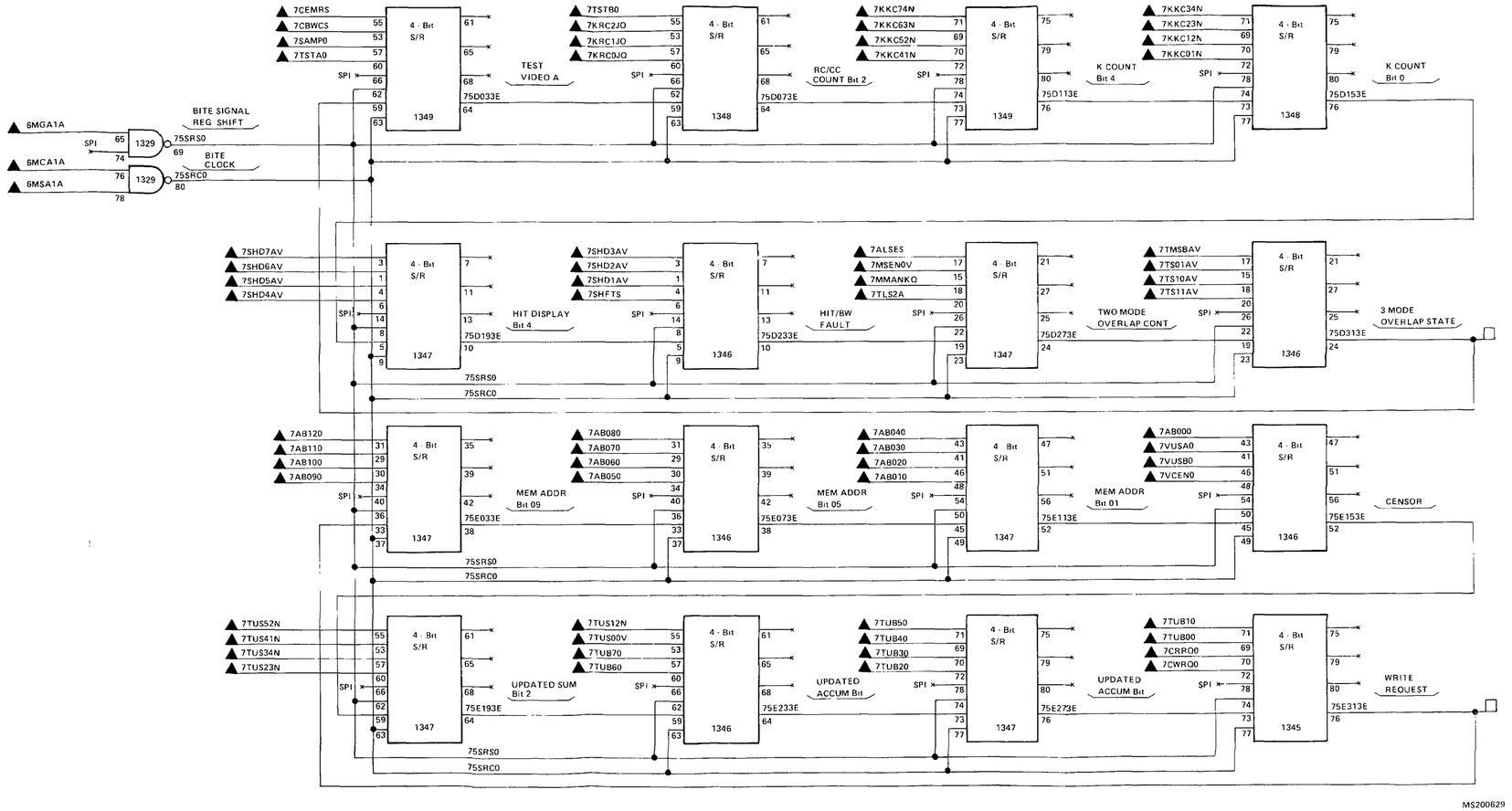
FO-50. ACM Signal and Panel BITE Registers Logic Diagram (Sheet 1 of 3)

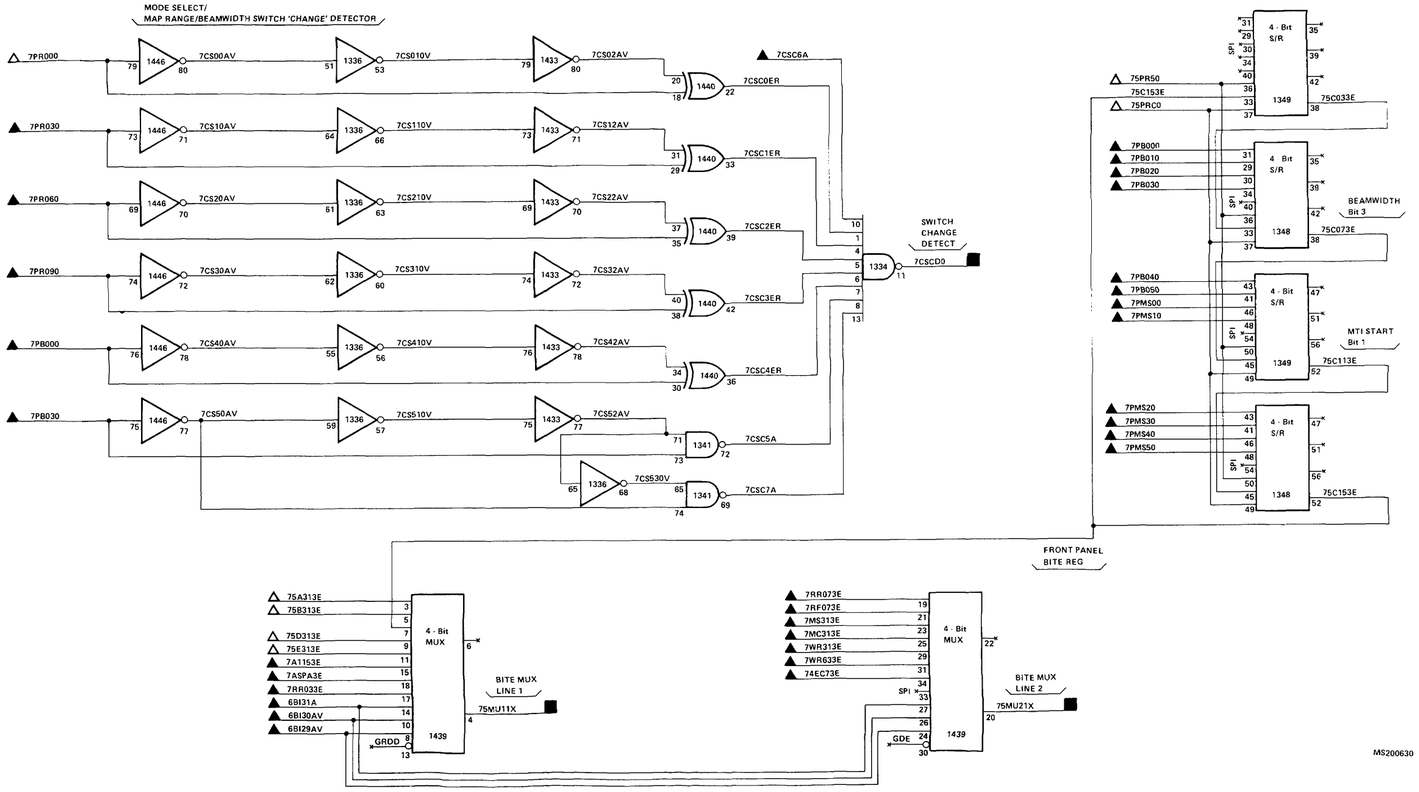
INPUT SIGNAL	INPUT SOURCE FO-SH	INPUT SIGNAL	INPUT SOURCE FO-SH
6MCA1A	05600	7KKC63N	02200
6MCA1A	26803	7KKC74N	02200
6MCA1A	27901	7KRC0JQ	02100
6MGA1A	05600	7KRC1JQ	02100
6MGA1A	26803	7KRC2JQ	02100
6MGA1A	27201	7MMANKQ	02400
6MSA1A	05600	7MSEN0V	02400
6MSA1A	26803	7SAMP0	03500
6MSA1A	27201	7SHD1AV	01502
7AB000	03100	7SHD2AV	01502
7AB000	26802	7SHD3AV	01502
7AB010	03100	7SHD4AV	01502
7AB010	26802	7SHD5AV	01502
7AB020	03100	7SHD6AV	01502
7AB020	26802	7SHD7AV	01502
7AB030	03100	7SHFTS	01501
7AB030	26802	7TLS2A	04700
7AB040	03100	7TMSBAV	04700
7AB040	26802	7TSTA0	03900
7AB050	03100	7TSTB0	03900
7AB050	26802	7TS01AV	04800
7AB060	03100	7TS10AV	04800
7AB060	26802	7TS11AV	04800
7AB070	03100	7TUB00	04100
7AB070	26802	7TUB10	04100
7AB080	03100	7TUB20	04100
7AB080	26802	7TUB30	04100
7AB090	03100	7TUB40	04100
7AB090	26802	7TUB50	04100
7AB100	03100	7TUB60	04000
7AB100	26802	7TUB70	04000
7AB110	03100	7TUS00V	03700
7AB110	26802	7TUS12N	03700
7AB120	03100	7TUS23N	03700
7AB120	26802	7TUS34N	03700
7ALSES	02900	7TUS41N	03700
7CBWCS	01800	7TUS52N	03700
7CEMRS	01700	7VCEN0	03900
7CRRQ0	01700	7VUSA0	03900
7CWRQ0	01700	7VUSB0	03900
7KKC01N	02200		
7KKC12N	02200		
7KKC23N	02200		
7KKC34N	02200		
7KKC41N	02200		
7KKC52N	02200		



FO-50. ACM Signal and Panel BITE Registers Logic Diagram (Sheet 2 of 3)

INPUT	
SIGNAL	SOURCE FO-SH
6BI29AV	05300
6BI29AV	26803
6BI29AV	27201
6BI30AV	05300
6BI30AV	26803
6BI30AV	27201
6BI31A	05300
6BI31A	26802
6BI31A	26803
6BI31A	27901
7ASPA3E	02800
7A1153E	03000
7CSC6A	03900
7MC313E	02300
7MS313E	02300
7PB000	32101
7PB010	32101
7PB020	32101
7PB030	32101
7PB040	26802
7PB040	32000
7PB040	32101
7PB050	32000
7PB050	32101
7PMS00	32102
7PMS10	32102
7PMS20	32102
7PMS30	32102
7PMS40	32102
7PMS50	32102
7PR030	32101
7PR060	32101
7PR090	32101
7RF073E	04400
7RR033E	04600
7RR073E	04300
7WR313E	04200
7WR313E	26802
7WR633E	04200
7WR633E	26802
74EC73E	05101

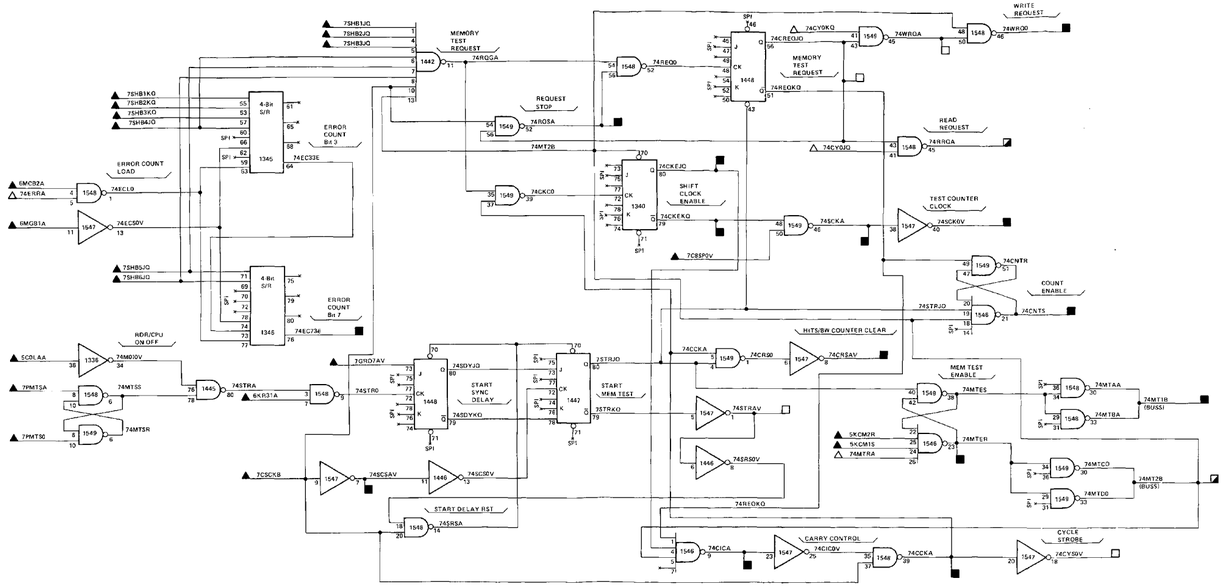
OUTPUT	
SIGNAL	DESTINATION FO-SH
7CSCD0	01501
75MU11X	05400
75MU21X	05400



FO-50. ACM Signal and Panel BITE Registers Logic Diagram (Sheet 3 of 3)

INPUT SIGNAL	SOURCE FO-SH
5COLAA	13400
5COLAA	26802
5COLAA	27001
5KCM1S	13400
5KCM2R	28201
6KR31A	05702
6MCB2A	05600
6MGB1A	05600
6MGB1A	27201
7CSCKB	01501
7CBSP0V	01501
7GRD7AV	01502
7PMTSA	26802
7PMTSA	31901
7PMTS0	26802
7PMTS0	31901
7SHB1JQ	03400
7SHB1KQ	03400
7SHB2JQ	03400
7SHB2KQ	03400
7SHB3JQ	03400
7SHB3KQ	03400
7SHB4JQ	03400
7SHB5JQ	03400
7SHB6JQ	03400

OUTPUT SIGNAL	DESTINATION FO-SH
74CCKA	01800
74C1CA	01800
74CKEJQ	02600
74CKEKQ	04300
74CNTS	02600
74CRSAV	03400
74EC73E	05003
74MTER	01600, 01700, 01800
74MT1B	01501, 01600, 01700, 01800, 03400, 04200, 13400, 14400, 26802, 26803, 28202
74MT2B	03400, 04200
74RQSA	04300
74RRQA	01700, 04300
74SCKA	01800, 04200
74SCKOV	03400
74SCSAV	03900, 04900
74WRQ0	01700



FO-51. ACM Memory Test Controller Logic Diagram (Sheet 1 of 2)

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

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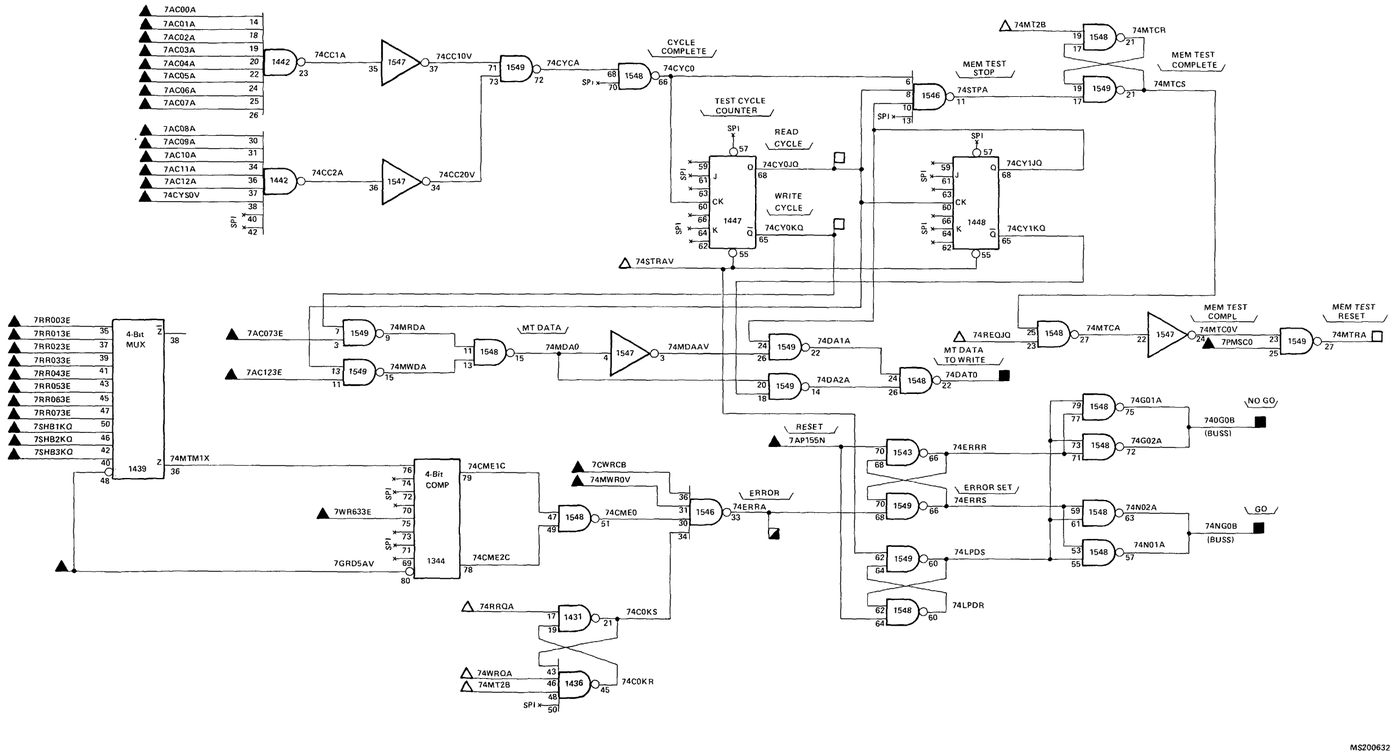
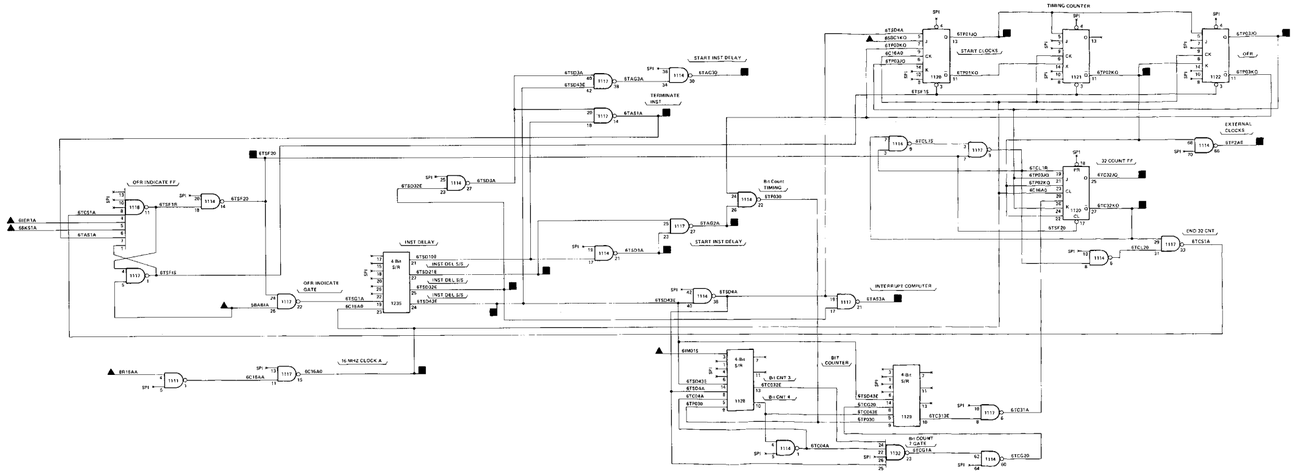
INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
7AC00A	03100	74DAT0	04200
7AC01A	03100	74ERRA	05900
7AC02A	03100	74NG0B	26802, 29201
7AC03A	03100	740G0B	26802, 29201
7AC04A	03100		
7AC05A	03100		
7AC06A	03100		
7AC07A	03100		
7AC073E	02600		
7AC08A	03100		
7AC09A	03100		
7AC10A	03100		
7AC11A	03100		
7AC12A	03100		
7AC123E	02600		
7AP155N	02700		
7CWRCB	04200		
7GRD5AV	01502		
7PMSC0	26802		
7RR003E	31901		
7RR013E	04600		
7RR023E	04600		
7RR033E	04600		
7RR043E	04600		
7RR053E	04600		
7RR063E	04300		
7RR073E	04300		
7SHB1KQ	03400		
7SHB2KQ	03400		
7SHB3KQ	03400		
7WR633E	04200		
7WR633E	26802		
74MWR0V	04200		



FO-51. ACM Memory Test Controller Logic Diagram (Sheet 2 of 2)

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
5BABIA	13302	6CT6A0	05900
61ER1A	05500	6TA52A	05300
61ER1A	26803	6TA630	05500
61ER1A	28201	6TA51A	05500
61M015	05500	6TA53A	13101, 26802, 28202
6SBC1KG	05900	6TC32JG	05400, 05600
6SKS1A	05902	6TC32KG	05600
8R16AA	14300	6TP01JG	05400, 05900
8R16AA	26802	6TP07KG	05500
8R16AA	27201	6TP03JG	05300
		6TP2A0	05600
		6TS01A	05500
		6TSS21E	05500
		6TS032E	05800
		6TS945E	05800
		6TSF20	05900



FO-52. BITE Start/Stop Control Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

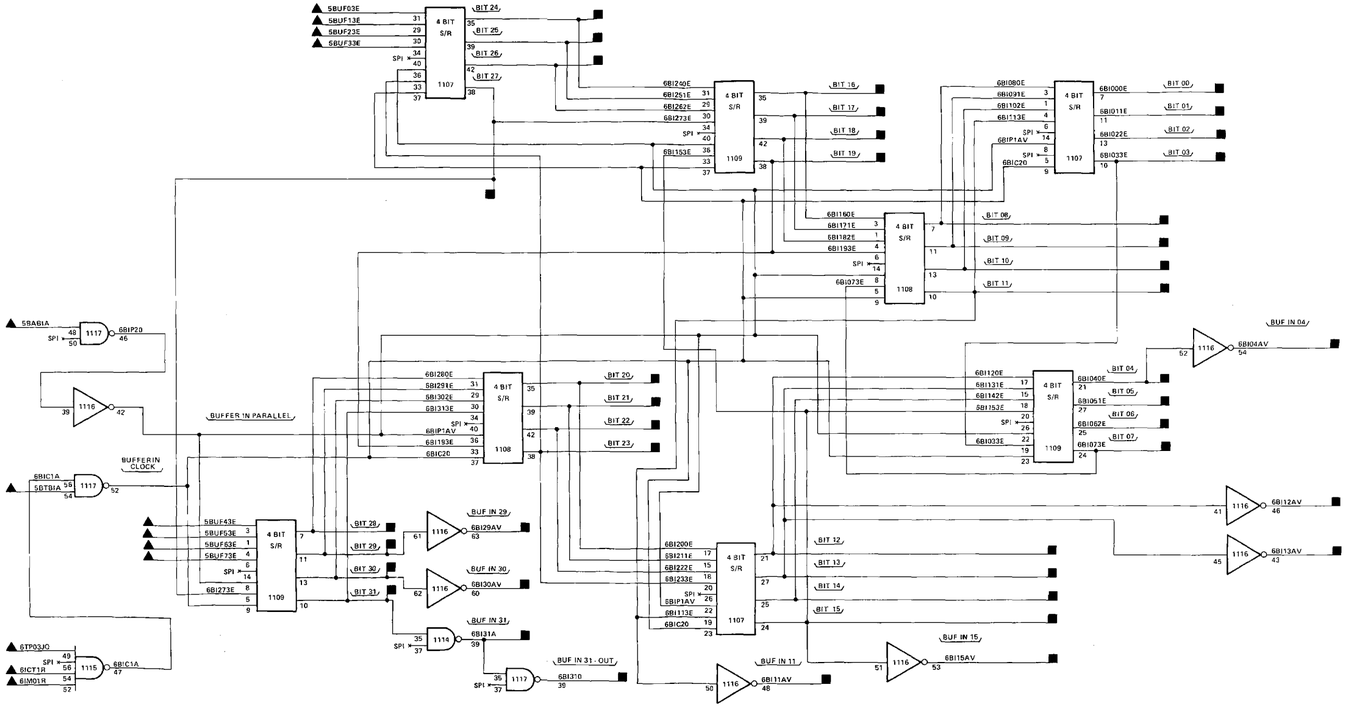
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
5BABIA	13302	6BI000E	26801
5BTBIA	13302	6BI011E	05500, 26801
5BTBIA	26802	6BI022E	05500, 26801
5BTBIA	28202	6BI033E	05500, 26801
5BUF03E	13301	6BI04AV	05500, 05600
5BUF03E	26803	6BI040E	05600, 05900, 26801
5BUF03E	28201	6BI051E	05600, 05900, 26801
5BUF13E	13301	6BI062E	05900, 26801
5BUF13E	26803	6BI073E	05900, 26801
5BUF13E	28201	6BI080E	05900, 26801
5BUF23E	13301	6BI091E	05900, 26801
5BUF23E	26803	6BI102E	05900, 26801
5BUF23E	28201	6BI11AV	05900, 26802
5BUF33E	13301	6BI113E	26801
5BUF33E	26803	6BI12AV	05900, 26802
5BUF33E	28201	6BI120E	26801
5BUF43E	13301	6BI13AV	05900, 26802
5BUF43E	26803	6BI131E	26801
5BUF43E	28201	6BI142E	05800, 26801
5BUF53E	13301	6BI15AV	05800
5BUF53E	26803	6BI153E	05800, 26801
5BUF53E	28201	6BI160E	05800
5BUF63E	13301	6BI171E	05800
5BUF63E	26803	6BI182E	05800
5BUF63E	28201	6BI193E	05800
5BUF73E	13301	6BI200E	05400, 05800
5BUF73E	26803	6BI211E	05400, 05800
5BUF73E	28201	6BI222E	05400, 05800
6ICT1R	05500	6BI233E	05400, 05800
6IM01R	05500	6BI240E	05800
6TP03JQ	05200	6BI251E	05800
		6BI262E	05800, 20502, 26802, 30701
		6BI273E	05800, 20502, 26802, 30701
		6BI280E	05800, 20502, 26802, 30701
		6BI29AV	05003, 05400, 16500, 26802, 27201
		6BI291E	05800
		6BI30AV	05003, 05400, 16500, 26802, 27201
		6BI302E	05800
		6BI31A	05003, 05400, 12500, 16500, 20502, 26802, 30701
		6BI310	26802
		6BI313E	05500, 05800, 12000, 12100



FO-53. BITE Input Buffer Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

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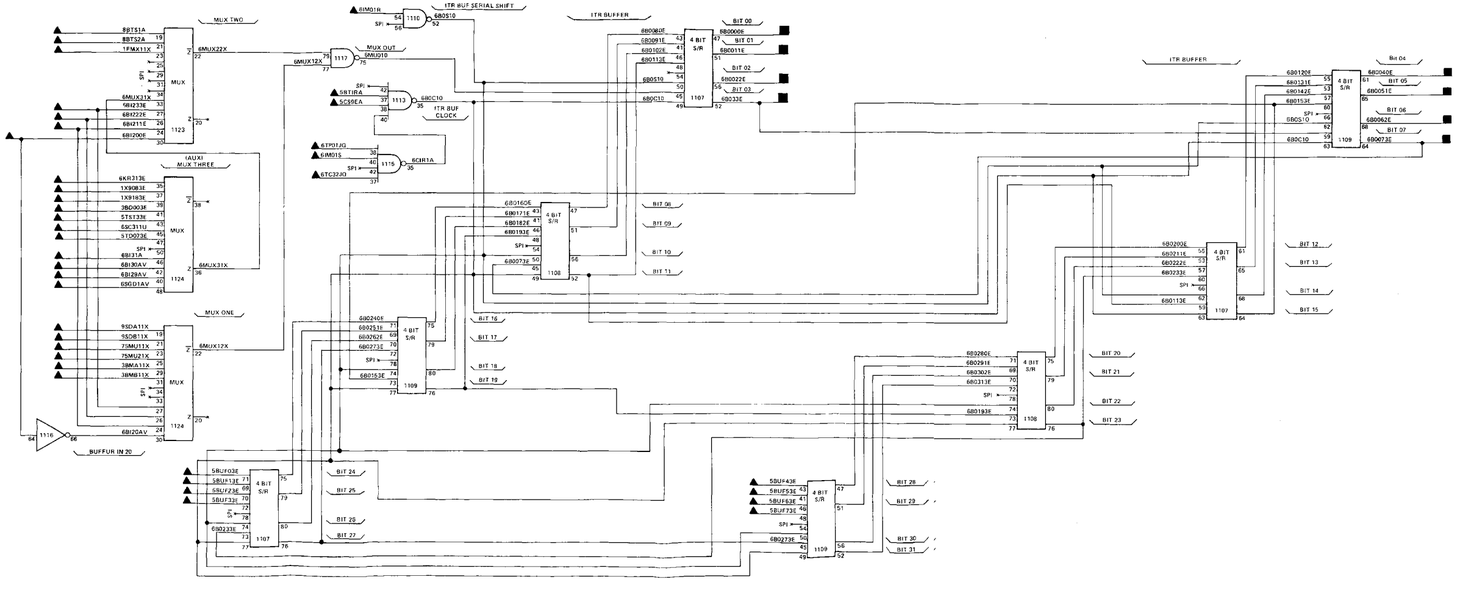
INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
5BA81A	13302	6B1000E	26801
5BT81A	13302	6B1011E	05500, 26801
5BT81A	26802	6B1022E	05500, 26801
5BT81A	28202	6B1033E	05500, 26801
5BUF03E	13301	6B104AV	05500, 05600
5BUF03E	26803	6B1040E	05600, 05900, 26801
5BUF03E	28201	6B1051E	05600, 05900, 26801
5BUF13E	13301	6B1062E	05900, 26801
5BUF13E	26803	6B1073E	05900, 26801
5BUF13E	28201	6B1080E	05900, 26801
5BUF23E	13301	6B1091E	05900, 26801
5BUF23E	26803	6B1102E	05900, 26801
5BUF23E	28201	6B111AV	05900, 26802
5BUF33E	13301	6B1113E	26801
5BUF33E	26803	6B112AV	05900, 26802
5BUF33E	28201	6B1120E	26801
5BUF43E	13301	6B113AV	05900, 26802
5BUF43E	26803	6B1131E	26801
5BUF43E	28201	6B1142E	05800, 26801
5BUF53E	13301	6B115AV	05800
5BUF53E	26803	6B1153E	05800, 26801
5BUF53E	28201	6B1160E	05800
5BUF63E	13301	6B1171E	05800
5BUF63E	26803	6B1182E	05800
5BUF63E	28201	6B1193E	05800
5BUF73E	13301	6B1200E	05400, 05800
5BUF73E	26803	6B1211E	05400, 05800
5BUF73E	28201	6B1222E	05400, 05800
61CT1R	05500	6B1233E	05400, 05800
61M01R	05500	6B1240E	05800
6TP03JG	05200	6B1251E	05800
		6B1262E	05800, 20502, 26802, 30701
		6B1273E	05800, 20502, 26802, 30701
		6B1280E	05800, 20502, 26802, 30701
		6B129AV	05003, 05400, 16500, 26802, 27201
		6B1291E	05800
		6B130AV	05003, 05400, 16500, 26802, 27201
		6B1302E	05800
		6B131A	05003, 05400, 12500, 16500, 20502, 26802, 30701
		6B1310	26802
		6B1313E	05500, 05800, 12000, 12100



FO-54. BITE ITR Buffer Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

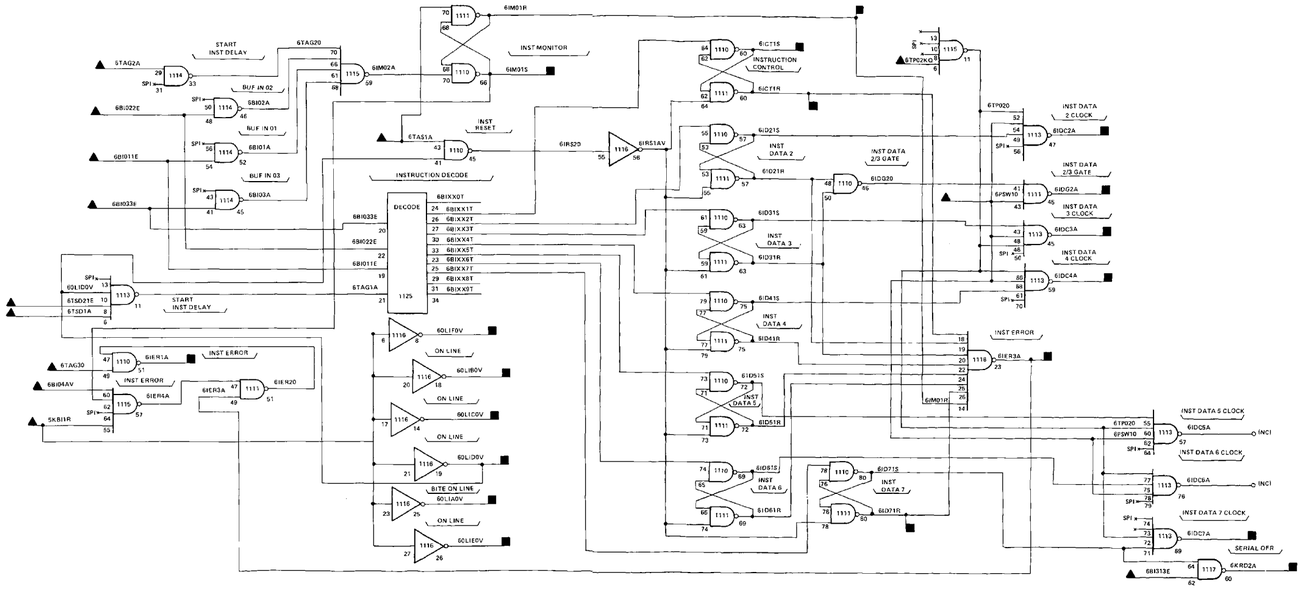
    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT SIGNAL	INPUT SOURCE FO-SH
1FMX11X	16500
1FMX11X	26802
1FMX11X	27201
1X9083E	01400
1X9183E	01400
3BD003E	12500
3BD003E	26802
3BMA11X	12500
3BMA11X	26802
3BMB11X	12500
3BMB11X	26802
5BTIRA	13302
5BTIRA	26802
5BTIRA	28201
5BUF03E	13301
5BUF03E	26803
5BUF03E	28201
5BUF13E	13301
5BUF13E	26803
5BUF13E	28201
5BUF23E	13301
5BUF23E	26803
5BUF23E	28201
5BUF33E	13301
5BUF33E	26803
5BUF33E	28201
5BUF43E	13301
5BUF43E	26803
5BUF43E	28201
5BUF53E	13301
5BUF53E	26803
5BUF53E	28201
5BUF63E	13301
5BUF63E	26803
5BUF63E	28201
5BUF73E	13301
5BUF73E	26803
5BUF73E	28201
5CS9EA	13300
5CS9EA	26802
5CS9EA	28202
5TD073E	26802
5TST33E	07500
5TST33E	26802
6B1200E	05300
6B1211E	05300

INPUT SIGNAL	INPUT SOURCE FO-SH
6B1222E	05300
6B1233E	05300
6B129AV	05300
6B129AV	26803
6B129AV	27201
6B130AV	05300
6B130AV	26803
6B130AV	27201
6B131A	05300
6B131A	26802
6B131A	26803
6B131A	27901
61M01R	05500
61M01S	05500
6KR313E	05702
6SC311U	05800
6SGD1AV	05701
6TC32JQ	05200
6TP01JQ	05200
75MU11X	05003
75MU21X	05003
8BTS1A	18000
8BTS1A	26802
8BTS1A	28401
8BTS2A	18000
8BTS2A	26802
8BTS2A	28401
9SDA11X	20502
9SDA11X	26802
9SDA11X	30701
9SDB11X	20502
9SDB11X	26802
9SDB11X	30701

OUTPUT DESTINATION SIGNAL	DESTINATION FO-SH
6B0000E	13800, 26802, 28202
6B0011E	13800, 26802, 28202
6B0022E	13800, 26802, 28202
6B0033E	13800, 26802, 28202
6B0040E	13800, 26802, 28202
6B0051E	13800, 26802, 28202
6B0062E	13800, 26802, 28202
6B0073E	13800, 26802, 28202



FO-55. BITE Instruction Decode Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

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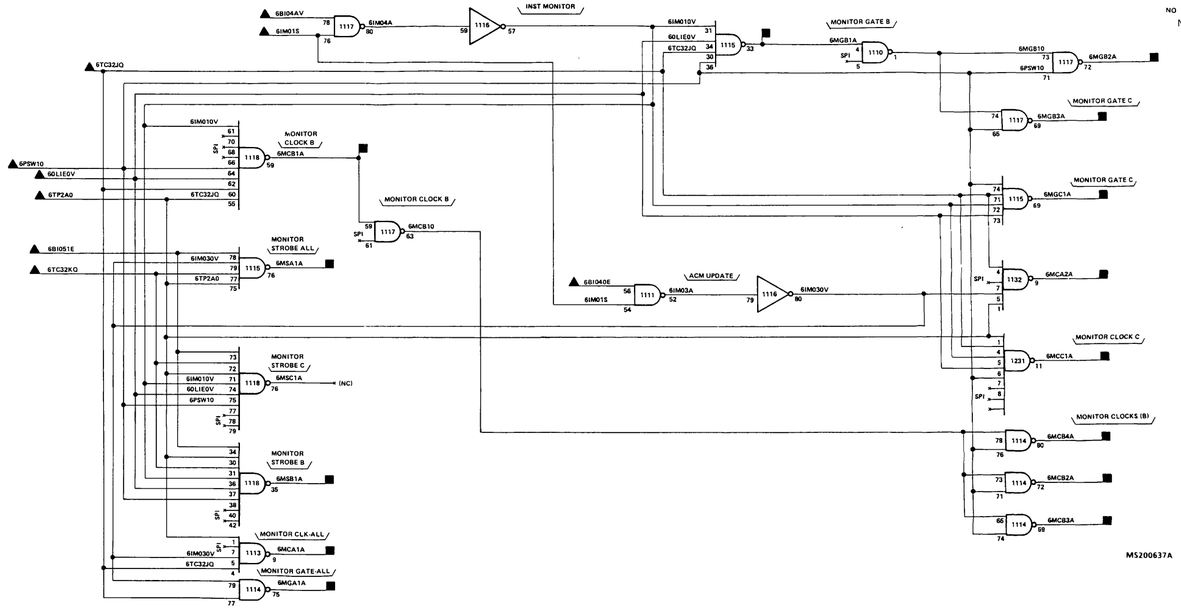
INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
5KB11R	26802	61CT1R	05300, 05900
5KB11R	26803	61CT1S	05800, 05900, 26801
5KB11R	28201	61DC2A	12000, 12300, 26802
6BI011E	05300	61DC3A	12000, 12100, 12300, 26802
6BI022E	05300	61DC4A	12000, 12300, 26802
6BI033E	05300	61DC7A	05701
6BI04AV	05300	61DG2A	07701, 12100, 26802
6BI313E	05300	61D71R	05701
6BI313E	26802	61ER1A	05200, 13500, 26802, 28201
6PSW10	26803	61ER3A	26801
6PSW10	27902	61M01R	05300, 05400
6PSW10	30702	61M01S	05200, 05400, 05600, 26801
6TAG2A	05200	6KRD2A	05701
6TAG30	05200	6OLIAOV	05701
6TAS1A	05200	6OLIBOV	05701, 05702
6TP02K0	05200	6OLICOV	05702
6TSD1A	05200	6OLIDOV	05702, 05900
6TSD21E	05200	6OLIEOV	05600, 05900
		6OLIFOV	05900, 20501, 26802



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

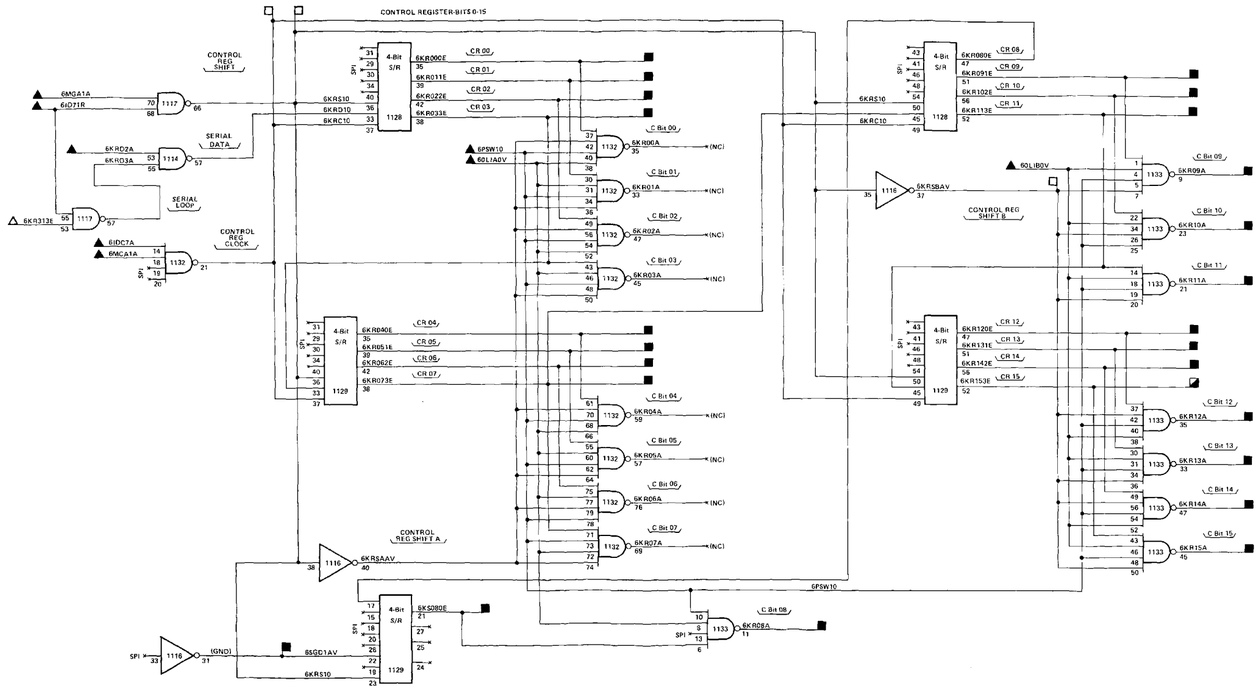
    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

Change 1 FO-56. BITE Monitor Timing Logic Diagram

INPUT SIGNAL	SOURCE FO-SH
6B104AV	05300
6B104DE	05300
6B105TE	05300
6IM01S	05500
6PSW1D	26803
6PSW1D	27902
6PSW1D	30702
6TC32JQ	05200
6TC32KQ	05200
6TP2AD	05200
60LIEDV	05500

OUTPUT SIGNAL	DESTINATION FO-SH
6MCA1A	01400, 05001, 05002, 05701, 12000, 12100, 16500, 20502, 26802, 30701
6MCA2A	26802
6MCB1A	05800, 08100, 08300, 11400, 11600, 11700, 11800, 12500, 13900, 14700, 16500, 26802, 26803, 27201
6MCB2A	01501, 01800, 03000, 04200, 04300, 04400, 04500, 05101
6MCB3A	26802
6MCB4A	26802
6MCC1A	18102, 18500, 18600, 20502, 26802, 30701
6MGA1A	01400, 05001, 05002, 05701, 12100, 12300, 16500, 20502, 26802, 27201
6MGB1A	01501, 02500, 02600, 02800, 04200, 04300, 04400, 04500, 05101, 11400, 11500, 11600, 12500, 26802, 27201
6MGB2A	05800, 13900, 14800, 15400, 15500, 26802, 26803
6MGB3A	26802
6MGC1A	18102, 18600, 19600, 26802, 30701
6MSA1A	01400, 05001, 05002, 12000, 12100, 12300, 16500, 20502, 26802, 27201
6MSB1A	12500, 16500, 26802, 27201



FO-57. BITE Control Register-Logic Diagram (Sheet 1 of 2)

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

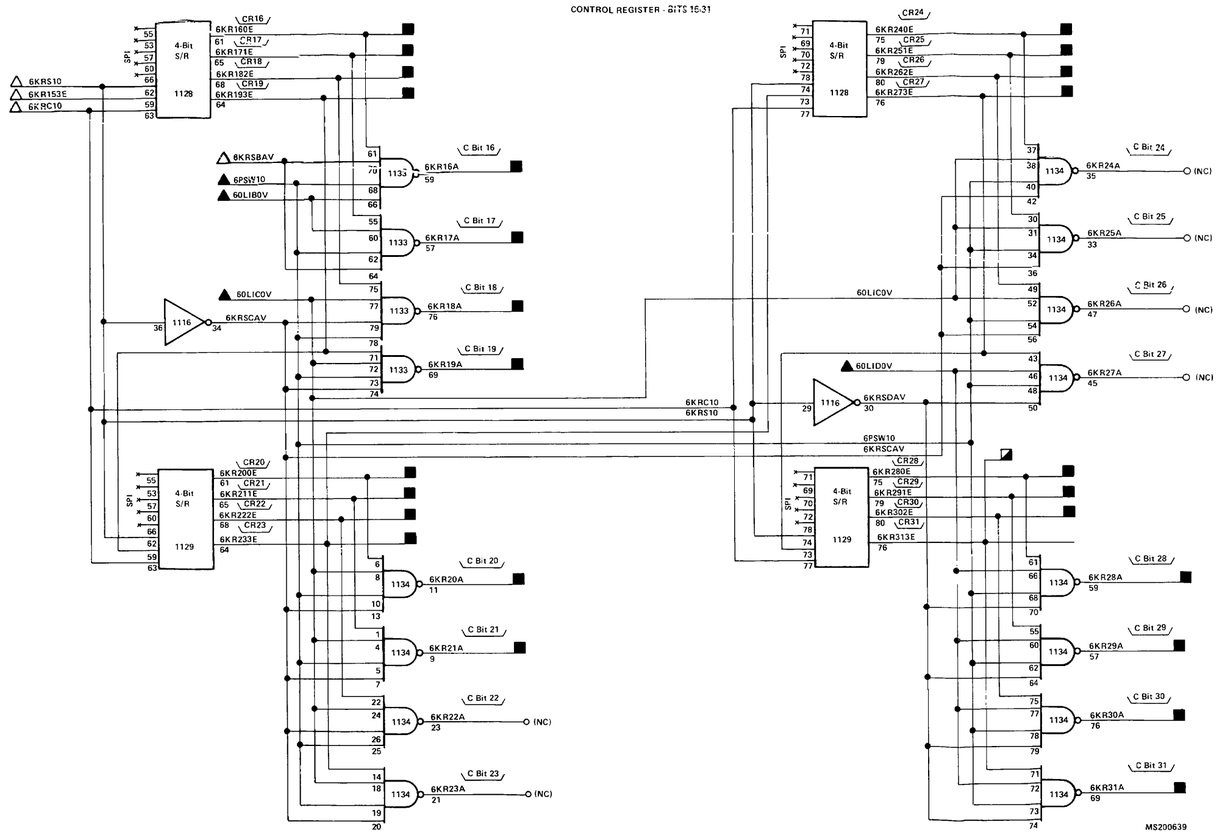
    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

MS20063B

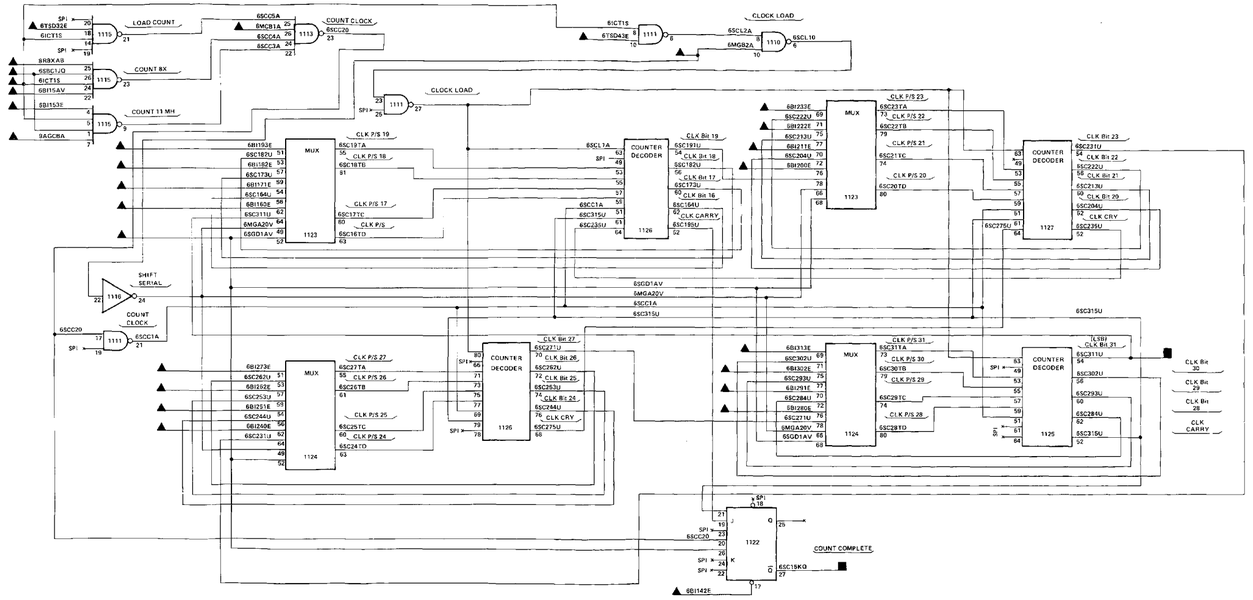
INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
6IDC7A	05500	6KR000E	26801
6ID71R	05500	6KR011E	26801
6KRD2A	05500	6KR022E	26801
6MCA1A	05600	6KR033E	26801
6MCA1A	26803	6KR040E	26801
6MCA1A	27901	6KR051E	26801
6MGA1A	05600	6KR062E	26801
6MGA1A	26803	6KR073E	26801
6MGA1A	27201	6KR08A	26802
6PSW10	26803	6KR09A	26802
6PSW10	27902	6KR091E	26801
6PSW10	30702	6KR10A	26802
6OLIA0V	05500	6KR102E	26801
6OLIB0V	05500	6KR11A	26802
		6KR113E	26801
		6KR12A	26802
		6KR120E	26801
		6KR13A	26802
		6KR131E	26801
		6KR14A	26802
		6KR142E	26801
		6KR15A	26802
		6KR153E	26801
		6KS080E	26801
		6SGD1AV	05400, 05800, 05900



FO-57. BITE Control Register-Logic Diagram (Sheet 2 of 2)

INPUT	
SIGNAL	SOURCE FO-SH
6BI142E	05300
6BI154V	05300
6BI153E	05300
6BI160E	05300
6BI171E	05300
6BI182E	05300
6BI193E	05300
6BI200E	05300
6BI211E	05300
6BI222E	05300
6BI233E	05300
6BI240E	05300
6BI251E	05300
6BI262E	05300
6BI262E	26803
6BI262E	27901
6BI273E	05300
6BI273E	26803
6BI273E	27901
6BI280E	05300
6BI280E	26803
6BI280E	27901
6BI291E	05300
6BI302E	05300
6BI313E	05300
6BI313E	26802
61CT1S	05500
6MCB1A	05600
6MCB1A	27201
6MGB2A	05600
6SBC1JQ	05900
6SD01AV	05701
6TSD32E	05200
6TSD43E	05200
8R8XAB	14900
8R8XAB	28203
9AGCBA	18500

OUTPUT	
SIGNAL	DESTINATION FO-SH
6SC16KQ	05900
6SC311U	05400



FO-58. BITE Program Clock Counter Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

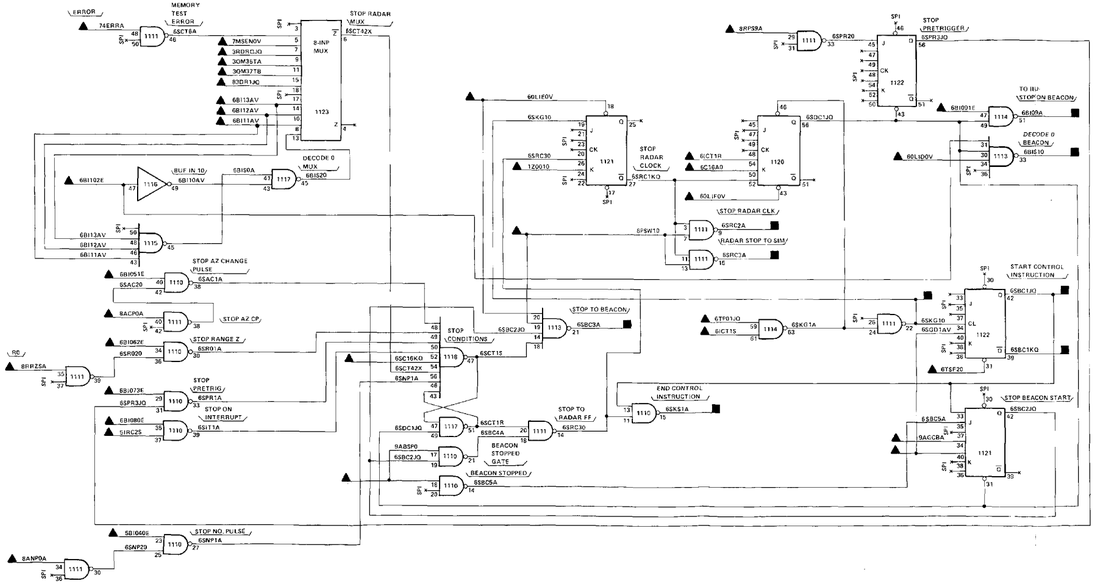
    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT SIGNAL	SOURCE FO-SH
1Z0010	00100
3QM36TA	08502
3QM36TA	26802
3QM37TB	08502
3QM37TB	26802
3RDRQJQ	11100
3RDRQJQ	26802
5IRC2S	26802
5IRC2S	26803
5IRC2S	28202
6B1040E	05300
6B1057E	05300
6B1062E	05300
6B1073E	05300
6B1080E	05300
6B1091E	05300
6B1102E	05300
6B111AV	05300
6B112AV	05300
6B113AV	05300
6C16A0	05200
6ICT1R	05500
6JCT1S	05500
6PSW10	26803
6PSW10	27902
6PSW10	30702
6SC16KQ	05800
6SG01AV	05701
6TP01JQ	05200
6TSF20	05200
6DL1D0V	05300
6DL1E0V	05500
6DL1F0V	05500
6DL1F0V	26803
7MSENDV	02400
74ERRA	05102
8ACP0A	17200
8ACP0A	28401
8ANP0A	17200
8ANP0A	28401
8RPS9A	14401
8RPS9A	26803
8RPS9A	28202
8RRZSA	14401
8RRZSA	28202
83DR1JQ	18300
83DR1JQ	26802
9ABSP0	20501
9ABSP0	26802
9ABSP0	30701
9AGCBA	18500

OUTPUT SIGNAL	DESTINATION FO-SH
6B1S10	20501, 26801, 26802, 30701
6B1D9A	20501, 26802, 30701
6SBC1JQ	05800
6SBC1KQ	05200
6SBC3A	20501, 26801, 26802, 30702
6SKG10	20501, 26802, 30702
6SKS1A	05200
6SRC2A	14300, 14700, 26801, 26802, 26803, 27201
6SRC3A	26801, 26802, 31902



FO-59. BITE Stop Conditions Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

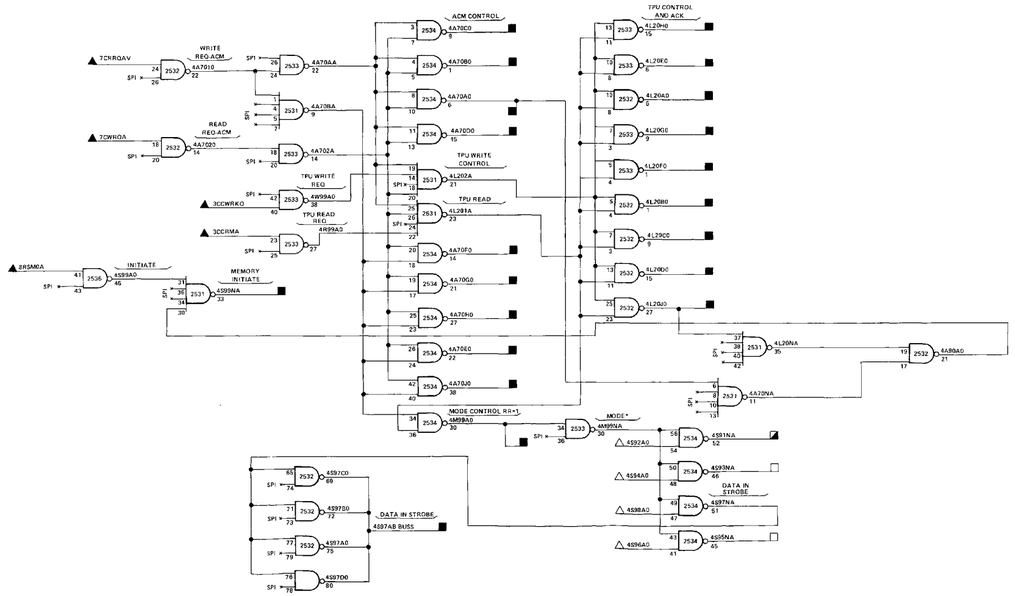
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3CCRMA	09500	4A70A0	06101, 06103, 06300
3CCWRKQ	09500	4A70B0	06300
7CRRQAV	26802	4A70C0	06101
7CWRQA	01700	4A70D0	06101, 06102
7CWRQA	26802	4A70E0	06102, 06103
8RSM0A	14300	4A70F0	06103
8RSM0A	26802	4A70G0	06103, 06104
		4A70H0	06104, 06105
		4A70J0	06105
		4L20A0	09500, 09600, 11309, 11900
		4L20B0	06101, 06300
		4L20C0	06101, 06102
		4L20D0	06102
		4L20E0	06103
		4L20F0	06103, 06104
		4L20G0	06104
		4L20H0	06105
		4L20J0	06101, 06103, 06300
		4M99A0	06500, 26802, 32300
		4S97AB	26802
		4S99NA	26802, 32300



FO-60. BITE Memory Multiplexer Control Unit Logic Diagram (Sheet 1 of 2)

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

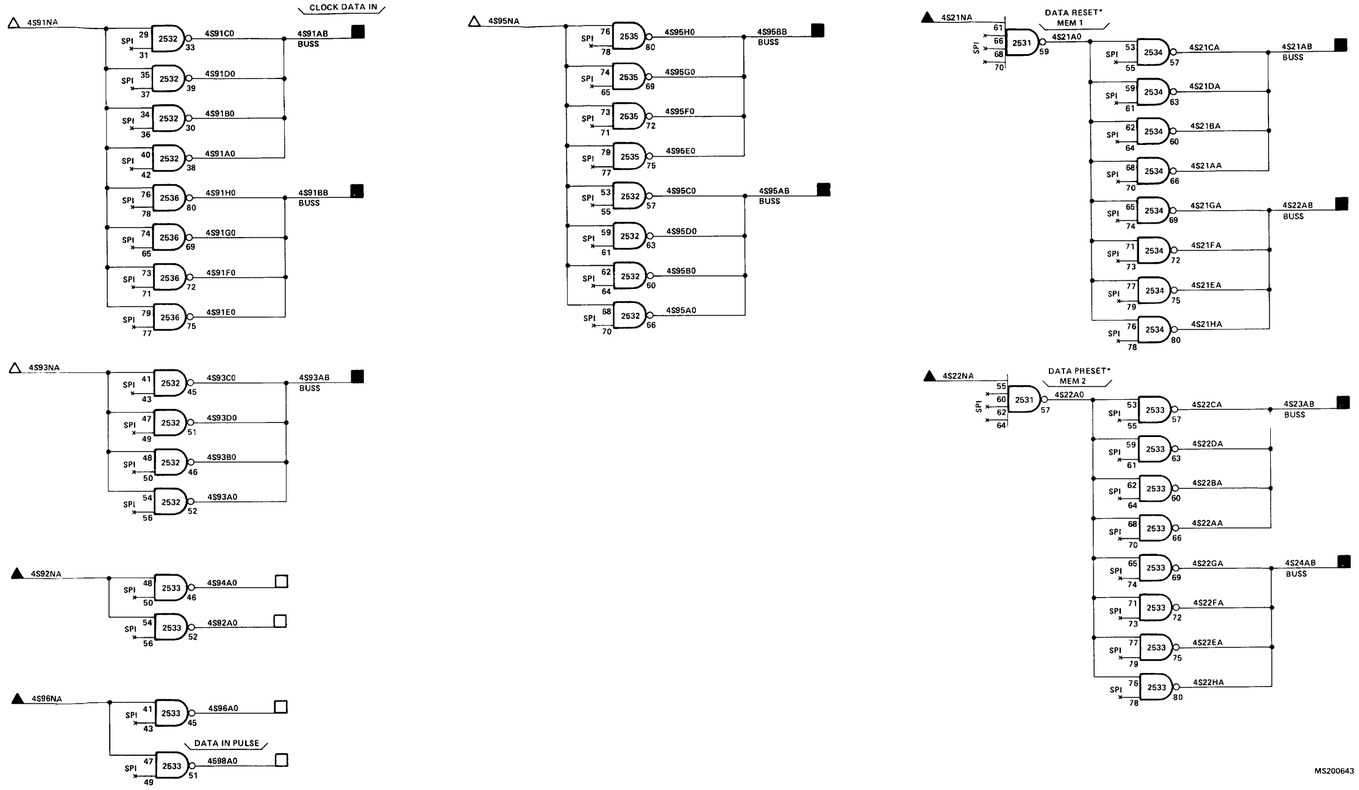
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

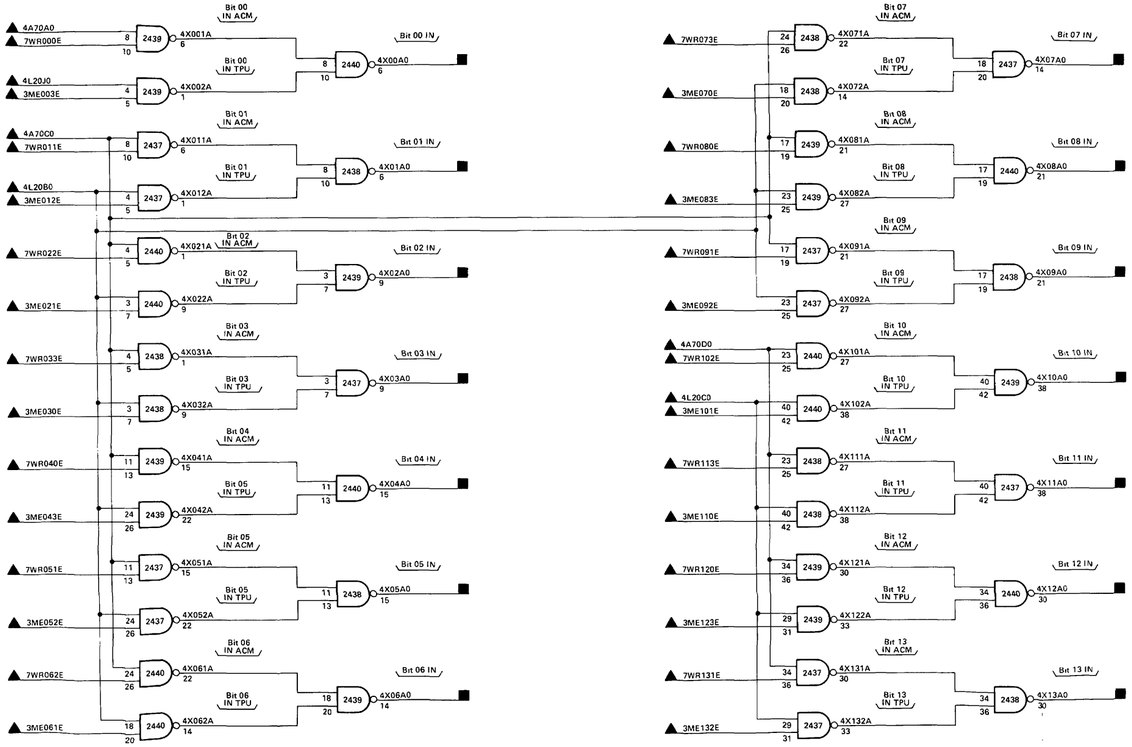
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
4S21NA	26802	4S21AB	26802, 32300
4S22NA	26802	4S22AB	26802, 32300
4S22NA	32602	4S23AB	26802, 32700
4S92NA	26802	4S24AB	26802, 32700
4S92NA	30901	4S91AB	26802, 32300
4S96NA	26802	4S91BB	26802, 32300
4S96NA	32400	4S93AB	26802
		4S95AB	26802
		4S95BB	26802, 32700



FO-60. Memory Multiplexer Control Unit Logic Diagram (Sheet 2 of 2)

INPUT SIGNAL	INPUT SOURCE FO-SH	INPUT SIGNAL	OUTPUT SOURCE FO-SH	OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
3ME003E	11400	7WR080E	04200	4X00A0	06200, 26802, 31001
3ME012E	11400	7WR080E	26802	4X01A0	06200, 26802, 31001
3ME021E	11400	7WR091E	04200	4X02A0	06200, 26802, 31001
3ME030E	11400	7WR091E	26802	4X03A0	06200, 26802, 31001
3ME043E	11400	7WR102E	04200	4X04A0	06200, 26802, 31001
3ME052E	11400	7WR102E	26802	4X05A0	06200, 26802, 31001
3ME061E	11400	7WR113E	04200	4X06A0	06200, 26802, 31001
3ME070E	11400	7WR113E	26802	4X07A0	06200, 26802, 31001
3ME083E	11400	7WR120E	04200	4X08A0	06200, 26802, 31001
3ME092E	11400	7WR120E	26802	4X09A0	06200, 26802, 31001
3ME101E	11400	7WR131E	04200	4X10A0	06200, 26802, 31001
3ME110E	11400	7WR131E	26802	4X11A0	06200, 26802, 31001
3ME123E	11400			4X12A0	06200, 26802, 31001
3ME132E	11400			4X13A0	06200, 26802, 31001
4A70A0	06001				
4A70C0	06001				
4A70D0	06001				
4L20B0	06001				
4L20C0	06001				
4L20J0	06001				
7WR000E	04200				
7WR000E	26802				
7WR011E	04200				
7WR011E	26802				
7WR022E	04200				
7WR022E	26802				
7WR033E	04200				
7WR033E	26802				
7WR040E	04200				
7WR040E	26802				
7WR051E	04200				
7WR051E	26802				
7WR062E	04200				
7WR062E	26802				
7WR073E	04200				
7WR073E	26802				



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

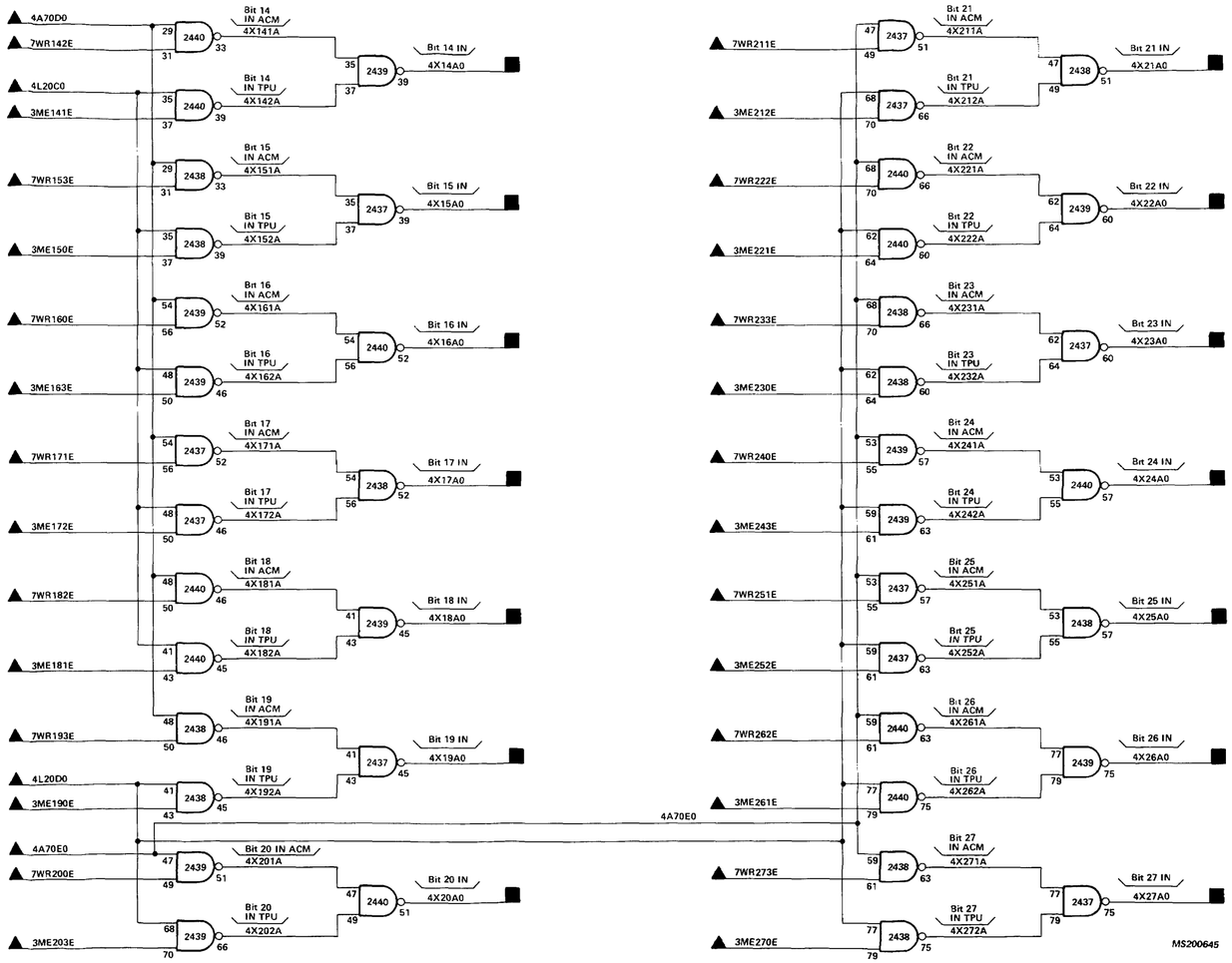
    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

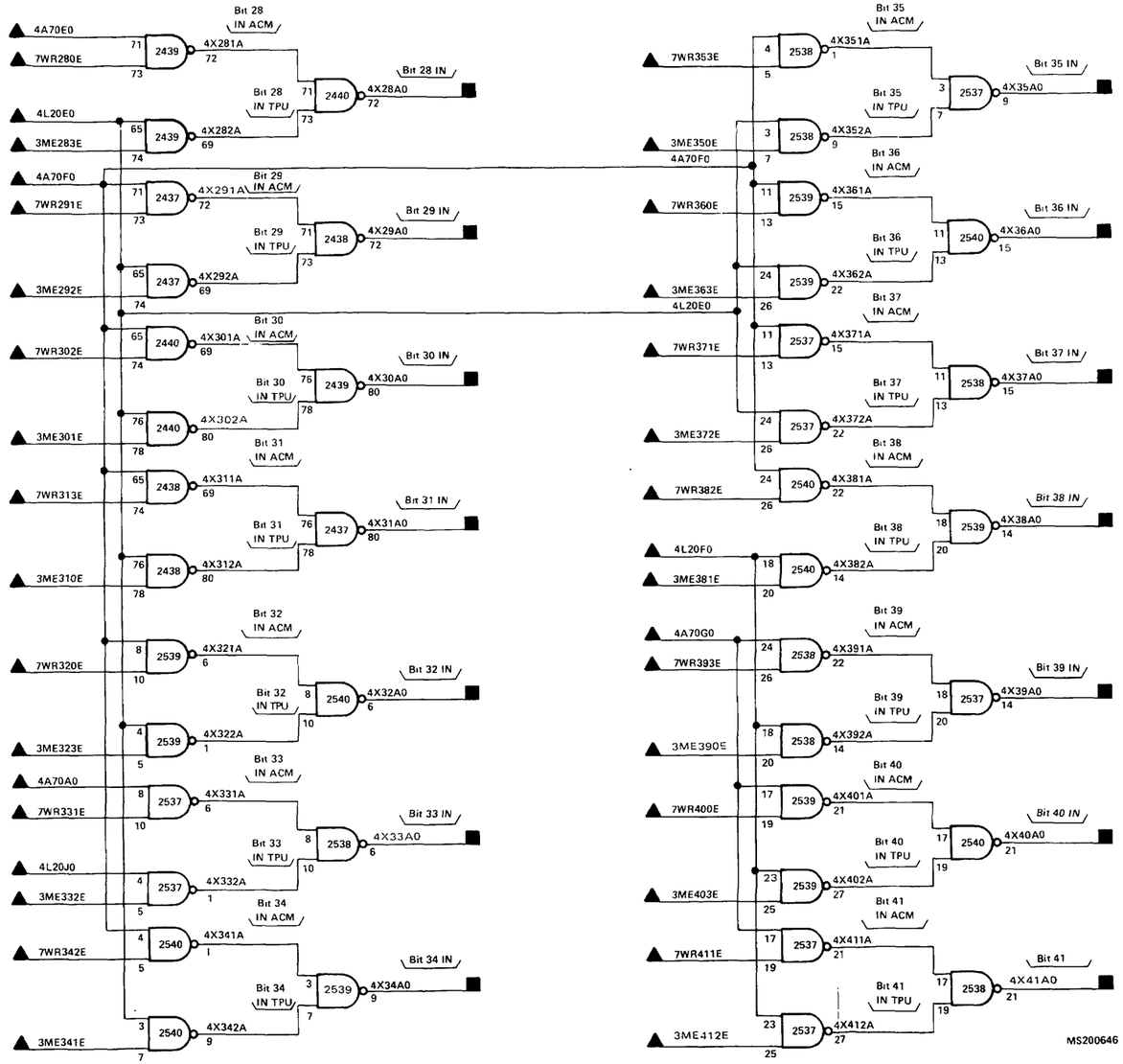
FO-61. Memory Multiplexer Data in Buffer Logic Diagram (Sheet 1 of 5)

INPUT		INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3ME141E	11400	7WR233E	04200	4X14A0	06200, 26802, 31001
3ME150E	11400	7WR233E	26802	4X15A0	06200, 26802, 31001
3ME163E	11400	7WR240E	04200	4X16A0	06200, 26802, 31001
3ME172E	11400	7WR240E	26802	4X17A0	06200, 26802, 31001
3ME181E	11400	7WR251E	04200	4X18A0	06200, 26802, 31001
3ME190E	11400	7WR251E	26802	4X19A0	06200, 26802, 31001
3ME203E	11400	7WR262E	04200	4X20A0	06200, 26802, 31001
3ME212E	11400	7WR262E	26802	4X21A0	06200, 26802, 31001
3ME221E	11400	7WR273E	04200	4X22A0	06200, 26802, 31001
3ME230E	11400	7WR273E	26802	4X23A0	06200, 26802, 31001
3ME243E	11400			4X24A0	06200, 26802, 31002
3ME252E	11400			4X25A0	06200, 26802, 31002
3ME261E	11400			4X26A0	06200, 26802, 31002
3ME270E	11400			4X27A0	06200, 26802, 31002
4A70D0	06001				
4A70E0	06001				
4L20C0	06001				
4L20D0	06001				
7WR142E	04200				
7WR142E	26802				
7WR153E	04200				
7WR153E	26802				
7WR160E	04200				
7WR160E	26802				
7WR171E	04200				
7WR171E	26802				
7WR182E	04200				
7WR182E	26802				
7WR193E	04200				
7WR193E	26802				
7WR200E	04200				
7WR200E	26802				
7WR211E	04200				
7WR211E	26802				
7WR222E	04200				
7WR222E	26802				



FO-61. Memory Multiplexer Data in Buffer Logic Diagram (sheet 2 of 5)

INPUT		OUTPUT	
SIGNAL.	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3ME421E	11400	4X42A0	06200, 26802, 32501
3ME430E	11400	4X43A0	06200, 26802, 32501
3ME443E	11400	4X44A0	06200, 26802, 32501
3ME452E	11400	4X45A0	06200, 26802, 32501
3ME461E	11400	4X46A0	06200, 26802, 32501
3ME470E	11400	4X47A0	06200, 26802, 32501
3ME483E	11400	4X48A0	06200, 26802, 32501
3ME492E	11400	4X49A0	06200, 26802, 32501
3ME501E	11400	4X50A0	06200, 26802, 32501
3ME510E	11400	4X51A0	06200, 26802, 32501
3ME523E	11400	4X52A0	06200, 26802, 32501
3ME532E	11400	4X53A0	06200, 26802, 32501
3ME541E	11400	4X54A0	06200, 26802, 32501
3ME550E	11400	4X55A0	06200, 26802, 32501
4A70G0	06001		
4A70H0	06001		
4L20F0	06001		
4L20G0	06001		
7WR422E	04200		
7WR422E	26802		
7WR433E	04200		
7WR433E	26802		
7WR440E	04200		
7WR440E	26802		
7WR451E	04200		
7WR451E	26802		
7WR462E	04200		
7WR462E	26802		
7WR473E	04200		
7WR473E	26802		
7WR480E	04200		
7WR480E	26802		
7WR491E	04200		
7WR491E	26802		
7WR502E	04200		
7WR502E	26802		
7WR513E	04200		
7WR513E	26802		
7WR520E	04200		
7WR520E	26802		
7WR531E	04200		
7WR531E	26802		
7WR542E	04200		
7WR542E	26802		
7WR553E	04200		
7WR553E	26802		

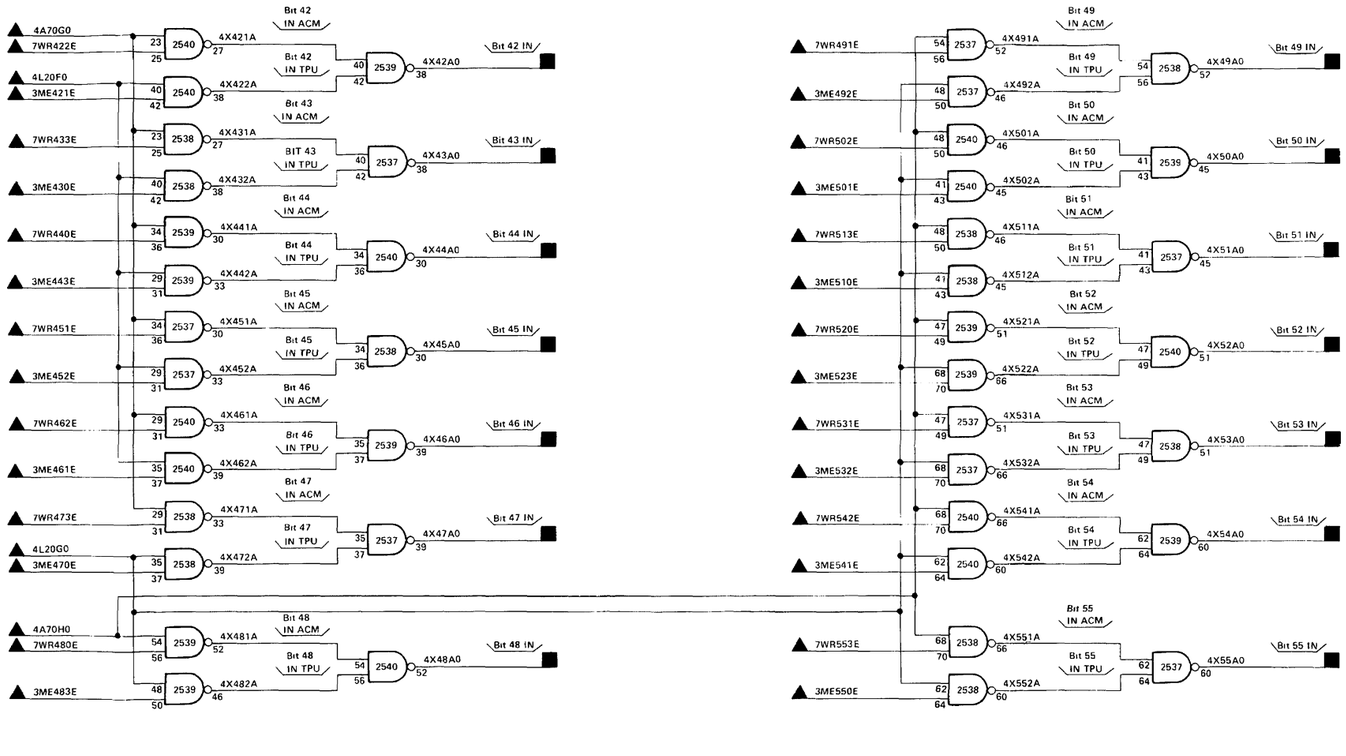


FO-61. Memory Multiplexer Data in Buffer Logic Diagram (Sheet 3 of 5)

INPUT SIGNAL	SOURCE FO-SH
3ME283E	11400
3ME292E	11400
3ME301E	11400
3ME310E	11400
3ME323E	11400
3ME332E	11400
3ME341E	11400
3ME350E	11400
3ME363E	11400
3ME372E	11400
3ME381E	11400
3ME390E	11400
3ME403E	11400
3ME412E	11400
4A70A0	06001
4A70E0	06001
4A70F0	06001
4A70G0	06001
4L20E0	06001
4L20F0	06001
4L20J0	06001
7WR280E	04200
7WR280E	26802
7WR291E	04200
7WR291E	26802
7WR302E	04200
7WR302E	26802
7WR313E	04200
7WR313E	26802
7WR320E	04200
7WR320E	26802
7WR331E	04200
7WR331E	26802
7WR342E	04200
7WR342E	26802
7WR353E	04200
7WR353E	26802
7WR360E	04200
7WR360E	26802
7WR371E	04200
7WR371E	26802
7WR382E	04200
7WR382E	26802
7WR393E	04200
7WR393E	26802
7WR400E	04200

INPUT SIGNAL	SOURCE FO-SH
7WR400E	26802
7WR411E	04200
7WR411E	26802

OUTPUT SIGNAL	DESTINATION FO-SH
4X28A0	06200, 26802, 31002
4X29A0	06200, 26802, 31002
4X30A0	06200, 26802, 31002
4X31A0	06200, 26802, 31002
4X32A0	06200, 26802, 32502
4X33A0	06200, 26802, 32501
4X34A0	06200, 26802, 32501
4X35A0	06200, 26802, 32501
4X36A0	06200, 26802, 32501
4X37A0	06200, 26802, 32501
4X38A0	06200, 26802, 32501
4X39A0	06200, 26802, 32501
4X40A0	06200, 26802, 32501
4X41A0	06200, 26802, 32501



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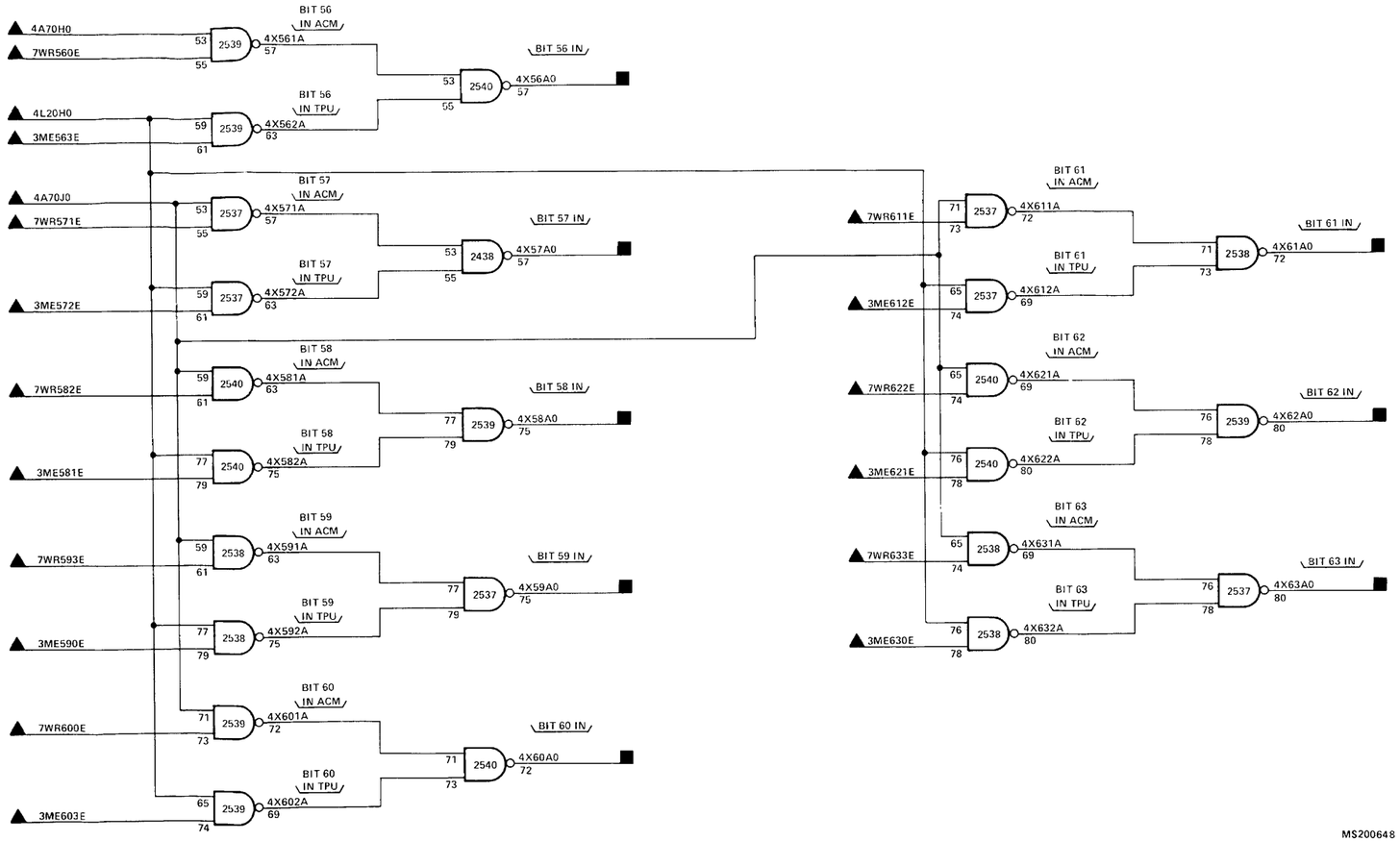
FO-61. Memory Multiplexer Data in Buffer Logic Diagram (Sheet 4 of 5)

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3ME563E	11400	4X56A0	06200, 26802, 32502
3ME572E	11400	4X57A0	06200, 26802, 32502
3ME581E	11400	4X58A0	06200, 26802, 32502
3ME590E	11400	4X59A0	06200, 26802, 32502
3ME603E	11400	4X60A0	06200, 26802, 32502
3ME612E	11400	4X61A0	06200, 26802, 32502
3ME621E	11400	4X62A0	06200, 26802, 32502
3ME630E	11400	4X63A0	06200, 26802, 32502
4A70H0	06001		
4A70J0	06001		
4L20H0	06001		
7WR560E	04200		
7WR560E	26802		
7WR571E	04200		
7WR571E	26802		
7WR582E	04200		
7WR582E	26802		
7WR593E	04200		
7WR593E	26802		
7WR600E	04200		
7WR600E	26802		
7WR611E	04200		
7WR611E	26802		
7WR622E	04200		
7WR622E	26802		
7WR633E	04200		
7WR633E	26802		



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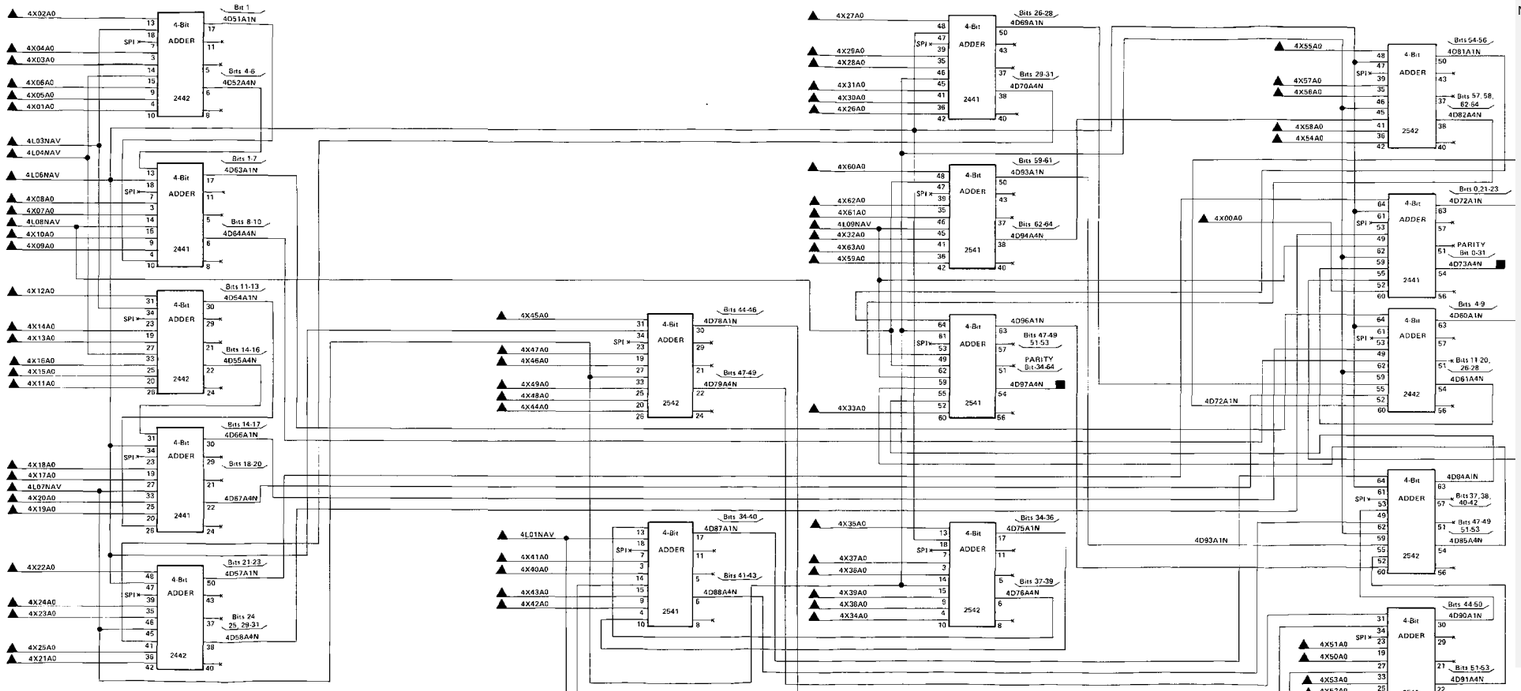
FO-61. Memory Multiplexer Data in Buffer Logic Diagram (Sheet 5 of 5)

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3MA00TA	11800	4A00A0	26802, 32300
3MA01TB	11800	4A01A0	26802, 32300
3MA02TC	11800	4A02A0	26802, 32300
3MA03TD	11800	4A03A0	26802, 32300
3MA04TA	11800	4A04A0	26802, 32300
3MA05TB	11800	4A05A0	26802, 32300
3MA06TC	11800	4A06A0	26802, 32300
3MA07TD	11800	4A06NA	26802, 32300
3MA080	11800	4A07A0	26802, 32300
4A70A0	06001	4A08A0	26802, 32300
4A70B0	06001	4A09A0	26802, 32300
4L20B0	06001	4A10A0	26802, 32300
4L20J0	06001	4A11A0	26802, 32300
7AB000	03100	4A12A0	26802, 32300
7AB000	26802	4L01NAV	06200, 06500
7AB010	03100	4L02NAV	06200, 06500
7AB010	26802	4L03NAV	06200, 06500
7AB020	03100	4L04NAV	06200, 06500
7AB020	26802	4L06NAV	06200
7AB030	03100	4L07NAV	06200
7AB030	26802	4L08NAV	06200
7AB040	03100	4L09NAV	06200
7AB040	26802		
7AB050	03100		
7AB050	26802		
7AB060	03100		
7AB060	26802		
7AB070	03100		
7AB070	26802		
7AB080	03100		
7AB080	26802		
7AB090	03100		
7AB090	26802		
7AB100	03100		
7AB100	26802		
7AB110	03100		
7AB110	26802		
7AB120	03100		
7AB120	26802		



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD

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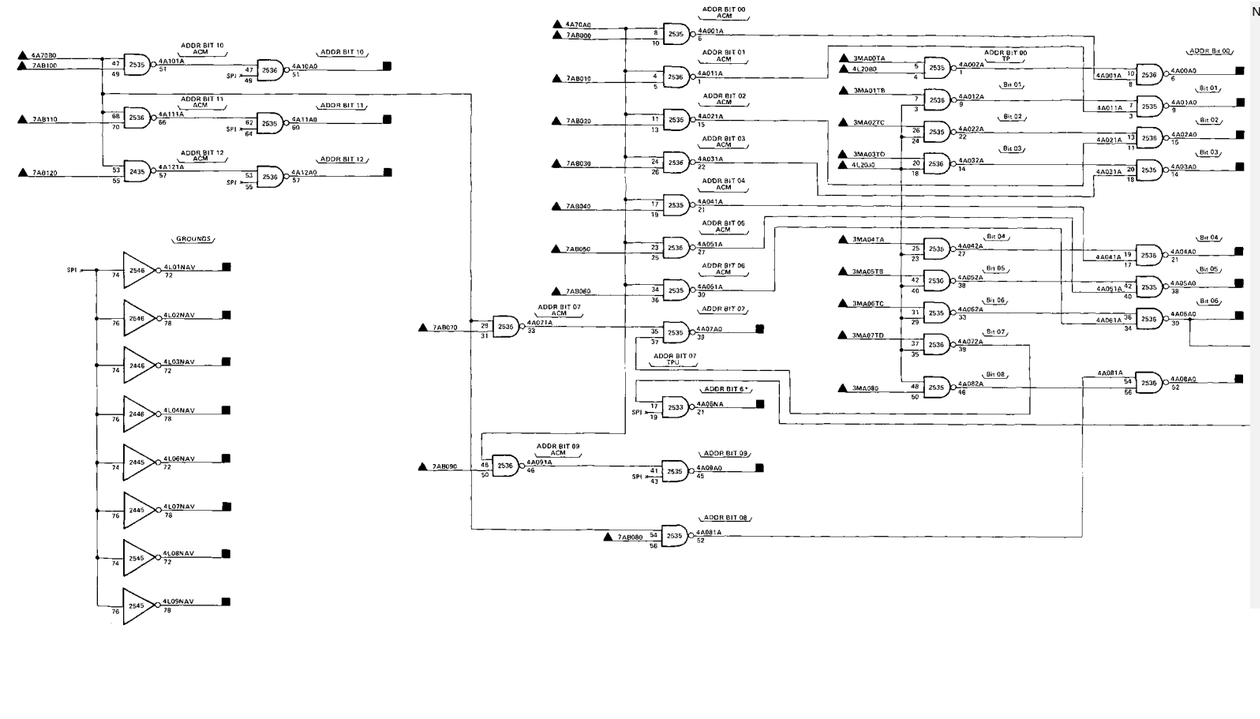
FO-62. Memory Multiplexer Parity Generator Logic Diagram

INPUT SIGNAL	INPUT SOURCE FO-SH	INPUT SIGNAL	INPUT SOURCE FO-SH	OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH	OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
4M00A0	26802	4M23A0	26802	4M00B0V	04600, 11303, 26802	4M21B0V	04600, 11305, 26802
4M00A0	32201	4M23A0	32202	4M00NAV	06500	4M21NAV	06500
4M01A0	26802	4M24A0	26802	4M01B0V	04600, 11303, 26802	4M22B0V	04300, 04400, 04500, 11305,
4M01A0	32201	4M24A0	32202	4M01NAV	06500		26802
4M02A0	26802	4M25A0	26802	4M02B0V	04600, 11303, 26802	4M22NAV	06500
4M02A0	32201	4M25A0	32202	4M02NAV	06500	4M23B0V	04300, 04400, 04500, 11305,
4M03A0	26802	4M26A0	26802	4M03B0V	04600, 11303, 26802		26802
4M03A0	32201	4M26A0	32202	4M03NAV	06500	4M23NAV	06500
4M04A0	26802	4M27A0	26802	4M04B0V	04600, 11303, 26802	4M24B0V	04600, 11301, 26802
4M04A0	32201	4M27A0	32202	4M04NAV	06500	4M24NAV	06500
4M05A0	26802	4M28A0	26802	4M05B0V	04600, 11303, 26802	4M25B0V	04600, 11301, 26802
4M05A0	32201	4M28A0	32202	4M05NAV	06500	4M25NAV	06500
4M06A0	26802	4M29A0	26802	4M06B0V	04300, 04400, 04500, 11303,	4M26B0V	04600, 11301, 26802
4M06A0	32201	4M29A0	32202		26802	4M26NAV	06500
4M07A0	26802	4M30A0	26802	4M06NAV	06500	4M27B0V	04600, 11301, 26802
4M07A0	32201	4M30A0	32202	4M07B0V	04300, 04400, 04500, 11303,	4M27NAV	06500
4M08A0	26802	4M31A0	26802		26802	4M28B0V	04600, 11301, 26802
4M08A0	32201	4M31A0	32202	4M07NAV	06500	4M28NAV	06500
4M09A0	26802			4M08B0V	04600, 11303, 26802	4M29B0V	04600, 11301, 26802
4M09A0	32201			4M08NAV	06500	4M29NAV	06500
4M1DA0	26802			4M09B0V	04600, 11303, 26802	4M30B0V	04300, 04400, 04500, 11301,
4M10A0	32201			4M09NAV	06500		26802
4M11A0	26802			4M10B0V	04600, 11304, 26802	4M30NAV	06500
4M11A0	32201			4M10NAV	06500	4M31B0V	04300, 04400, 04500, 11301,
4M12A0	26802			4M11B0V	04600, 11304, 26802		26802
4M12A0	32201			4M11NAV	06500	4M31NAV	06500
4M13A0	26802			4M12B0V	04600, 11304, 26802		
4M13A0	32201			4M12NAV	06500		
4M14A0	26802			4M13B0V	04600, 11304, 26802		
4M14A0	32201			4M13NAV	06500		
4M15A0	26802			4M14B0V	04300, 04400, 04500, 11304,		
4M15A0	32201				26802		
4M16A0	26802			4M14NAV	06500		
4M16A0	32201			4M15B0V	04300, 04400, 04500, 11304,		
4M17A0	26802				26802		
4M17A0	32201			4M15NAV	06500		
4M18A0	26802			4M16B0V	04600, 11304, 26802		
4M18A0	32201			4M16NAV	06500		
4M19A0	26802			4M17B0V	04600, 11304, 26802		
4M19A0	32201			4M17NAV	06500		
4M20A0	26802			4M18B0V	04600, 11304, 26802		
4M20A0	32201			4M18NAV	06500		
4M21A0	26802			4M19B0V	04600, 11304, 26802		
4M21A0	32201			4M19NAV	06500		
4M22A0	26802			4M20B0V	04600, 11305, 26802		
4M22A0	32201			4M20NAV	06500		



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INPUT FROM ANOTHER FIGURE
   - △ INPUT FROM SAME FIGURE
   - ■ OUTPUT TO ANOTHER FIGURE
   - □ OUTPUT TO BOTH SAME AND A FIGURE
   - ◪ OUTPUT TO SAME FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOF

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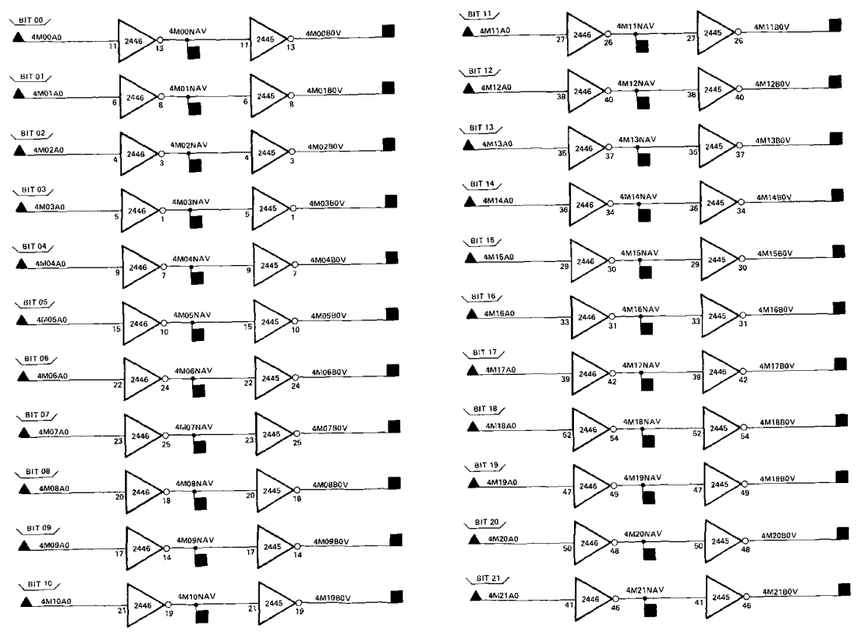
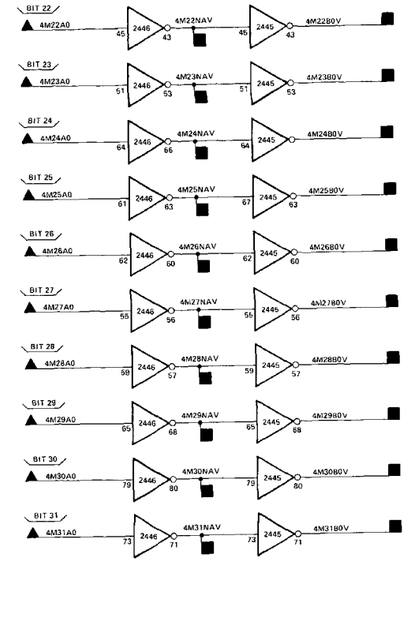
FO-63. Memory Multiplexer Address Multiplexer Logic Diagram

INPUT SIGNAL	INPUT SOURCE FO-SH	INPUT SIGNAL	INPUT SOURCE FO-SH	INPUT SIGNAL	INPUT SOURCE FO-SH	OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
4L01NAV	06300	4X19A0	06102	4X42A0	06104	4D73A4N	26802, 31002
4L02NAV	06300	4X19A0	31001	4X42A0	32501	4D97A4N	26802, 32502
4L03NAV	06300	4X20A0	06102	4X43A0	06104		
4L04NAV	06300	4X20A0	31001	4X43A0	32501		
4L06NAV	06300	4X21A0	06102	4X44A0	06104		
4L07NAV	06300	4X21A0	31001	4X44A0	32501		
4L08NAV	06300	4X22A0	06102	4X45A0	06104		
4L09NAV	06300	4X22A0	31001	4X45A0	32501		
4X00A0	06101	4X23A0	06102	4X46A0	06104		
4X00A0	31001	4X23A0	31001	4X46A0	32501		
4X01A0	06101	4X24A0	06102	4X47A0	06104		
4X01A0	31001	4X24A0	31002	4X47A0	32501		
4X02A0	06101	4X25A0	06102	4X48A0	06104		
4X02A0	31001	4X25A0	31002	4X48A0	32501		
4X03A0	06101	4X26A0	06102	4X49A0	06104		
4X03A0	31001	4X26A0	31002	4X49A0	32501		
4X04A0	06101	4X27A0	06102	4X50A0	06104		
4X04A0	31001	4X27A0	31002	4X50A0	32501		
4X05A0	06101	4X28A0	06103	4X51A0	06104		
4X05A0	31001	4X28A0	31002	4X51A0	32501		
4X06A0	06101	4X29A0	06103	4X52A0	06104		
4X06A0	31001	4X29A0	31002	4X52A0	32501		
4X07A0	06101	4X30A0	06103	4X53A0	06104		
4X07A0	31001	4X30A0	31002	4X53A0	32501		
4X08A0	06101	4X31A0	06103	4X54A0	06104		
4X08A0	31001	4X31A0	31002	4X54A0	32501		
4X09A0	06101	4X32A0	06103	4X55A0	06104		
4X09A0	31001	4X32A0	32502	4X55A0	32501		
4X10A0	06101	4X33A0	06103	4X56A0	06105		
4X10A0	31001	4X33A0	32501	4X56A0	32502		
4X11AD	06101	4X34A0	06103	4X57A0	06105		
4X11A0	31001	4X34A0	32501	4X57A0	32502		
4X12A0	06101	4X35A0	06103	4X58A0	06105		
4X12A0	31001	4X35A0	32501	4X58A0	32502		
4X13A0	06101	4X36A0	06103	4X59A0	06105		
4X13A0	31001	4X36A0	32501	4X59A0	32502		
4X14AD	06102	4X37A0	06103	4X60A0	06105		
4X14A0	31001	4X37A0	32501	4X60A0	32502		
4X15A0	06102	4X38A0	06103	4X61A0	06105		
4X15A0	31001	4X38A0	32501	4X61A0	32502		
4X16A0	06102	4X39A0	06103	4X62A0	06105		
4X16A0	31001	4X39A0	32501	4X62A0	32502		
4X17A0	06102	4X40A0	06103	4X63A0	06105		
4X17A0	31001	4X40A0	32501	4X63A0	32502		
4X18A0	06102	4X41A0	06103				
4X18A0	31001	4X41A0	32501				





FO-64. Memory Multiplexer Data Buffer Logic Diagram (Sheet 1 of 2)

NOTES: UNLESS OTHERWISE SPECIFIED

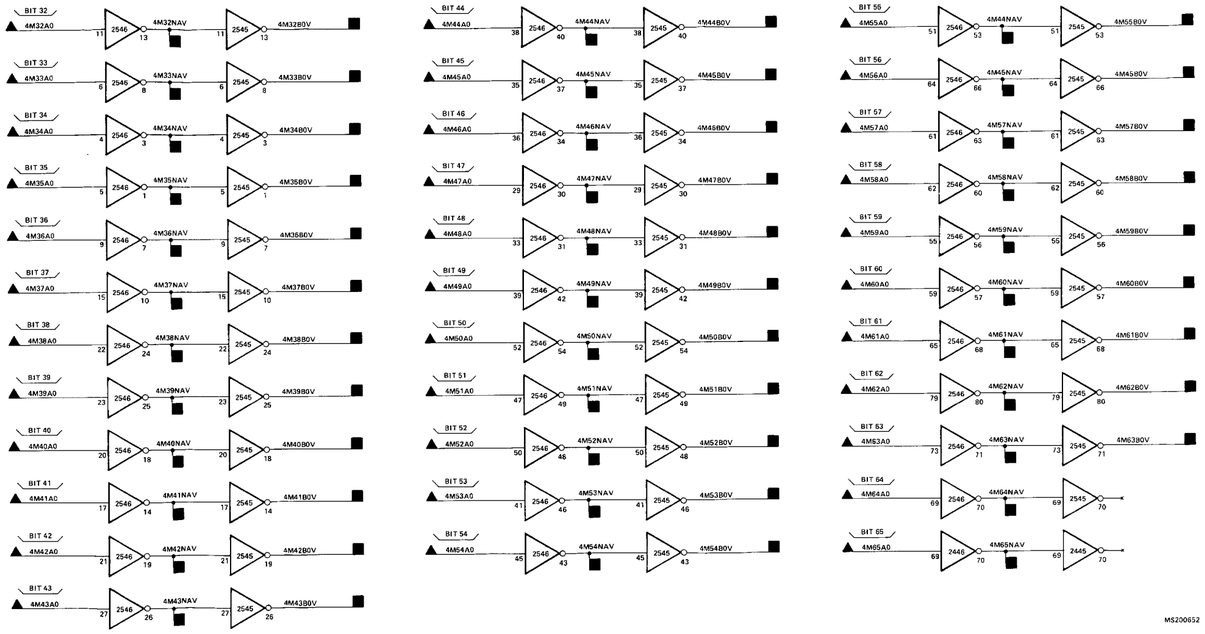
1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANO FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

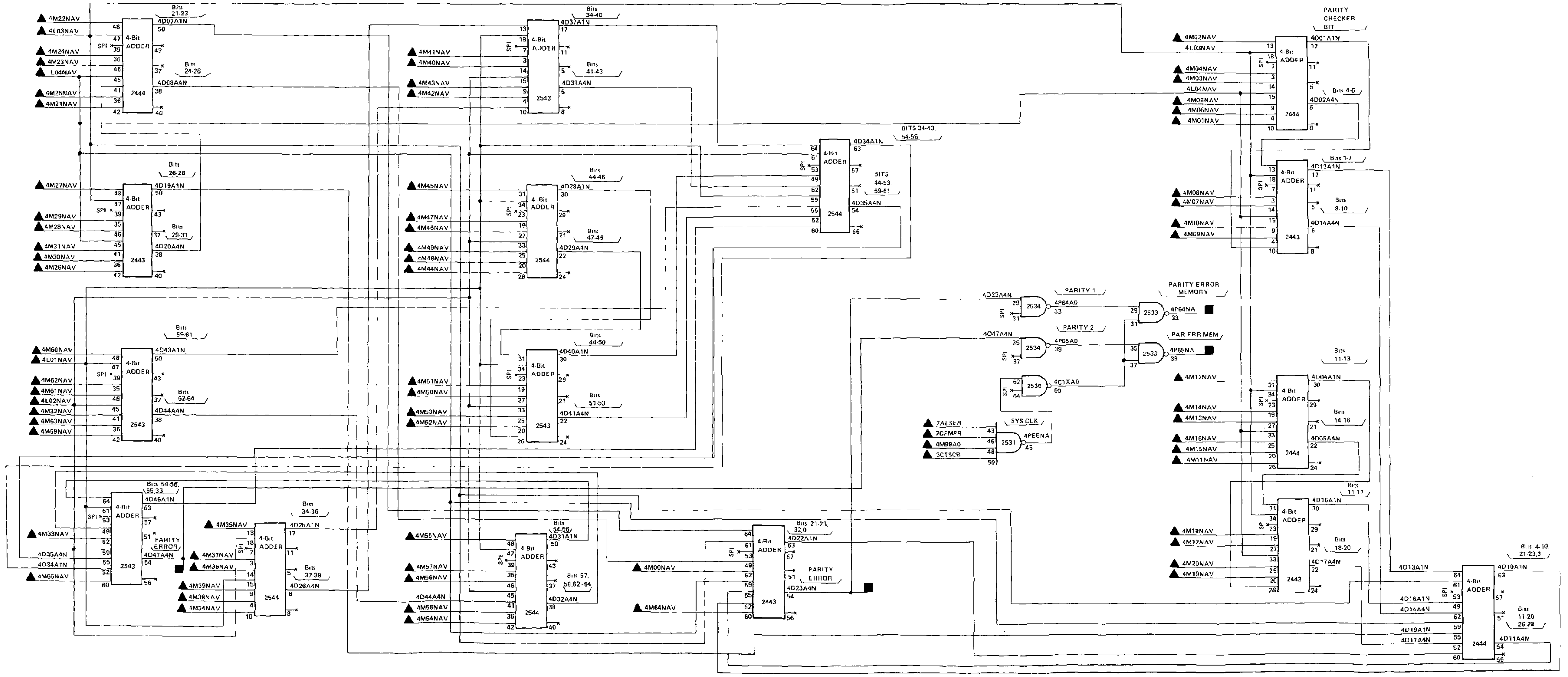
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND

INPUT		INPUT		OUTPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH	SIGNAL	DESTINATION FO-SH
4M32A0	26802	4M55A0	26802	4M32B0V	04600, 11306, 26802	4M53B0V	04600, 11301, 26802
4M32A0	32602	4M55A0	32601	4M32NAV	06500	4M53NAV	06500
4M33A0	26802	4M56A0	26802	4M33B0V	04600, 11306, 26802	4M54B0V	04300, 04400, 04500, 11301,
4M33A0	32601	4M56A0	32602	4M33NAV	06500		26802
4M34A0	26802	4M57A0	26802	4M34B0V	04600, 11306, 26802	4M54NAV	06500
4M34A0	32601	4M57A0	32602	4M34NAV	06500	4M55B0V	04300, 04400, 04500, 11302,
4M35A0	26802	4M58A0	26802	4M35B0V	04600, 11306, 26802		26802
4M35A0	32601	4M58A0	32602	4M35NAV	06500	4M55NAV	06500
4M36A0	26802	4M59A0	26802	4M36B0V	04600, 11306, 26802	4M56B0V	04600, 08200, 11302, 26802
4M36A0	32601	4M59A0	32602	4M36NAV	06500	4M56NAV	06500
4M37A0	26802	4M60A0	26802	4M37B0V	04600, 11307, 26802	4M57B0V	04600, 11302, 26802
4M37A0	32601	4M60A0	32602	4M37NAV	06500	4M57NAV	06500
4M38A0	26802	4M61A0	26802	4M38B0V	04300, 04400, 04500, 11307,	4M58B0V	04600, 11302, 26802
4M38A0	32601	4M61A0	32602		26802	4M58NAV	06500
4M39A0	26802	4M62A0	26802	4M38NAV	06500	4M59B0V	04600, 11302, 26802
4M39A0	32601	4M62A0	32602	4M39B0V	04300, 04400, 04500, 11302,	4M59NAV	06500
4M40A0	26802	4M63A0	26802		26802	4M60B0V	04600, 11302, 26802
4M40A0	32601	4M63A0	32602	4M39NAV	06500	4M60NAV	06500
4M41A0	26802	4M64A0	26802	4M40B0V	04600, 11302, 26802	4M61B0V	04600, 09500, 11302, 26802
4M41A0	32601	4M64A0	32202	4M40NAV	06500	4M61NAV	06500
4M42A0	26802	4M65A0	26802	4M41B0V	04600, 11302, 26802	4M62B0V	04300, 04400, 04500, 11302,
4M42A0	32601	4M65A0	32602	4M41NAV	06500		26802
4M43A0	26802			4M42B0V	04600, 11302, 26802	4M62NAV	06500
4M43A0	32601			4M42NAV	06500	4M63B0V	04300, 04400, 04500, 08200,
4M44A0	26802			4M43B0V	04600, 11302, 26802		09500, 11308, 26802
4M44A0	32601			4M43NAV	06500	4M63NAV	06500
4M45A0	26802			4M44B0V	04600, 11302, 26802	4M64NAV	06500
4M45A0	32601			4M44NAV	06500	4M65NAV	06500
4M46A0	26802			4M45B0V	04600, 11302, 26802		
4M46A0	32601			4M45NAV	06500		
4M47A0	26802			4M46B0V	04300, 04400, 04500, 11302,		
4M47A0	32601				26802		
4M48A0	26802			4M46NAV	06500		
4M48A0	32601			4M47B0V	04300, 04400, 04500, 11301,		
4M49A0	26802				26802		
4M49A0	32601			4M47NAV	06500		
4M50A0	26802			4M48B0V	04600, 11301, 26802		
4M50A0	32601			4M48NAV	06500		
4M51A0	26802			4M49B0V	04600, 11301, 26802		
4M51A0	32601			4M49NAV	06500		
4M52A0	26802			4M50B0V	04600, 11301, 26802		
4M52A0	32601			4M50NAV	06500		
4M53A0	26802			4M51B0V	04600, 11301, 26802		
4M53A0	32601			4M51NAV	06500		
4M54A0	26802			4M52B0V	04600, 11301, 26802		
4M54A0	32601			4M52NAV	06500		



FO-64. Memory Multiplexer Data Buffer Logic Diagram (Sheet 2 of 2)

INPUT		INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3CTSCB	07600	4M41NAV	06402	4P64NA	13500, 26802, 31302
4L01NAV	06300	4M42NAV	06402	4P65NA	13500, 26802, 31302
4L02NAV	06300	4M43NAV	06402		
4L03NAV	06300	4M44NAV	06402		
4L04NAV	06300	4M45NAV	06402		
4M00NAV	06401	4M46NAV	06402		
4M01NAV	06401	4M47NAV	06402		
4M02NAV	06401	4M48NAV	06402		
4M03NAV	06401	4M49NAV	06402		
4M04NAV	06401	4M50NAV	06402		
4M05NAV	06401	4M51NAV	06402		
4M06NAV	06401	4M52NAV	06402		
4M07NAV	06401	4M53NAV	06402		
4M08NAV	06401	4M54NAV	06402		
4M09NAV	06401	4M55NAV	06402		
4M10NAV	06401	4M56NAV	06402		
4M11NAV	06401	4M57NAV	06402		
4M12NAV	06401	4M58NAV	06402		
4M13NAV	06401	4M59NAV	06402		
4M14NAV	06401	4M60NAV	06402		
4M15NAV	06401	4M61NAV	06402		
4M16NAV	06401	4M62NAV	06402		
4M17NAV	06401	4M63NAV	06402		
4M18NAV	06401	4M64NAV	06402		
4M19NAV	06401	4M65NAV	06402		
4M20NAV	06401	4M99A0	06001		
4M21NAV	06401	4M99A0	30901		
4M22NAV	06401	7ALSER	02900		
4M23NAV	06401	7ALSER	26802		
4M24NAV	06401	7CFMPR	01900		
4M25NAV	06401	7CFMPR	26802		
4M26NAV	06401				
4M27NAV	06401				
4M28NAV	06401				
4M29NAV	06401				
4M30NAV	06401				
4M31NAV	06401				
4M32NAV	06402				
4M33NAV	06402				
4M34NAV	06402				
4M35NAV	06402				
4M36NAV	06402				
4M37NAV	06402				
4M38NAV	06402				
4M39NAV	06402				
4M40NAV	06402				



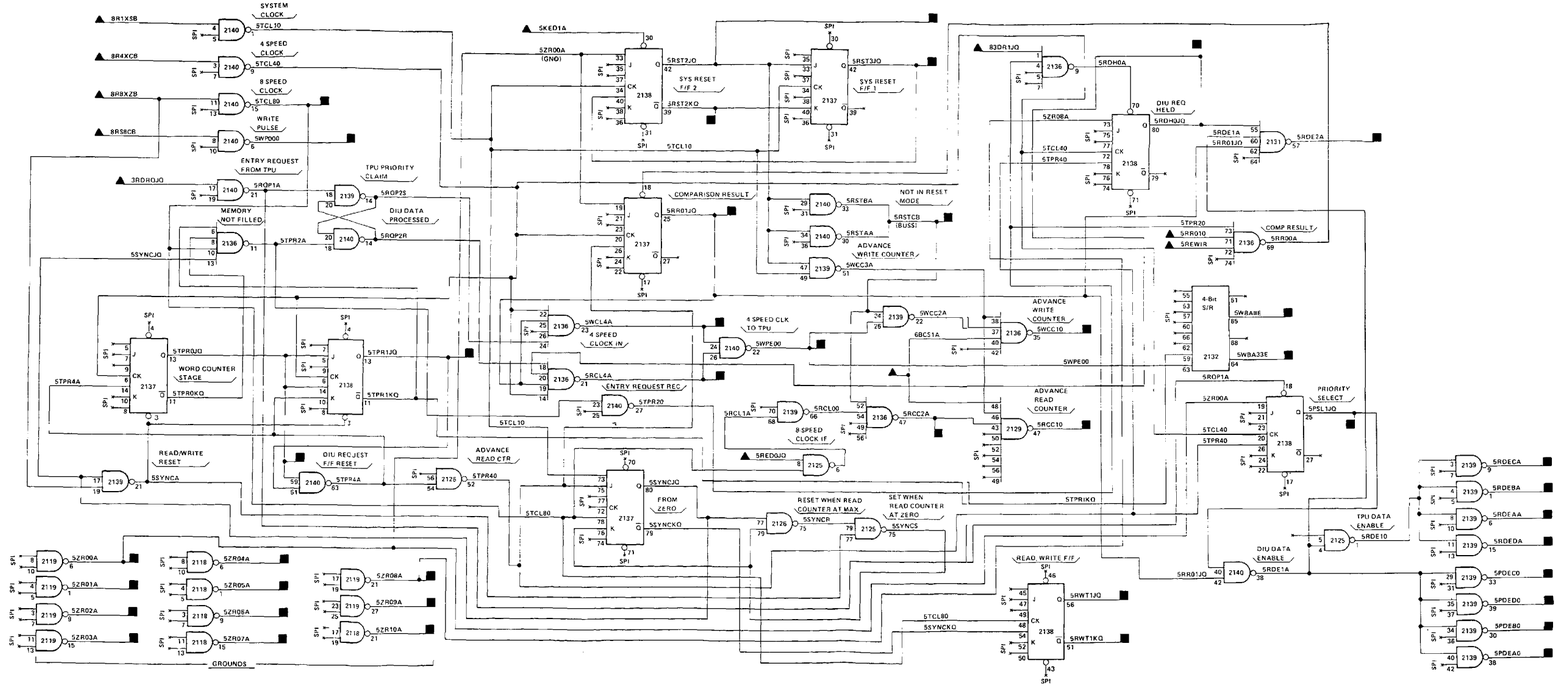
NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN: FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 2 CARD CAGE (1A1A1A5)
3. REFERENCES ARE AS FOLLOWS
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE.
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE.
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.
    - B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-38 FOR CARD PIN/ TEST POINT FOR MTS TESTABLE CARDS.
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2130, A2230, A2330, A2430, AND A2530.

MS200853

FO-65. Memory Multiplexer Parity Checker Logic Diagram

INPUT SIGNAL	INPUT SOURCE FO-SH	OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
3RDRQJQ	11100	5PDEAQ	06800
3RDRQJQ	26802	5PDEBQ	06800
5KE01A	13402,	5PDECQ	06800
5KE01A	26803	5PDEDQ	06800
5KED1A	31301	5PSL1JQ	07500
5RE00JQ	06700	5RCC1Q	07000
5REW1R	07202	5RCC2A	07202
5RR01Q	07201	5RCL4A	18102, 18300, 26802, 30802
6BC51A	27201	5RDEAA	06800
8RS8CB	14300	5RDEBA	06800
8RS8CB	26802	5RDECA	06800
8RS8CB	26203	5RDEDA	06800
8R1XSB	00300	5RDE2A	07500, 18300, 26802, 30802
8R1XSB	14900	5RSTCB	06700, 06900, 07000, 07202
8R1XSB	26802	5RST2JQ	06700
8R1XSB	26803	5RST2KQ	06700, 07202
8R1XSB	27501	5RWT1JQ	07100, 07500
8R4XCB	14900	5RWT1KQ	06700, 07100
8R4XCB	26802	5TCL80	06700
8R4XCB	27201	5WBA11E	07202
8R8XZB	14300	5WBA33E	07201
8R8XZB	27201	5WCC1D	06900
83DR1JQ	18300	5WCL4A	11100
83DR1JQ	26802	5WPE00	07301, 07302, 26802, 30301
		5WP000	06700, 07202, 07301, 07302, 26802, 30301
		5ZR00A	06900, 07000
		5ZR01A	06900, 07000
		5ZR02A	06900, 07000, 07100
		5ZR03A	06900, 07000
		5ZR04A	06900, 07201, 07202
		5ZR05A	07201, 07202
		5ZR06A	07201, 07202
		5ZR07A	07201, 07202
		5ZR08A	07201, 07202
		5ZR09A	07201, 07202
		5ZR10A	07201, 07202



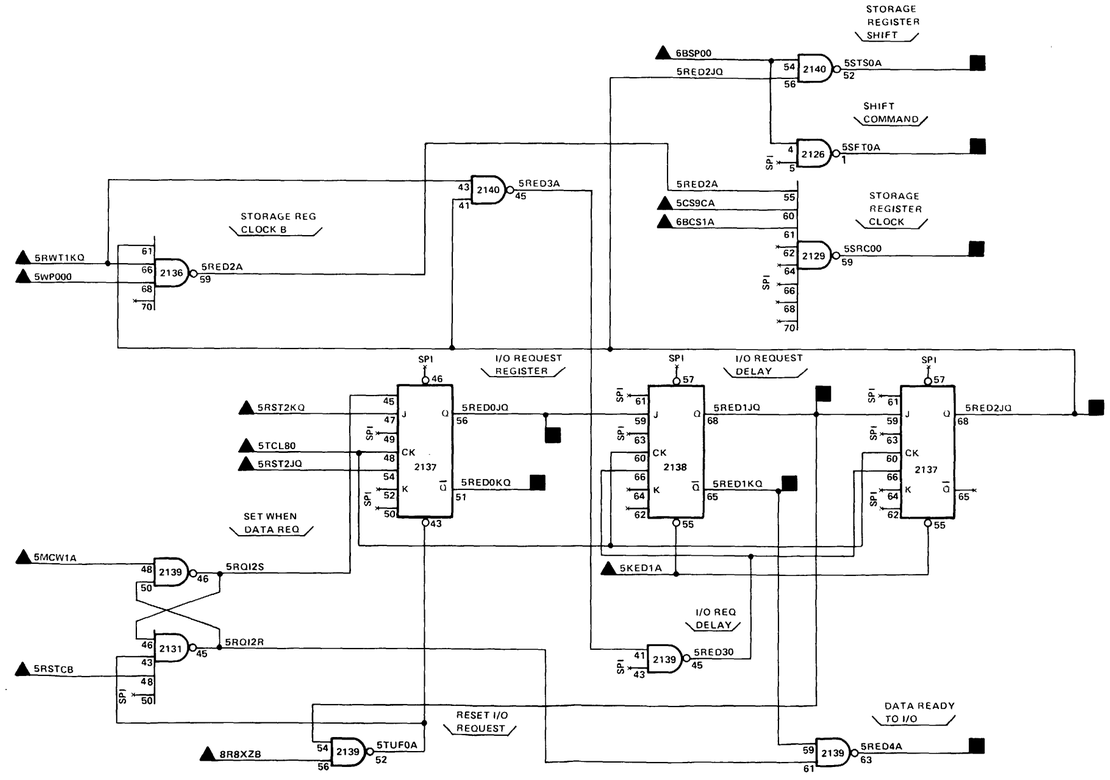
NOTES UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN, FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 2 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE.
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE.
4. *REFER TO TABLE 5-1 FOR CARD LOCATION IN* LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.
    - B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-38 FOR CARD PIN/ *TEST POINT FOR* MTS TESTABLE CARDS.
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2130, A2230, A2330, A2430 AND A2530.

MS200654A

Change 1 FO-66. Report Buffer Input Control Logic Diagram

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
5CS9CA	13000	5RED0JQ	06600
5CS9CA	26802	5RED0KQ	07202
5CS9CA	31301	5RED1KQ	07202
5KED1A	13400	5RED2JQ	07500
5KED1A	26803	5RED4A	07500, 13200, 26802, 31301
5KED1A	31301	5SFT0A	06900, 07000, 07500
5MCW1A	13200	5SRC00	07400
5MCW1A	26802	5STS0A	07400
5MCW1A	31301		
5RSTCB	06600		
5RST2JQ	06600		
5RST2KQ	06600		
5RWT1KQ	06600		
5TCL80	06600		
5WP000	06600		
5WP000	30301		
6BCS1A	27201		
6BSP00	26802		
6BSP00	27201		
8R8XZB	14300		
8R8XZB	27201		



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XI FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

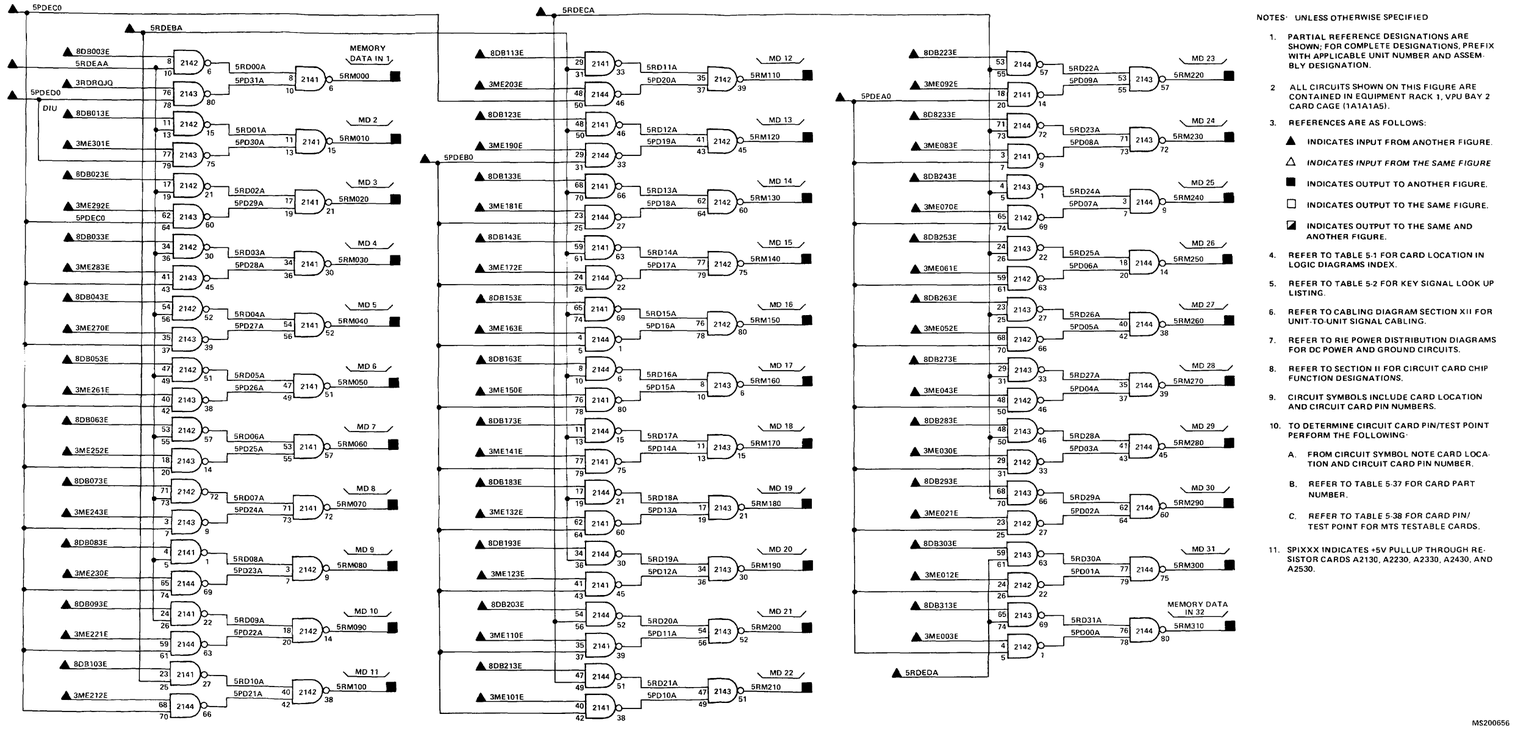
    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

MS200655

FO-67. Report Buffer Output Control Logic Diagram

INPUT SIGNAL	SOURCE FO-SH	INPUT SIGNAL	SOURCE FO-SH	INPUT SIGNAL	SOURCE FO-SH	OUTPUT SIGNAL	DESTINATION FO-SH
3ME003E	11400	8DB023E	30801	8DB253E	30802	5RM000	07301, 26802, 30201
3ME012E	11400	8DB033E	26802	8DB263E	26802	5RM010	07301, 26802, 30201
3ME021E	11400	8DB033E	30801	8DB263E	30802	5RM020	07301, 26802, 30201
3ME030E	11400	8DB043E	26802	8DB273E	26802	5RM030	07301, 26802, 30201
3ME043E	11400	8DB043E	30801	8DB273E	30802	5RM040	07301, 26802, 30201
3ME052E	11400	8DB053E	26802	8DB283E	26802	5RM050	07301, 26802, 30201
3ME061E	11400	8DB053E	30801	8DB283E	30802	5RM060	07301, 26802, 30201
3ME070E	11400	8DB063E	26802	8DB293E	26802	5RM070	07301, 26802, 30201
3ME083E	11400	8DB063E	30801	8DB293E	30802	5RM080	07301, 26802, 30201
3ME092E	11400	8DB073E	26802	8DB303E	26802	5RM090	07301, 26802, 30201
3ME101E	11400	8DB073E	30801	8DB303E	30802	5RM100	07301, 26802, 30201
3ME110E	11400	8DB083E	26802	8DB313E	26802	5RM110	07301, 26802, 30201
3ME123E	11400	8DB083E	30801	8DB313E	30802	5RM120	07301, 26802, 30201
3ME132E	11400	8DB093E	26802			5RM130	07301, 26802, 30201
3ME141E	11400	8DB093E	30801			5RM140	07301, 26802, 30201
3ME150E	11400	8DB103E	26802			5RM150	07301, 26802, 30201
3ME163E	11400	8DB103E	30801			5RM160	07302, 26802, 30201
3ME172E	11400	8DB113E	26802			5RM170	07302, 26802, 30202
3ME181E	11400	8DB113E	30801			5RM180	07302, 26802, 30202
3ME190E	11400	8DB123E	26802			5RM190	07302, 26802, 30202
3ME203E	11400	8DB123E	30801			5RM200	07302, 26802, 30202
3ME212E	11400	8DB133E	26802			5RM210	07302, 26802, 30202
3ME221E	11400	8DB133E	30801			5RM220	07302, 26802, 30202
3ME230E	11400	8DB143E	26802			5RM230	07302, 26802, 30202
3ME243E	11400	8DB143E	30801			5RM240	07302, 26802, 30202
3ME252E	11400	8DB153E	26802			5RM250	07302, 26802, 30202
3ME261E	11400	8DB153E	30801			5RM260	07302, 26802, 30202
3ME270E	11400	8DB163E	26802			5RM270	07302, 26802, 30202
3ME283E	11400	8DB163E	30801			5RM280	07302, 26802, 30202
3ME292E	11400	8DB173E	26802			5RM290	07302, 26802, 30202
3ME301E	11400	8DB173E	30801			5RM300	07302, 26802, 30202
3RDRQJQ	11100	8DB183E	26802			5RM310	07302, 26802, 30202
3RDRQJQ	26802	8DB183E	30801				
5PDEA0	06600	8DB193E	26802				
5PDEB0	06600	8DB193E	30801				
5PDEC0	06600	8DB203E	26802				
5PDED0	06600	8DB203E	30801				
5RDEAA	06600	8DB213E	26802				
5RDEBA	06600	8DB213E	30801				
5RDECA	06600	8DB223E	26802				
5RDEDA	06600	8DB223E	30801				
8DB003E	26802	8DB233E	26802				
8DB003E	30801	8DB233E	30802				
8DB013E	26802	8DB243E	26802				
8DB013E	30801	8DB243E	30802				
8DB023E	26802	8DB253E	26802				



NOTES: UNLESS OTHERWISE SPECIFIED

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2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 2 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE.
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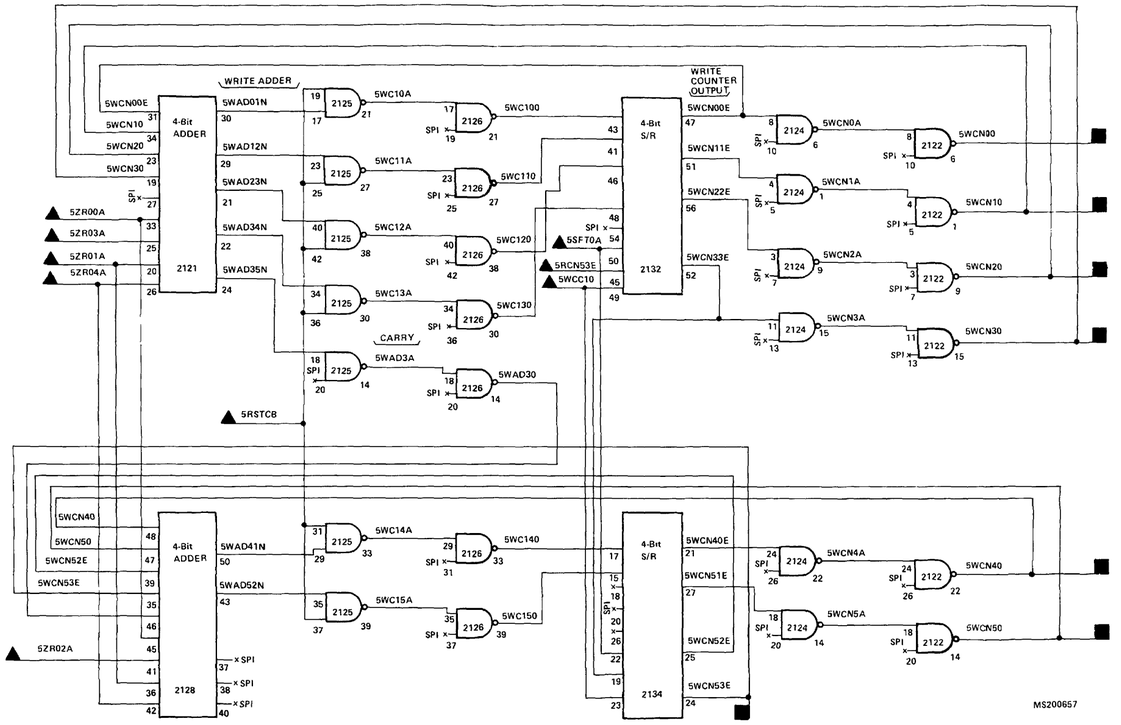
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.
    - B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.
    - C. *REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.*
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2130, A2230, A2330, A2430, AND A2530.

MS200656

FO-68. Report Buffer Data Multiplexer Logic Diagram

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
5RCN53E	07000	5WCN00	07100, 07201, 07202
5RSTCB	06600	5WCN10	07100, 07201, 07202
5SFT0A	06700	5WCN20	07100, 07201, 07202
5WCC10	06600	5WCN30	07100, 07201, 07202
5ZR00A	06600	5WCN40	07100, 07201, 07202
5ZR01A	06600	5WCN50	07100, 07201
5ZR02A	06600	5WCN53E	07500
5ZR03A	06600		
5ZR04A	06600		



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3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
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   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
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6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION I FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

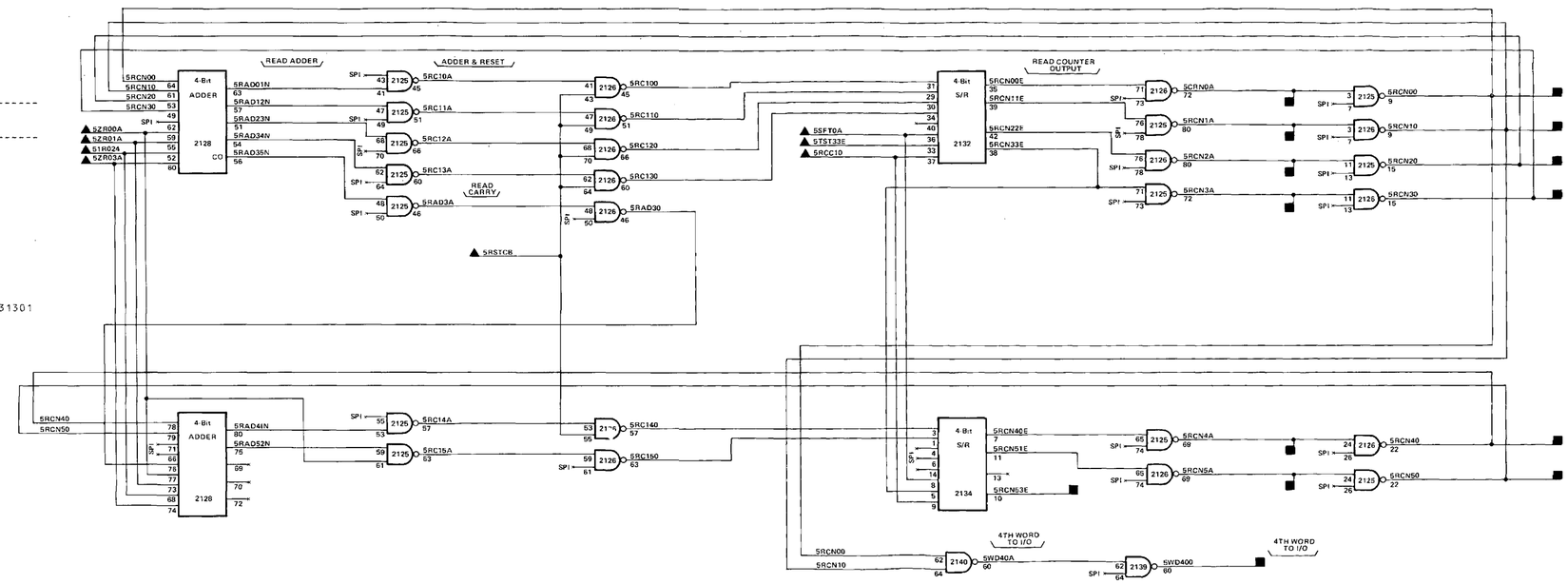
    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

FO-69. Report Buffer Write counter Logic Diagram

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
5RCC10	06600	5RCN0A	07201
5RSTCB	06600	5RCN00	07100, 07202
5SFT0A	06700	5RCN1A	07201
5TST33E	07500	5RCN10	07100, 07202
5TST33E	26802	5RCN2A	07201
5ZR00A	06600	5RCN20	07100, 07202
5ZR01A	06600	5RCN3A	07201
5ZR02A	06600	5RCN30	07100, 07202
5ZR03A	06600	5RCN4A	07201
		5RCN40	07100, 07202
		5RCN5A	07201
		5RCN50	07100, 07202
		5RCN53E	06900
		5WD400	07500, 13200, 26802, 31301



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

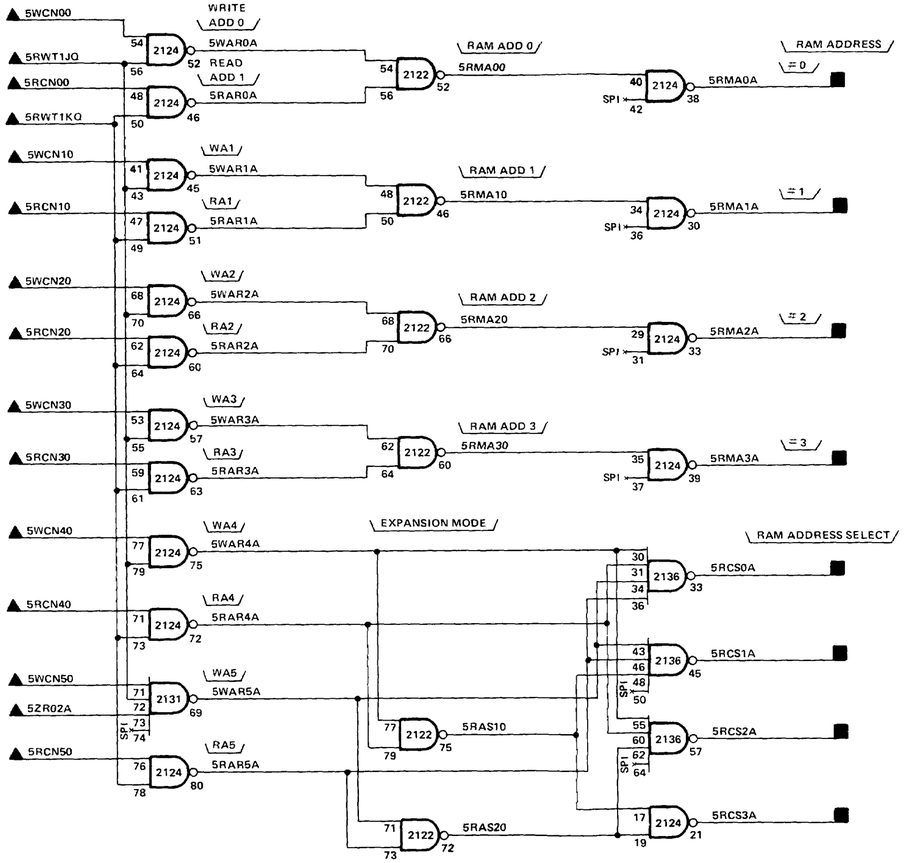
    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

MS200658

FO-70. Report Buffer Read Counter Logic Diagram

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
5RCN00	07000	5RCS0A	07301, 07500, 26802, 30201
5RCN10	07000	5RCS1A	07301, 07302, 07500, 26802,
5RCN20	07000		30201
5RCN30	07000	5RCS2A	07500, 26802
5RCN40	07000	5RCS3A	07500, 26802
5RCN50	07000	5RMA0A	07301, 07302, 07500, 26802,
5RWT1JQ	06600		30201
5RWT1KQ	06600	5RMA1A	07301, 07302, 07500, 26802,
5WCN00	06900		30201
5WCN10	06900	5RMA2A	07301, 07302, 07500, 26802,
5WCN20	06900		30201
5WCN30	06900	5RMA3A	07301, 07302, 07500, 26802,
5WCN40	06900		30201
5WCN50	06900		
5ZR02A	06600		



FO-71. Report Buffer Address Multiplexer Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

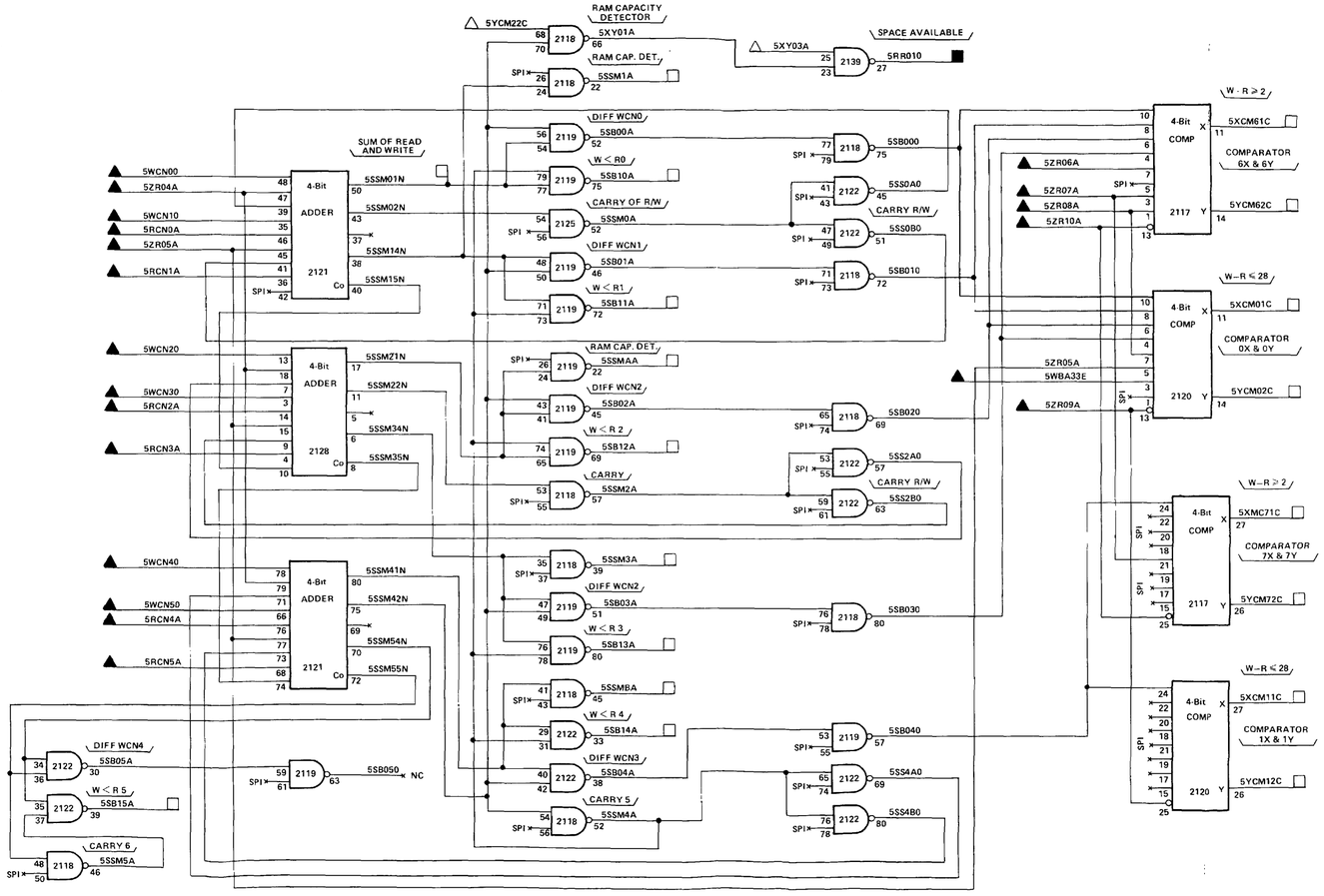
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FORMTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
5RCN0A	07000	5RR010	06600, 07500, 11100
5RCN1A	07000		
5RCN2A	07000		
5RCN3A	07000		
5RCN4A	07000		
5RCN5A	07000		
5WBA33E	06600		
5WCN00	06900		
5WCN10	06900		
5WCN20	06900		
5WCN30	06900		
5WCN40	06900		
5WCN50	06900		
5ZR04A	06600		
5ZR05A	06600		
5ZR06A	06600		
5ZR07A	06600		
5ZR08A	06600		
5ZR09A	06600		
5ZR10A	06600		



FO-72. Report Buffer Memory Evaluation Logic Diagram (Sheet 1 of 2)

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

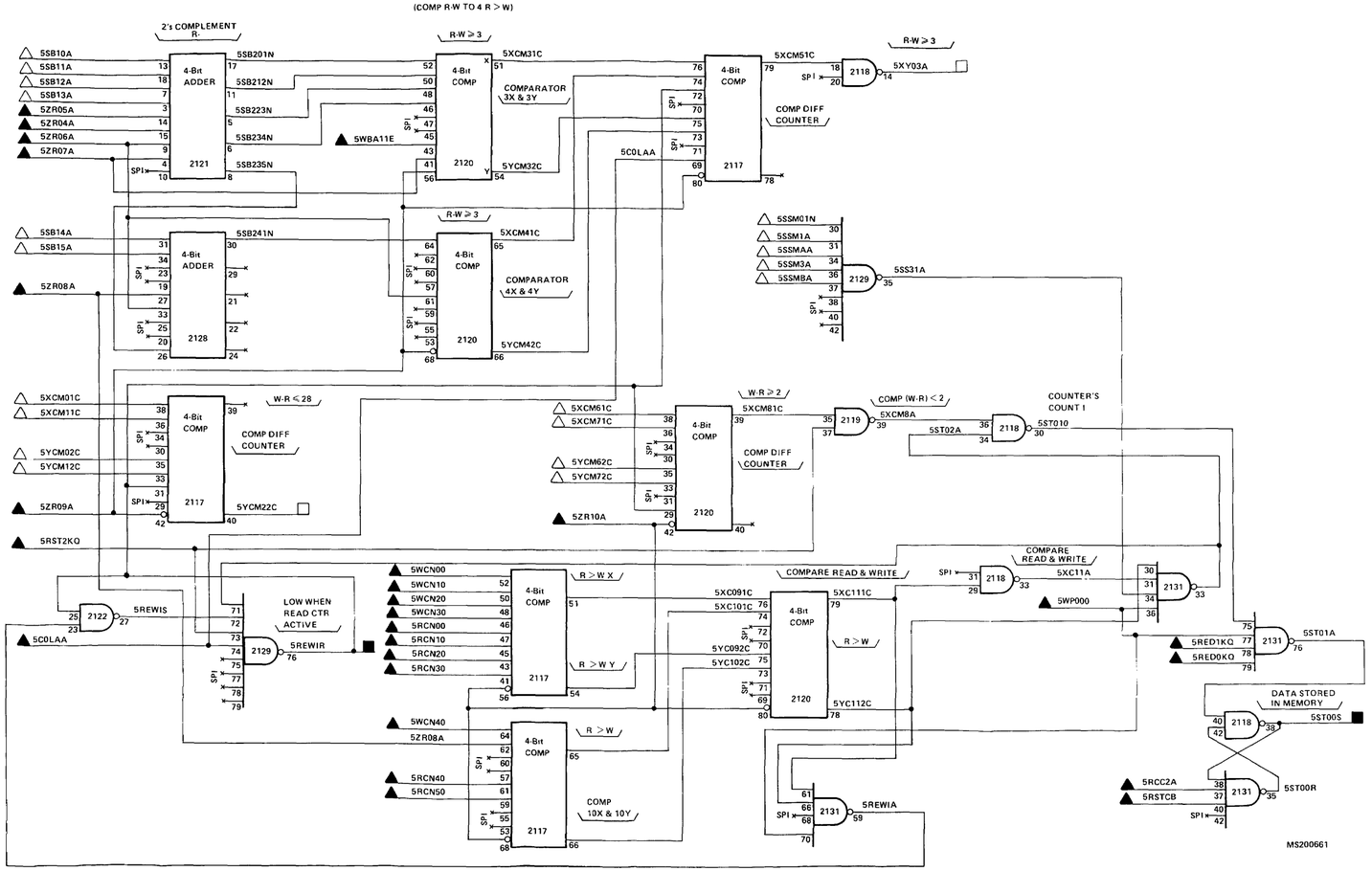
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT		OUTPUT	
	SOURCE		DESTINATION
SIGNAL	FO-SH	SIGNAL	FO-SH
5C0LAA	13400	5REWIR	06600
5C0LAA	26802	5ST00S	07500, 13200, 26802, 31301
5C0LAA	27001		
5RCC2A	06600		
5RCN00	07000		
5RCN10	07000		
5RCN20	07000		
5RCN30	07000		
5RCN40	07000		
5RCN50	07000		
5RED0KQ	06700		
5RED1KQ	06700		
5RSTCB	06600		
5RST2KQ	06600		
5WBA11E	06600		
5WCN00	06900		
5WCN10	06900		
5WCN20	06900		
5WCN30	06900		
5WCN40	06900		
5WP000	06600		
5WP000	30301		
5ZR04A	06600		
5ZR05A	06600		
5ZR06A	06600		
5ZR07A	06600		
5ZR08A	06600		
5ZR09A	06600		
5ZR10A	06600		

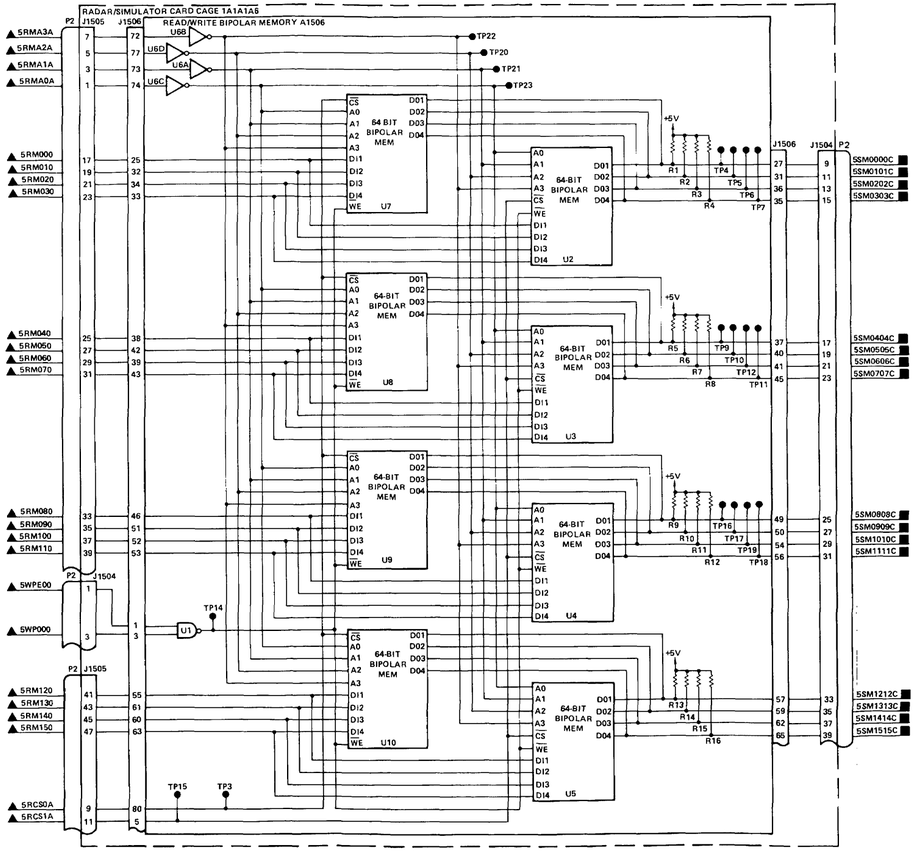


FO-72. Report Buffer Memory Evaluation Logic Diagram (Sheet 2 of 2)

INPUT	
SIGNAL	SOURCE FO-SH
5RCS0A	07100
5RCS0A	30201
5RCS1A	07100
5RCS1A	30201
5RMA0A	07100
5RMA0A	30201
5RMA1A	07100
5RMA1A	30201
5RMA2A	07100
5RMA2A	30201
5RMA3A	07100
5RMA3A	30201
5RM000	06800
5RM000	30201
5RM010	06800
5RM010	30201
5RM020	06800
5RM020	30201
5RM030	06800
5RM030	30201
5RM040	06800
5RM040	30201
5RM050	06800
5RM050	30201
5RM060	06800
5RM060	30201
5RM070	06800
5RM070	30201
5RM080	06800
5RM080	30201
5RM090	06800
5RM090	30201
5RM100	06800
5RM100	30201
5RM110	06800
5RM110	30201

INPUT	
SIGNAL	SOURCE FO-SH
5RM120	06800
5RM120	30201
5RM130	06800
5RM130	30201
5RM140	06800
5RM140	30201
5RM150	06800
5RM150	30201
5WPE00	06600
5WPE00	30301
5WP000	06600
5WP000	30301

OUTPUT	
SIGNAL	DESTINATION FO-SH
5SM0000C	07400, 30301
5SM0101C	07400, 30301
5SM0202C	07400, 30301
5SM0303C	07400, 30301
5SM0404C	07400, 30301
5SM0505C	07400, 30301
5SM0606C	07400, 30301
5SM0707C	07400, 30301
5SM0808C	07400, 30301
5SM0909C	07400, 30301
5SM1010C	07400, 30301
5SM1111C	07400, 30301
5SM1212C	07400, 30301
5SM1313C	07400, 30301
5SM1414C	07400, 30301
5SM1515C	07400, 30301



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.

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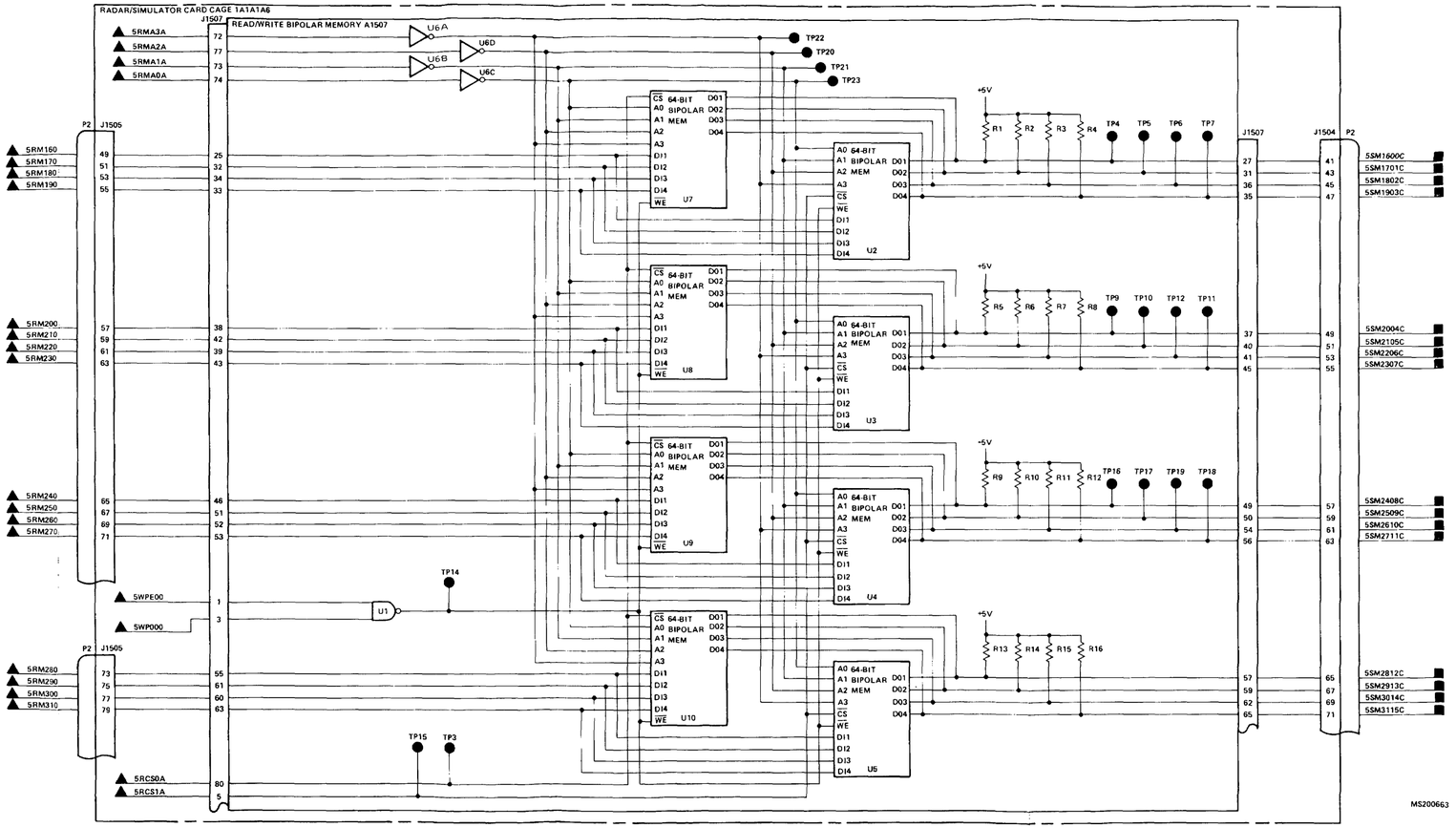
FO-73. Report Buffer Target Report RAM Logic Diagram (Sheet 1 of 2)

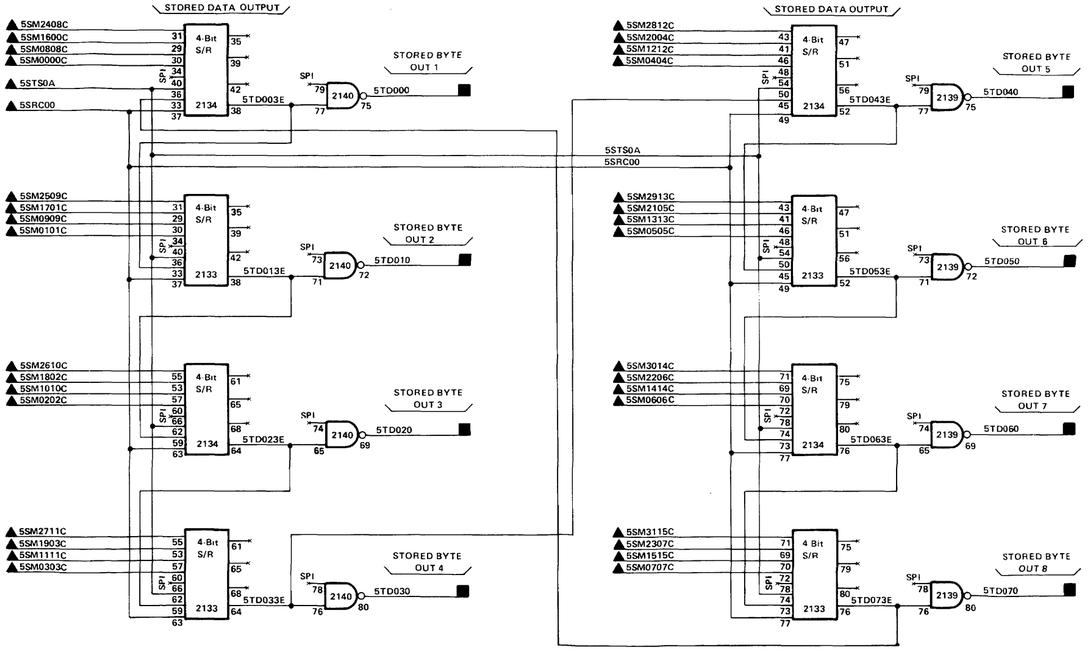
INPUT		INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
5GND505	26803	5RM280	30202	5SM1600C	07400, 30301
5RCS1A	07100	5RM290	06800	5SM1701C	07400, 30301
5RCS1A	30201	5RM290	30202	5SM1802C	07400, 30301
5RMA0A	07100	5RM300	06800	5SM1903C	07400, 30301
5RMA0A	30201	5RM300	30202	5SM2004C	07400, 30302
5RMA1A	07100	5RM31Q	06800	5SM2105C	07400, 30302
5RMA1A	30201	5RM310	30202	5SM2206C	07400, 30302
5RMA2A	07100	5WPE00	06600	5SM2307C	07400, 30302
5RMA2A	30201	5WPE00	30301	5SM2408C	07400, 30302
5RMA3A	07100	5WP000	06600	5SM2509C	07400, 30302
5RMA3A	30201	5WP000	30301	5SM2610C	07400, 30302
5RM160	06800			5SM2711C	07400, 30302
5RM160	30201			5SM2812C	07400, 30302
5RM170	06800			5SM2913C	07400, 30302
5RM170	30202			5SM3014C	07400, 30302
5RM180	06800			5SM3115C	07400, 30302
5RM180	30202				
5RM190	06800				
5RM190	30202				
5RM200	06800				
5RM200	30202				
5RM210	06800				
5RM210	30202				
5RM220	06800				
5RM220	30202				
5RM230	06800				
5RM230	30202				
5RM240	06800				
5RM240	30202				
5RM250	06800				
5RM250	30202				
5RM260	06800				
5RM260	30202				
5RM270	06800				
5RM270	30202				
5RM280	06800				



FO-73. Report Buffer Target Report RAM Logic Diagram (Sheet 2 of 2)

INPUT	
SIGNAL	SOURCE FO-SH
5PSL1JQ	06600
5RCS0A	07100
5RCS0A	30201
5RCS1A	07100
5RCS1A	30201
5RCS2A	07100
5RCS3A	07100
5RDE2A	06600
5RDE2A	26803
5RDE2A	28602
5RED2JQ	06700
5RED4A	06700
5RED4A	26803
5RED4A	28101
5RMA0A	07100
5RMA0A	30201
5RMA1A	07100
5RMA1A	30201
5RMA2A	07100
5RMA2A	30201
5RMA3A	07100
5RMA3A	30201
5RR010	07201
5RWT1JQ	06600
5SFT0A	06700
5ST00S	07202
5ST00S	26803
5ST00S	28101
5WCN53E	06900
5WD400	07000
5WD400	26803
5WD400	28101
6BCP1A	26802
6BCP1A	27201
6BCS1A	27201

OUTPUT	
SIGNAL	DESTINATION FO-SH
5TST33E	05400, 07000, 26802



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INPUT FROM ANOTHER FIGURE
   - △ INPUT FROM SAME FIGURE
   - ■ OUTPUT TO ANOTHER FIGURE
   - □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   - ◪ OUTPUT TO SAME FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.
    - B. REFER TO TABLE 5037 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

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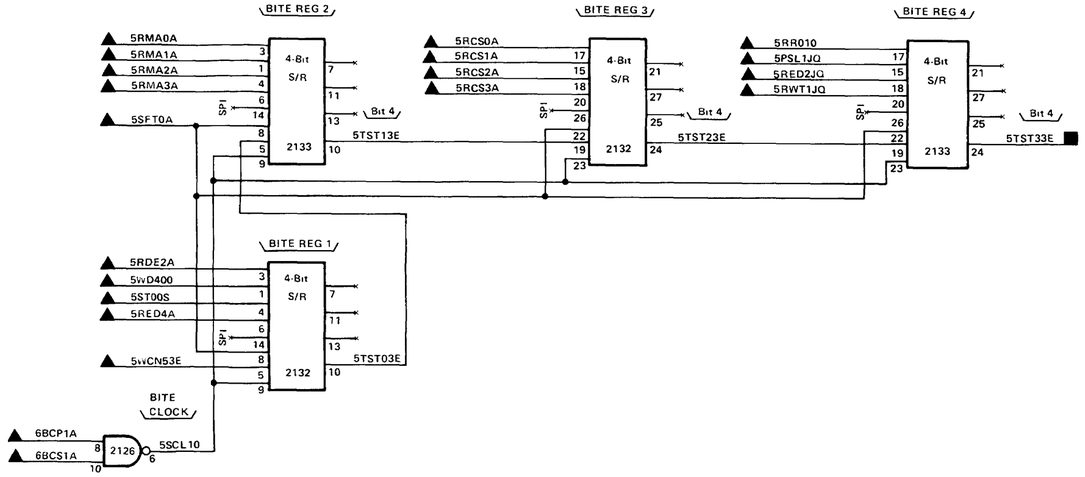
FO-74. Report Buffer Stored Data Output Register Logic diagram

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3MI380V	11600	3AXC1JQ	07900
3MI381E	11600	3AXC1KQ	07900, 08100
3QB06AV	07701	3AXC2JQ	07900
3TA092E	12000	3AXC30	12200, 12300
3TC170	12300	3CTCTJQ	07800
3TFASA	11301	3CTC3B	07800, 08100, 10500, 10700, 11000, 11100, 11700
3TFAS0V	07900		
8BDA0A	17200	3CTP10	12500
8BDA0A	26802	3CTP30	12500
8BDA0A	28500	3CTP40	11309, 11500
8RC120	14600	3CTP50	11100
8RC120	26803	3CTSCB	06500, 07701, 07800, 08200, 08300, 08400, 08900, 09000, 09400, 09500, 09700, 10600, 11100, 11200, 11700, 11900, 12200
8RC120	27501		
8RRZSB	14600		
8RRZSB	26803		
8RRZSB	27502		
8R1XSB	00300	3CTT10	10500
8R1XSB	14900	3CTT2A	08400, 11800
8R1XSB	26802	3CTT20E	09400, 10500
8R1XSB	26803	3CTT3A	11000, 11800
8R1XSB	27501	3CTT31E	09600, 11000
8R8XZB	14300	3CTT4A	11700, 11800
8R8XZB	27201	3CTT42E	08400, 10500, 10700, 11900
		3CTT53E	08900
		3CTT6A	11000
		3CTT60E	10500, 11000, 11100
		3CTT71E	08900, 11100, 11200
		3CTT82E	07800, 08000, 08100, 10700, 11700
		3FAE00V	09200, 26802
		3FAE10V	09200, 26802
		3FAE20V	09200, 26802
		3FAE30V	09200, 26802
		3FAE40V	09200, 26802
		3FAE50V	09200, 26802



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INPUT FROM ANOTHER FIGURE
   - △ INPUT FROM SAME FIGURE
   - ■ OUTPUT TO ANOTHER FIGURE
   - □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
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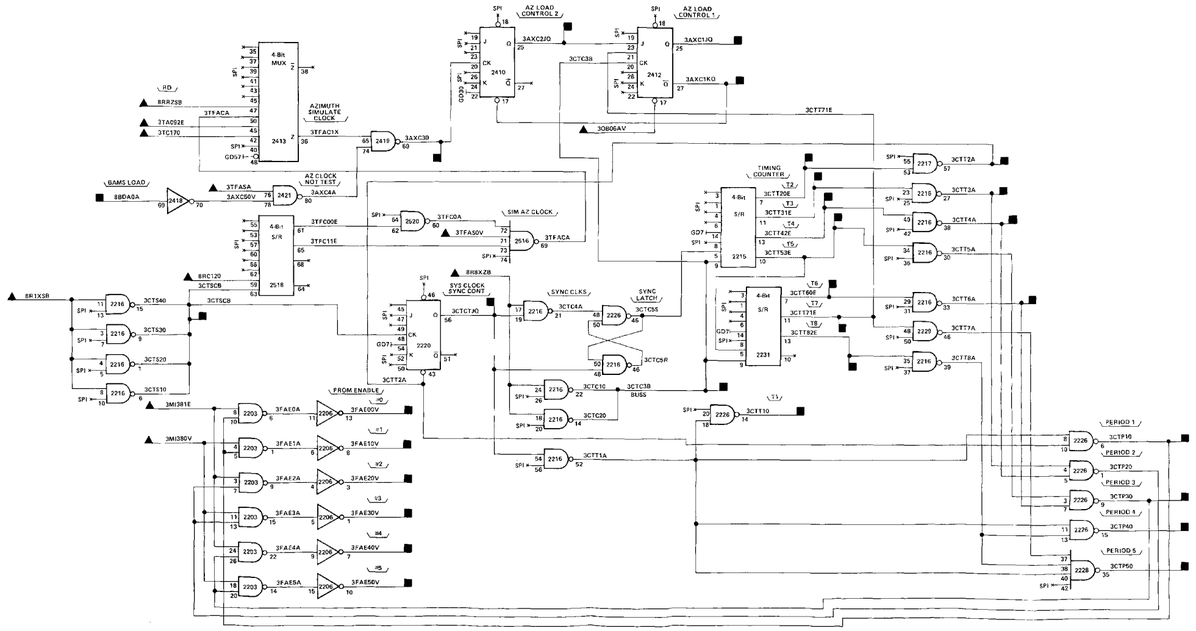
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.
    - B. REFER TO TABLE 5037 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

FO-75. Report Buffer BITE Register Logic Diagram

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SN	SIGNAL	DESTINATION FO-SN
3CCRLJQ	11900	3CCRR0V	08200, 09500, 09700, 11700
3CCRMJQ	11900	3QB00JQ	11800, 11900
3CCRRA	09500	3QB00KQ	11800
3CTSCB	07600	3QB01JQ	08502, 12200
3MA0VA	11900	3QB01KQ	08400
3MZ562X	11308	3QB04AV	09000
3MZ612X	11308	3QB04R	08400, 11100
3MZ622X	11308	3QB04S	07800, 08200, 10700
3MZ631X	11308	3QB06AV	07600, 08100, 08400
3QE02A	08400	3QB07AV	08400, 09500
3QW412E	08502	3QB08AV	08200, 09500
5KED1A	13400	3QB09AV	10500, 10600
5KED1A	26803	3QB10AV	09500, 11100, 11900
5KED1A	31301	3QB11AV	08300, 08502, 11000, 11900
61DG2A1	05500	3QB12AV	08502, 08900, 09000, 09700,
8RRZSB	26802		11700
8RRZSB	14600	3QB13A	08100, 09400, 10600, 11700
8RRZSB	26803	3QB14AV	07800, 11900
8RRZSB	27502	3QE00A	13101, 26802, 31301



FO-76. TPU Azimuth Load Control Timing Counter Logic diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
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6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

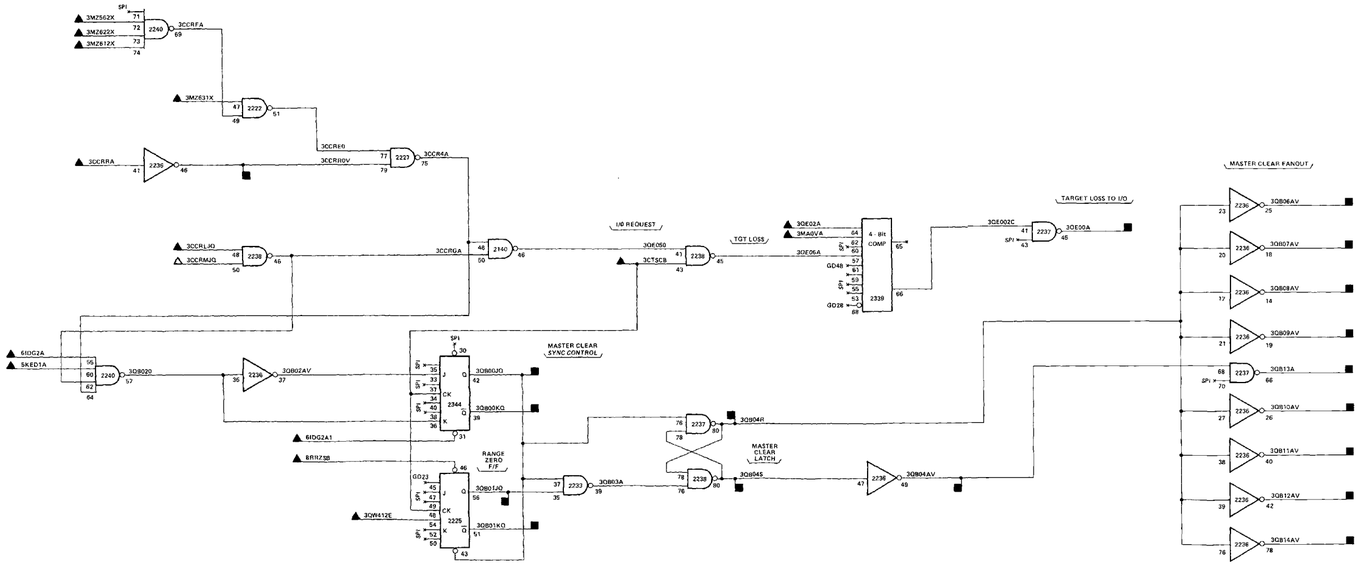
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3AMD4A	08100	3APCKD	08000
3AMD40V	08100	3ARB20	08700, 09800
3CMD1JQ	08200	3ARC30	08100
3CTCTJQ	07600	3ARC40	09900, 10100
3CTC3B	07600	3ARC7S	09700, 09800
3CTSCB	07600	3ARC9A	08200
3CTT82E	07600		
3QB04S	07701		
3QB14AV	07701		
3QC07S	08400		
3QZSSJQ	08502		
3QZSSQV	08200		
3QZ412E	08502		
3TIM1TB	08300		
8RRZSB	14600		
8RRZSB	26803		
8RRZSB	27502		



FO-77. TPU Master Clear Ground sources and Target Loss Logic Diagram (Sheet 1 of 2)

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
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6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

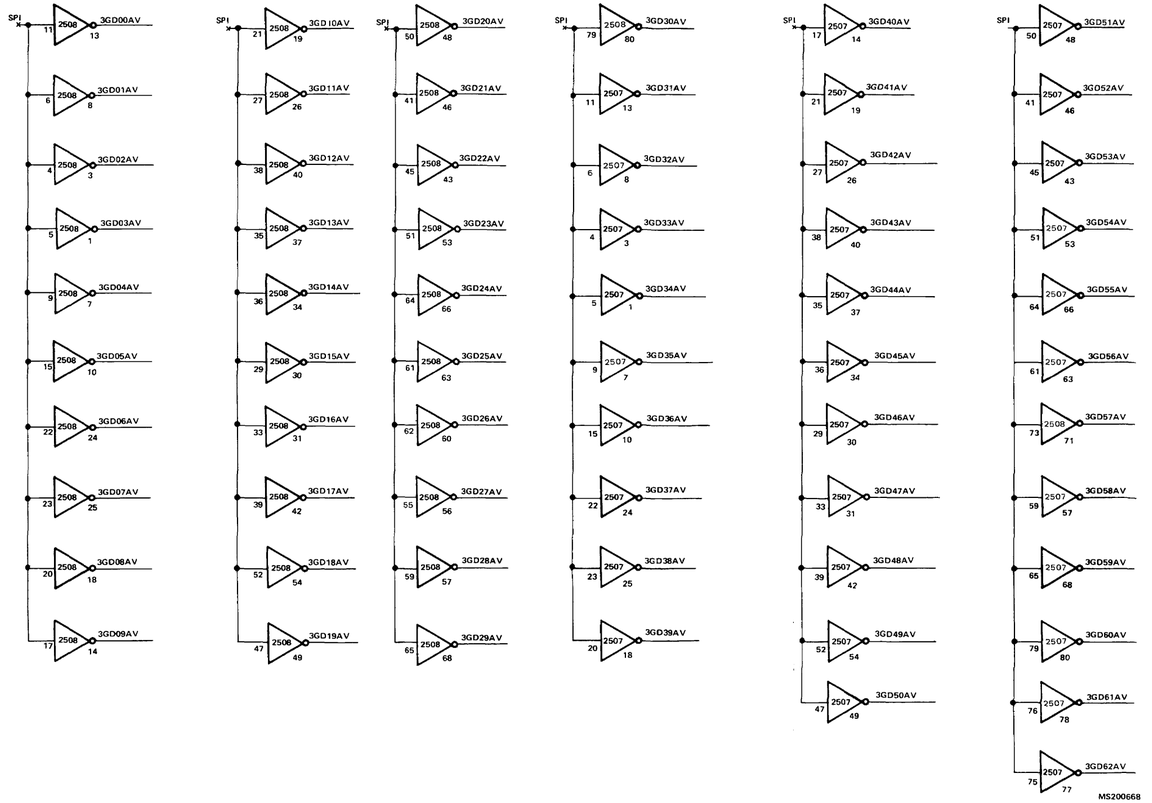
    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

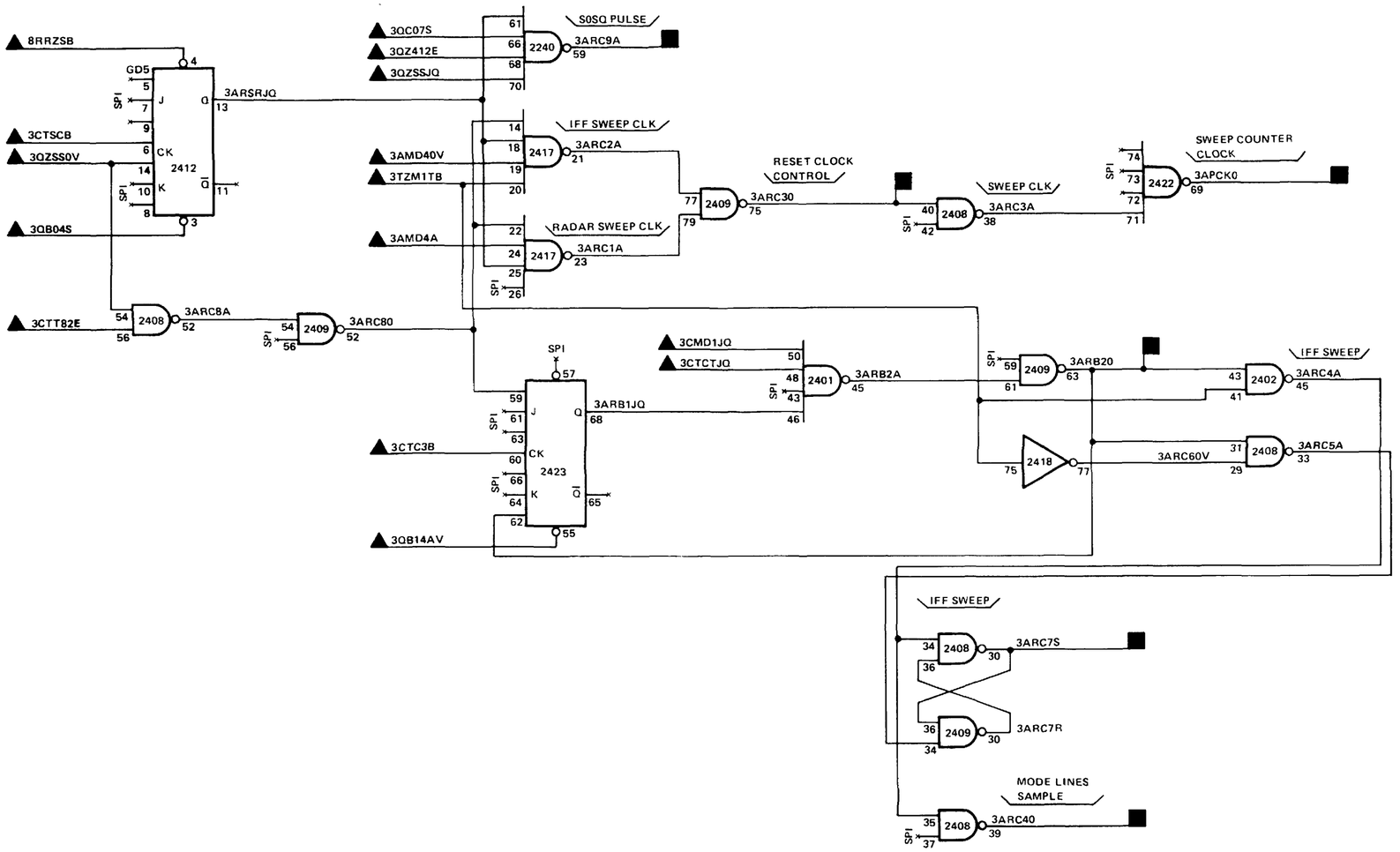
INPUT	
SIGNAL	SOURCE FO-SH
5SM0000C	07301
5SM0000C	26802
5SM0000C	30301
5SM0101C	07301
5SM0101C	26802
5SM0101C	30301
5SM0202C	07301
5SM0202C	26802
5SM0202C	30301
5SM0303C	07301
5SM0303C	26802
5SM0303C	30301
5SM0404C	07301
5SM0404C	26802
5SM0404C	30301
5SM0505C	07301
5SM0505C	26802
5SM0505C	30301
5SM0606C	07301
5SM0606C	26802
5SM0606C	30301
5SM0707C	07301
5SM0707C	26802
5SM0707C	30301
5SM0808C	07301
5SM0808C	26802
5SM0808C	30301
5SM0909C	07301
5SM0909C	26802
5SM0909C	30301
5SM1010C	07301
5SM1010C	26802
5SM1010C	30301
5SM1111C	07301
5SM1111C	26802
5SM1111C	30301
5SM1212C	07301
5SM1212C	26802
5SM1212C	30301
5SM1313C	07301
5SM1313C	26802
5SM1313C	30301
5SM1414C	07301
5SM1414C	26802
5SM1414C	30301
5SM1515C	07301

INPUT	
SIGNAL	SOURCE FO-SH
5SM1515C	26802
5SM1515C	30301
5SM1600C	07302
5SM1600C	26802
5SM1600C	30301
5SM1701C	07302
5SM1701C	26802
5SM1701C	30301
5SM1802C	07302
5SM1802C	26802
5SM1802C	30301
5SM1903C	07302
5SM1903C	26802
5SM1903C	30301
5SM2004C	07302
5SM2004C	26802
5SM2004C	30302
5SM2105C	07302
5SM2105C	26802
5SM2105C	30302
5SM2206C	07302
5SM2206C	26802
5SM2206C	30302
5SM2307C	07302
5SM2307C	26802
5SM2307C	30302
5SM2408C	07302
5SM2408C	26802
5SM2408C	30302
5SM2509C	07302
5SM2509C	26802
5SM2509C	30302
5SM2610C	07302
5SM2610C	26802
5SM2610C	30302
5SM2711C	07302
5SM2711C	26802
5SM2711C	30302
5SM2812C	07302
5SM2812C	26802
5SM2812C	30302
5SM2913C	07302
5SM2913C	26802
5SM2913C	30302
5SM3014C	07302
5SM3014C	26802
5SM3014C	30302
5SM3115C	07302
5SM3115C	26802
5SM3115C	30302
5SRC00	06700
5STS0A	06700

OUTPUT	
SIGNAL	DESTINATION FO-SH
5TD000	13700, 26802, 31301
5TD010	13800, 26802, 31301
5TD020	13700, 26802, 31301
5TD030	13700, 26802, 31301
5TD040	13700, 26802, 31301
5TD050	13700, 26802, 31301
5TD060	13700, 26802, 31301
5TD070	13700, 26802, 31301



FO-77. TPU Master Clear Ground Sources and Target Loss Logic Diagram (Sheet 2 of 2)



FO-78. TPU Sweep/Sample Clocks Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

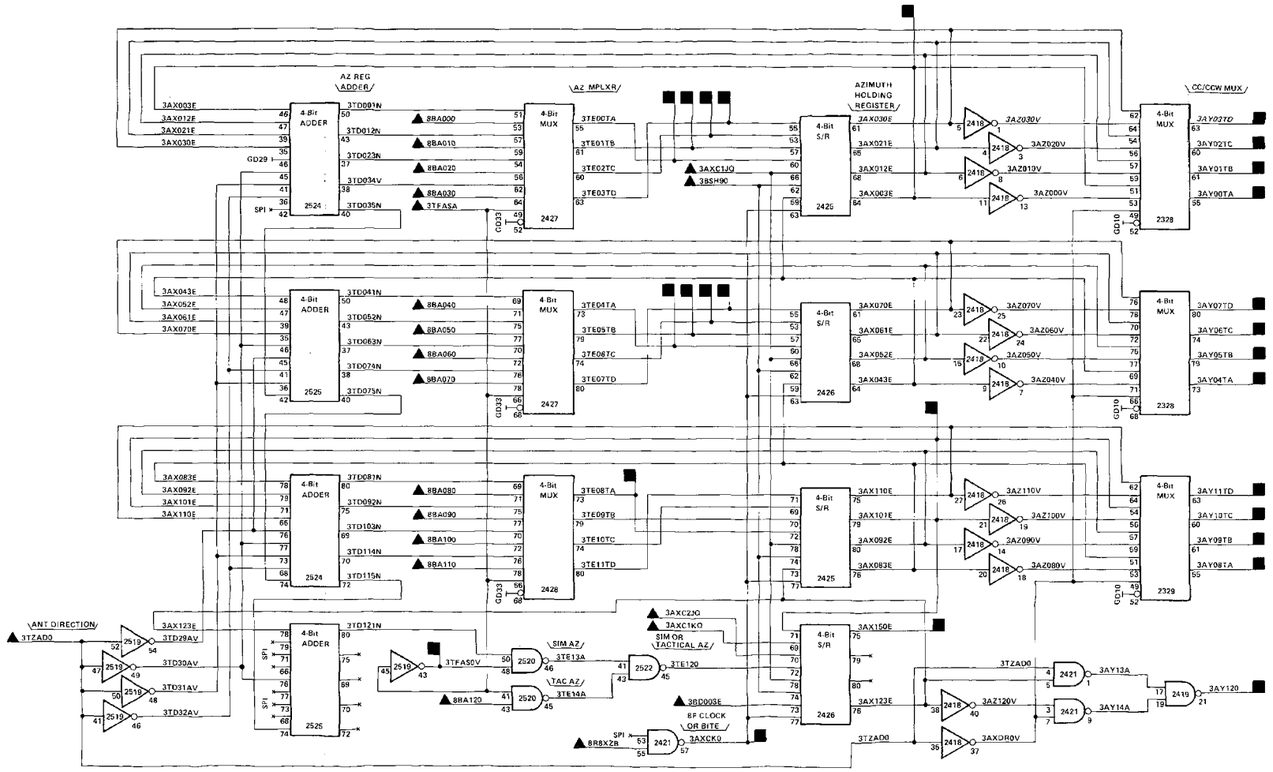
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3AXC1JQ	07600	3AXCKO	08100, 12500
3AXC1KQ	07600	3AX003E	08100
3AXC2JQ	07600	3AX101E	11900
3BD003E	12500	3AX150E	11900
3BD003E	26802	3AY00TA	10800, 11303, 11306
3BSH90	12500	3AY01TB	10800, 11303, 11306
3TFASA	11301	3AY02TC	10800, 11303, 11306
3TZAD0	11301	3AY03TD	10800, 11303, 11307
8BA000	17102	3AY04TA	10800, 11303, 11307
8BA000	26802	3AY05TB	10800, 11303, 11307
8BA000	28500	3AY06TC	10800, 11303, 11307
8BA010	17102	3AY07TD	10800, 11303, 11307
8BA010	26802	3AY08TA	10800, 11304, 11307
8BA010	28500	3AY09TB	10800, 11304, 11307
8BA020	17102	3AY10TC	10800, 11304, 11307
8BA020	26802	3AY11TD	10800, 11304, 11307
8BA020	28500	3AY120	10800, 11304, 11307
8BA030	17102	3TE00TA	12000
8BA030	26802	3TE01TB	12000
8BA030	28500	3TE02TC	12000
8BA040	17102	3TE03TD	12000
8BA040	26802	3TE04TA	12000
8BA040	28500	3TE05TB	12000
8BA050	17102	3TE06TC	12000
8BA050	26802	3TE07TD	12000
8BA050	28500	3TE08TA	12000
8BA060	17102	3TFASOV	07600, 11301
8BA060	26802		
8BA060	28500		
8BA070	17102		
8BA070	26802		
8BA070	28500		
8BA080	17102		
8BA080	26802		
8BA080	28500		
8BA090	17102		
8BA090	26802		
8BA090	28500		
8BA100	17102		
8BA100	26802		
8BA100	28500		
8BA110	17102		
8BA110	26802		
8BA110	28500		
8BA120	17102		
8BA120	26802		
8BA120	28500		
8R8XZB	14300		
8R8XZB	27201		



FO-79. TPU Azimuth Holding Register Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

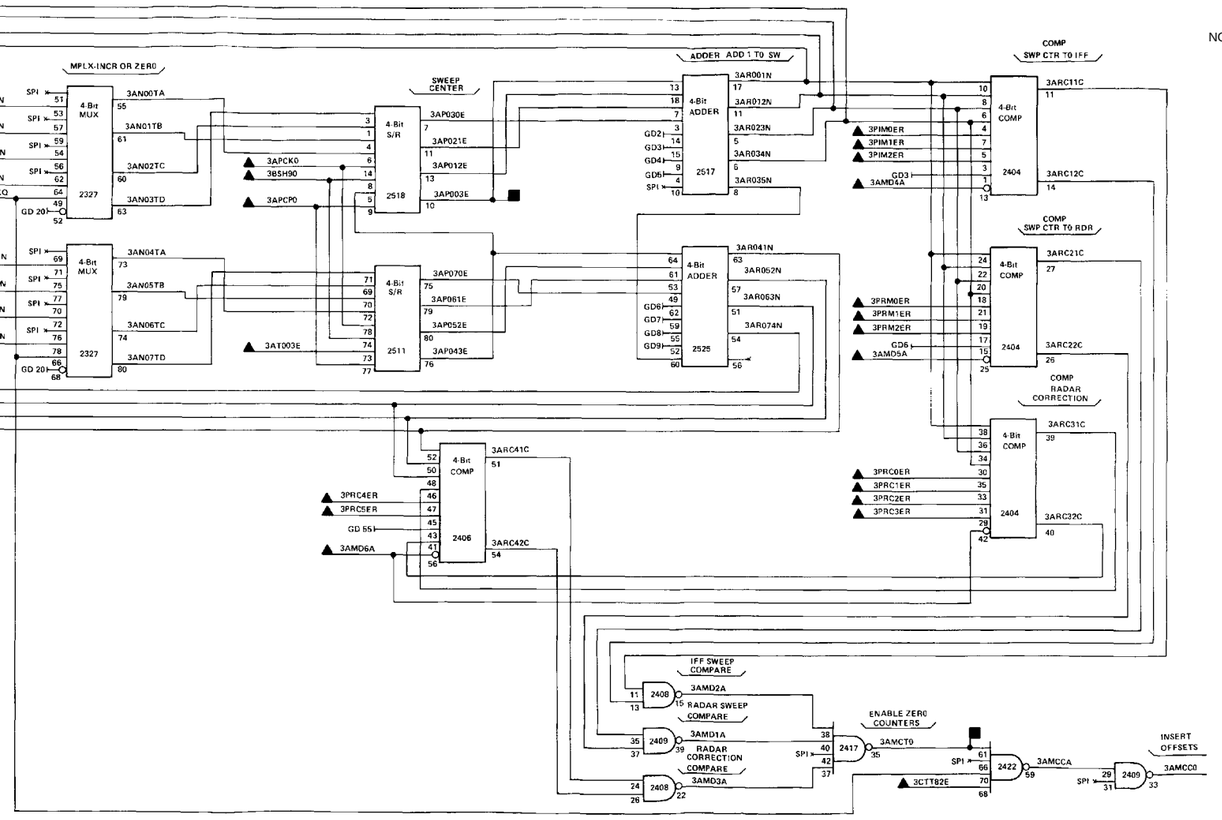
    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT	
SIGNAL	SOURCE FO-SH
3AMD4A	08100
3AMD5A	08100
3AMD6A	08100
3APCKO	07800
3APCPO	08100
3ARD3KQ	08100
3AT003E	08100
3BSH90	12500
3CTT82E	07600
3PIM0ER	12000
3PIM1ER	12000
3PIM2ER	12000
3PRC0ER	12100
3PRC1ER	12100
3PRC2ER	12100
3PRC3ER	12100
3PRC4ER	12100
3PRC5ER	12100
3PRM0ER	12000
3PRM1ER	12000
3PRM2ER	12000

OUTPUT	
SIGNAL	DESTINATION FO-SH
3AMCC0	08100
3AMCT0	08100
3AP003E	08100



FO-80. TPU IFF/Radar Sweep Compare and Correction Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

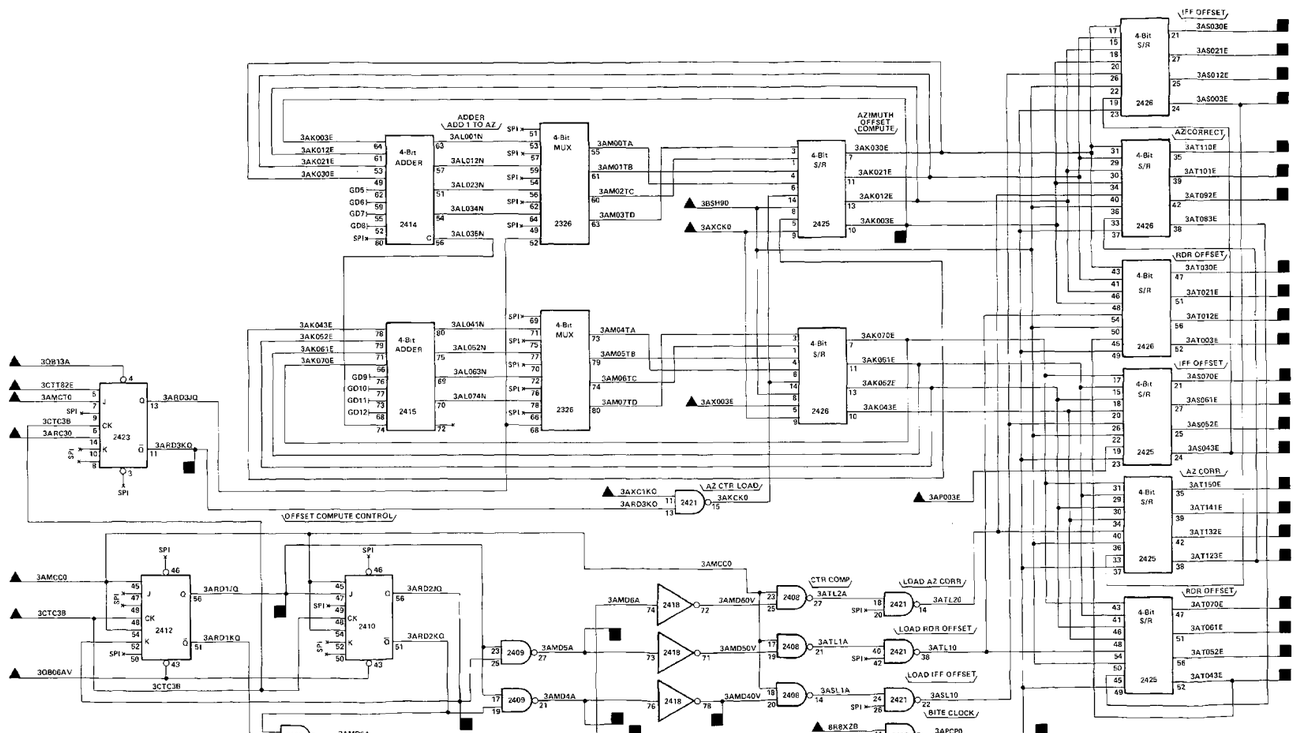
    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

MS200671

INPUT SIGNAL	INPUT SOURCE FO-SH	OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
3AMCC0	08000	3AK003E	12500
3AMCT0	08000	3AMD4A	07800, 08000
3AP003E	08000	3AMD40V	07800
3ARC30	07800	3AMD5A	08000
3AXC1KQ	07900	3AMD6A	08000
3AXC1KQ	07600	3APCP0	08000
3AX003E	07900	3ARD1JQ	12500
3BSH90	12500	3ARD2JQ	12500
3CTC3B	07600	3ARD3KQ	08000
3CTT82E	07600	3AS003E	10800
3QB06AV	07701	3AS012E	10800
3QB13A	07701	3AS021E	10800
6MCB1A	05600	3AS030E	10800
6MCB1A	27201	3AS043E	10800
8R8X2B	14300	3AS052E	10800
8R8X2B	27201	3AS061E	10800
		3AS070E	10800
		3AT003E	08000, 10800, 12500
		3AT012E	10800
		3AT021E	10800
		3AT030E	10800
		3AT043E	10800
		3AT052E	10800
		3AT061E	10800
		3AT070E	10800
		3AT092E	10700
		3AT101E	10700
		3AT110E	10700
		3AT123E	10700
		3AT132E	10700
		3AT141E	10700
		3AT150E	10700



FO-81. TPU IFF/Radar Offset and Correction Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

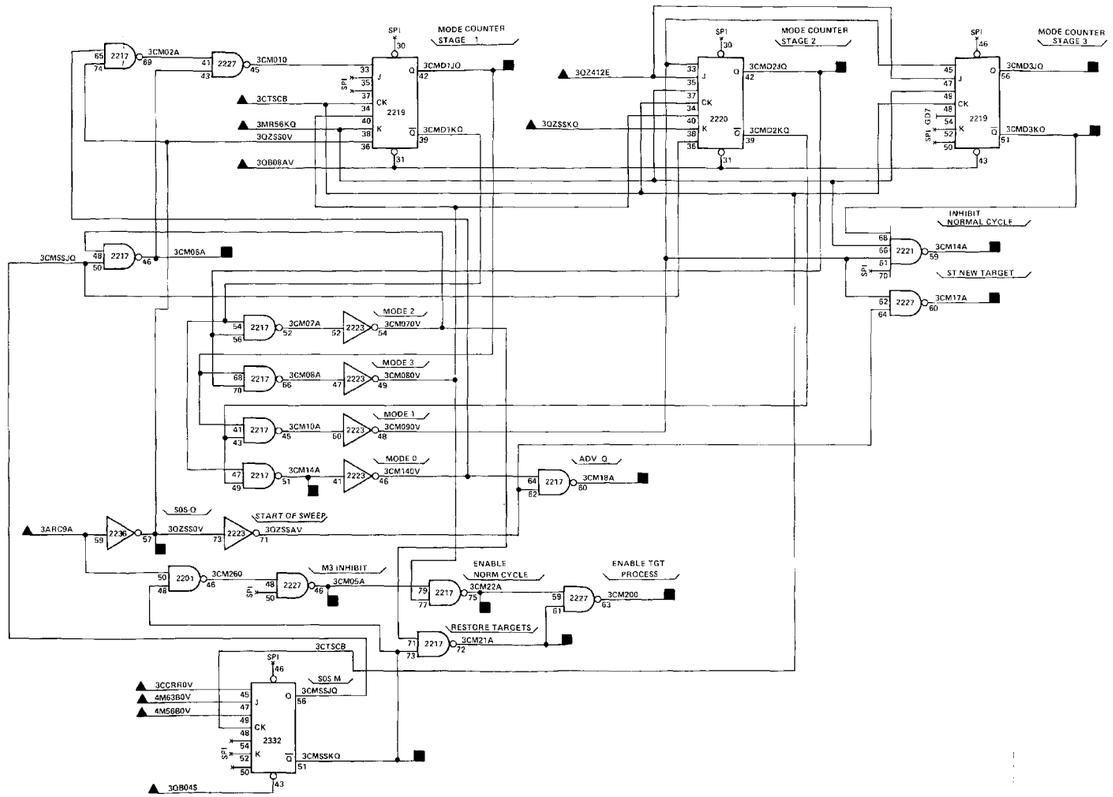
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT		OUTPUT	
	SOURCE	DESTINATION	
SIGNAL	FO-SH	SIGNAL	FO-SH
3ARC9A	07800	3CMD1JQ	07800, 09600, 12500
3CCRR0V	07701	3CMD2JQ	08900, 11700, 12500
3CTSCB	07600	3CMD3JQ	08502, 11700, 11900, 12500
3MR56KQ	11700	3CMD3KQ	11700
3QB04S	07701	3CMSSKQ	11900
3QB08AV	07701	3CM05A	09600
3QZSSKQ	08502	3CM06A	09600
3QZ412E	08502	3CM14A	09600
4M56B0V	06402	3CM17A	09000
4M56B0V	26802	3CM18A	08400
4M63B0V	06402	3CM19A	09500, 09600
		3CM20D	09600, 09800, 10600, 11100, 12500
		3CM21A	09000
		3CM22A	09000
		3QZSS0V	07800



FO-82. TPU Mode Counter and Control Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

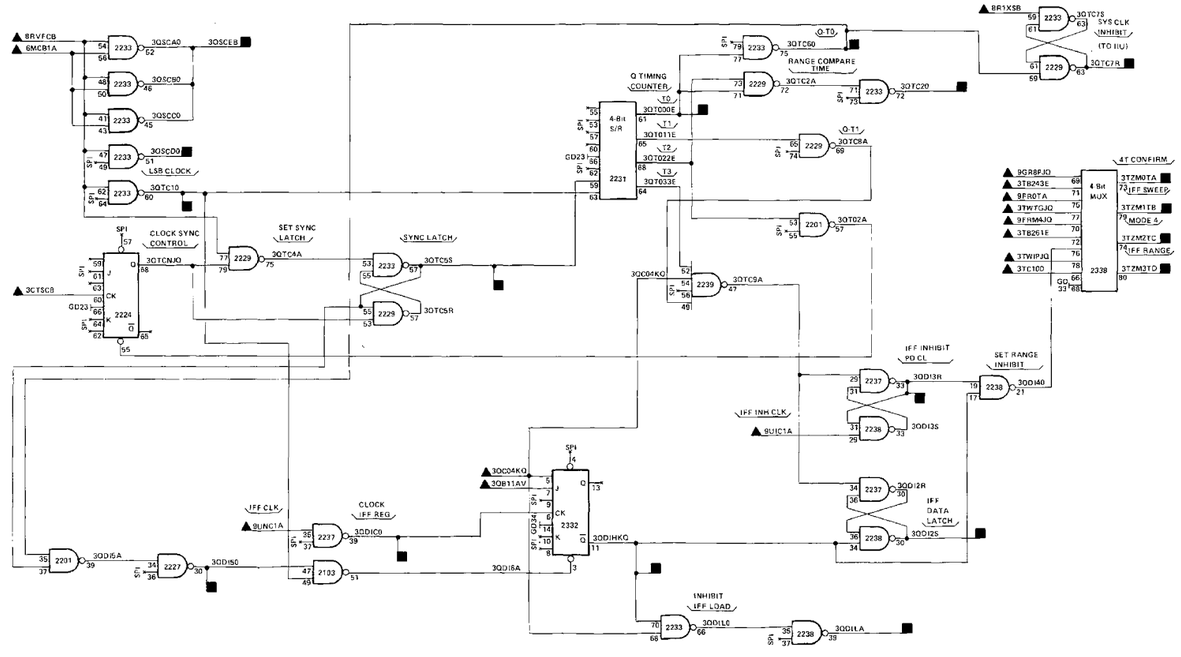
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3CTSCB	07600	3QDICO	08503
3QB11AV	07701	3QDIHKQ	11200
3QC04KQ	08400	3QDILA	08503
3TB243E	12100	3QDI2S	08400, 08503
3TB261E	12100	3QDI3R	08400
3TC100	12300	3QDI5D	08400
3TWIPJQ	12200	3QSCDO	08502, 12200
3TWTGJQ	12200	3QSCEB	08501, 08502, 08503, 12500
6MCB1A	05600	3QTC1D	08400, 09000
6MCB1A	27201	3QTC2D	09000
8RVFCB	14700	3QTC5S	08400, 08502
8RVFCB	26802	3QTC6D	08400, 08502
8RVFCB	27502	3QTC7R	19600, 20400, 26802, 27302
8R1XSB	003DD	3QTODDE	08700
8R1XSB	14900	3TZMOTA	10100
8R1XSB	26802	3TZM1TB	07800
8R1XSB	26803	3TZM2TC	10100
8R1XSB	27501	3TZM3TD	08400
9FRM4JQ	18700		
9FRM4JQ	26802		
9FRM4JQ	27302		
9FROTA	18700		
9FROTA	26802		
9FROTA	27301		
9GR8PJQ	19200		
9GR8PJQ	26802		
9GR8PJQ	27302		
9UIC1A	20400		
9UIC1A	26802		
9UIC1A	27301		
9UNC1A	20400		
9UNC1A	27301		



FO-83. TPU Queue Timing and IFF Data Synchronizer Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

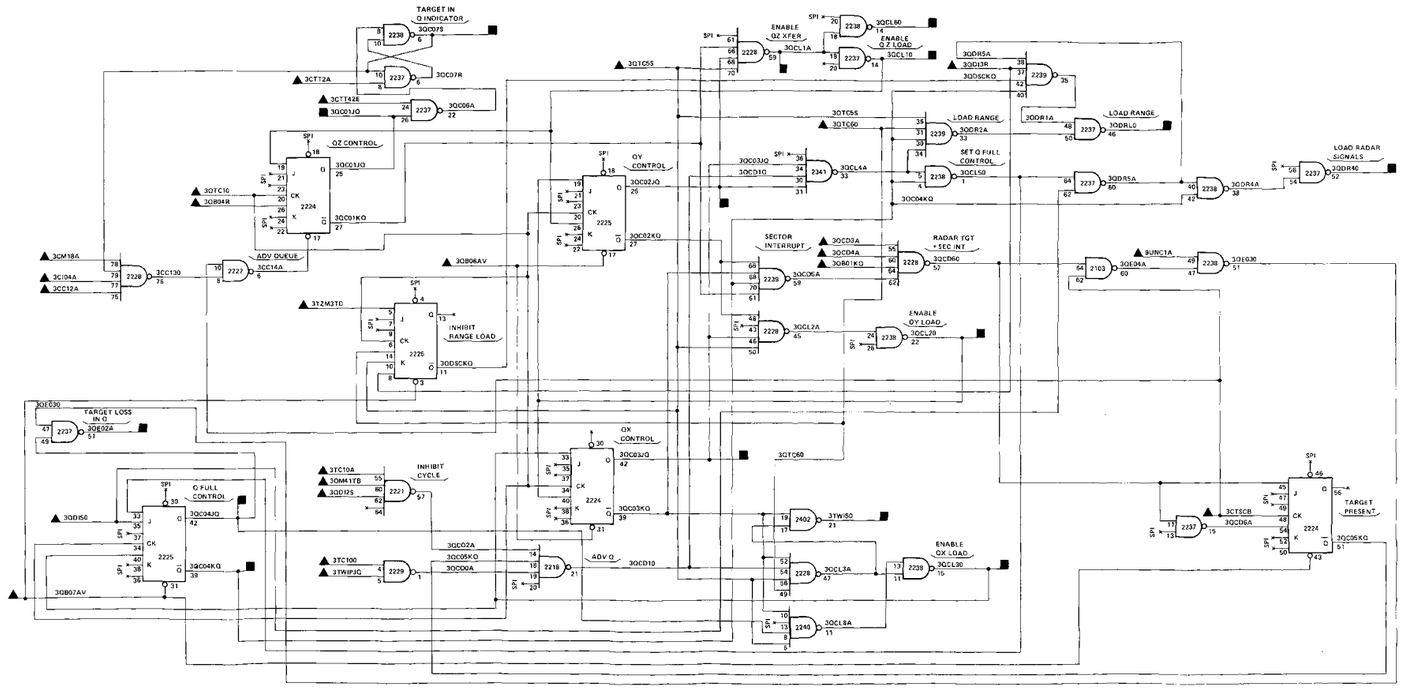
    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT	
SIGNAL	SOURCE FO-SH
3CC12A	09500
3CJ04A	09600
3CM18A	08200
3CT6CB	07600
3CT72A	07600
3CTT42E	07600
3QB01KQ	07701
3QB04R	07701
3QB06AV	07701
3QB07AV	07701
3QCD3A	08502
3QCD4A	08502
3QD12S	08300
3QD13R	08300
3QD15Q	08300
3QM417B	08502
3QTC1D	08300
3QTC5S	08300
3QTC6Q	08300
5TC10A	12300
3TC10D	12300
3TW1PJQ	12200
3T2M3TD	08300
9UNC1A	20400
9UNC1A	27301

OUTPUT	
SIGNAL	DESTINATION FO-SH
3QCL1A	09000
3QCL10	08501, 08502, 08503
3QCL20	08501, 08502, 08503
3QCL30	08501, 08502, 08503
3QCL60	08502
3QC01JQ	12500
3QC02JQ	12500
3QC03JQ	12500
3QC04JQ	12500
3QC04KQ	08300
3QC07S	07800, 09100, 09600
3QDRL0	08501
3QDR4D	08502
3QED2A	07701
3TW150	12200



FO-84. TPU Queue Register Transfer Control Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

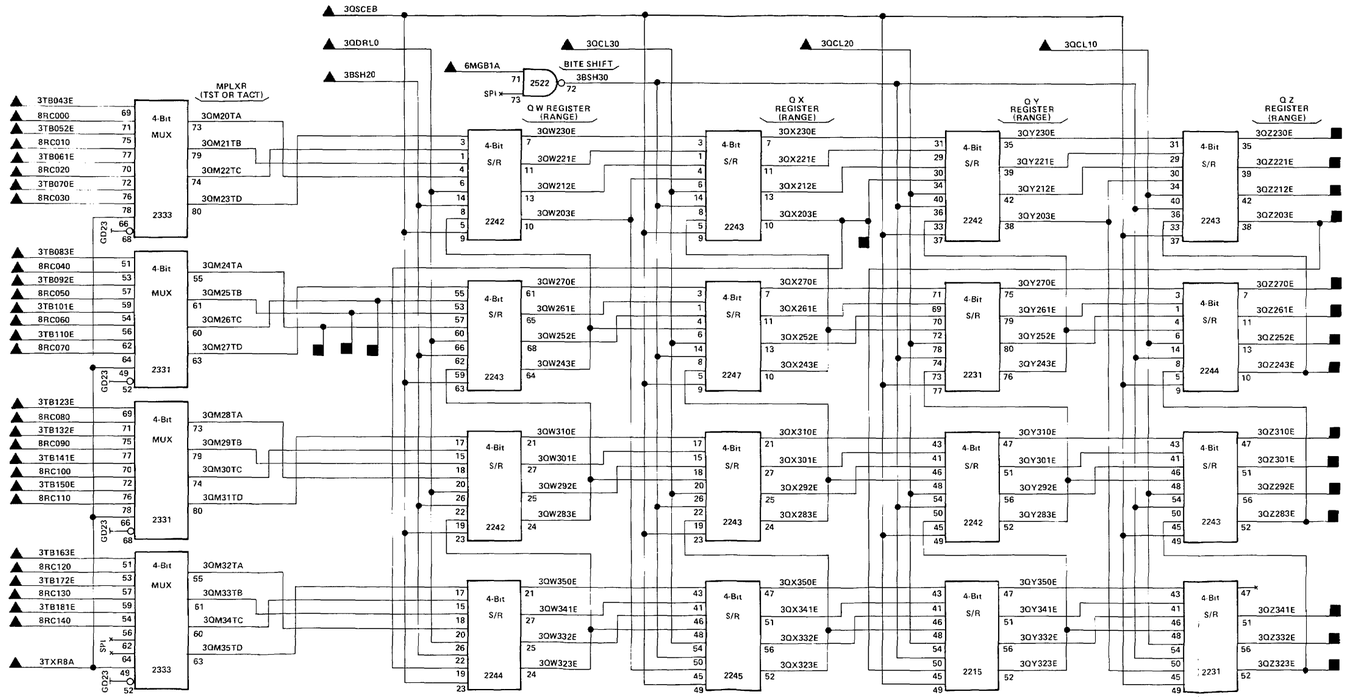
    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT SIGNAL	INPUT SOURCE FO-SH
3BSH20	12500
3QCL10	08400
3QCL20	08400
3QCL30	08400
3QDRL0	08400
3QSCEB	08300
3TB043E	12100
3TB052E	12100
3TB061E	12100
3TB070E	12100
3TB083E	12100
3TB092E	12100
3TB101E	12100
3TB110E	12100
3TB123E	12100
3TB132E	12100
3TB141E	12100
3TB150E	12100
3TB163E	12100
3TB172E	12100
3TB181E	12100
3TXR8A	12200
8RC000	14900
8RC000	27501
8RC010	14900
8RC010	27501
8RC020	14900
8RC020	26803
8RC020	27501
8RC030	14900
8RC030	26803
8RC030	27501
8RC040	14900
8RC040	26803
8RC040	27501
8RC050	14900
8RC050	26803
8RC050	27501
8RC060	14900
8RC060	26803
8RC060	27501
8RC070	14900
8RC070	26803
8RC070	27501
8RC080	14900
8RC080	26803

INPUT SIGNAL	INPUT SOURCE FO-SH
8RC080	27501
8RC090	14900
8RC090	26803
8RC090	27501
8RC100	14600
8RC100	26802
8RC100	26803
8RC100	27501
8RC110	14600
8RC110	26802
8RC110	26803
8RC110	27501
8RC120	14600
8RC120	26803
8RC120	27501
8RC130	14600
8RC130	26802
8RC130	26803
8RC130	27501
8RC140	14600
8RC140	26802
8RC140	26803
8RC140	27501

OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
3QM24TA	12100
3QM25TB	12100
3QM26TC	12100
3QX203E	12500
3QZ203E	08600, 11304, 12500
3QZ212E	08600, 11304
3QZ221E	08600, 11304
3QZ230E	08600, 08800, 11304
3QZ243E	08600, 08800, 11305
3QZ252E	08600, 08800, 11305
3QZ261E	08600, 08800, 11305
3QZ270E	08600, 08800, 11305
3QZ283E	08600, 08800, 11306
3QZ292E	08600, 08800, 11306
3QZ301E	08600, 08800, 11306
3QZ310E	08600, 08800, 11306
3QZ323E	08600, 08800, 11306
3QZ332E	08600, 08800, 11306
3QZ341E	08600, 08800, 11306



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUITCATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FORMTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

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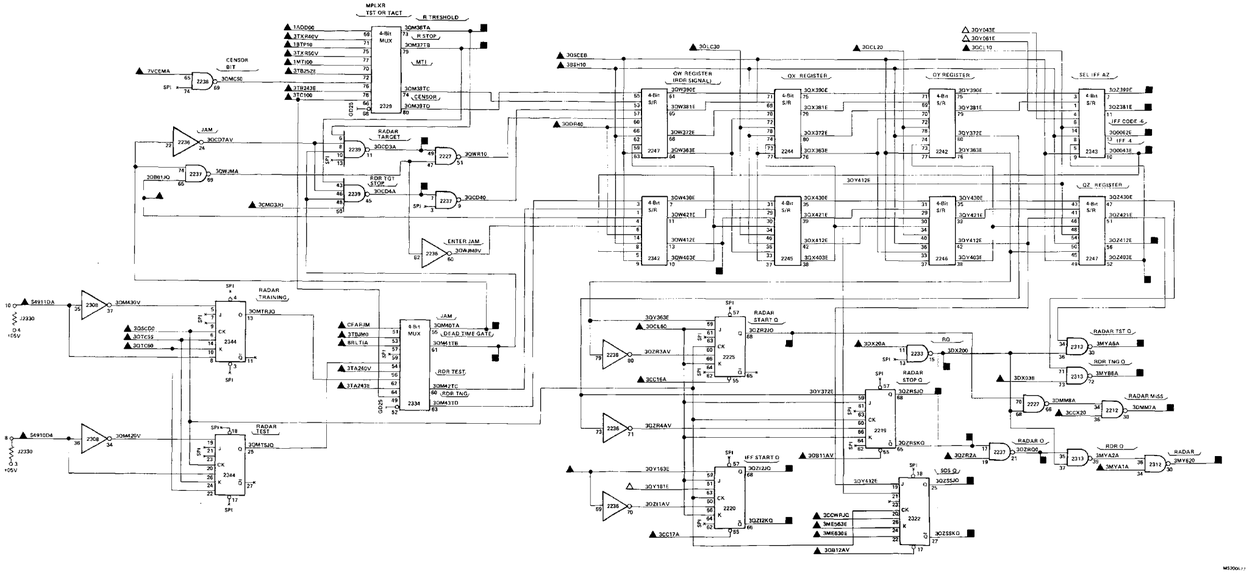
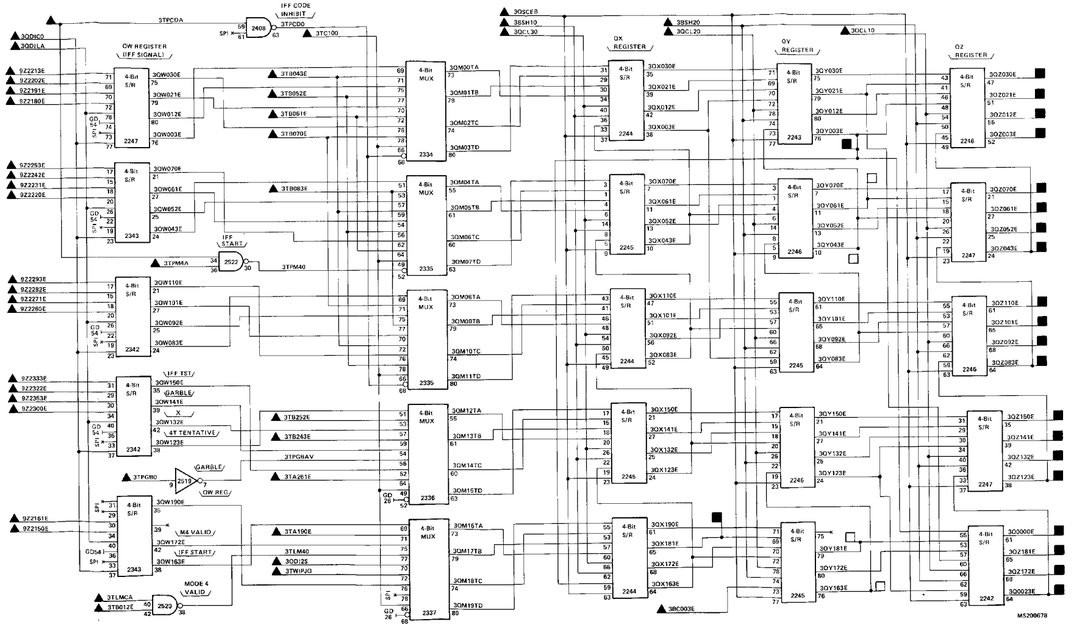
FO-85.TPU Quene Input Multiplexing and Registers Logic Diagram (Sheet 1 of 3)

INPUT		INPUT		OUTPUT	
SIGNAL	SOURCE FO-SM	SIGNAL	SOURCE FO-SM	SIGNAL	DESTINATION FO-SM
CFARJM	26802	3TA2436	12000	3GMM7A	09700
5491084	26802	3TBJM0	12000	3GA200	09300
5491084	31302	3TB2436	12100	3KYAAA	09300
5491184	26802	3TB2528	12300	3MYB6A	09300
5491184	31302	3TC100	12300	3MY620	11306
1A0000	16300	3TA4406	12200	3GC03A	08400
1BTP10	16300	3TA4306	12200	3KC04A	08400
1BTP10	27502	7VCEMA	03900	3GM58AA	05900, 26802
1MT100	15300	7VCEMA	26802	3GM37TB	05900, 26802
1MT100	26802	8RLT1A	14500	3GM40TA	09800
3B3N1G	12500	8RLT1A	26802	3GM41TB	08400
3CCWRJG	09500	8RLT1A	26901	3GM61TC	07701
3CCX20	09500			3GT37JG	09100
3CC19A	09400			3GJ22KG	09600
3CC17A	09400			3GJ82JG	09600
3CM03JG	08200			3GZ83JG	09400
3GX03B	09200			3GZ92KG	09100
3KA03B	29601			3GZ92JG	09300, 09400
3KA20A	09500			3GJ55KG	07800, 11700
3ME563E	11400			3GJ55KG	08200, 09600
3ME630E	11400			3GJ38JG	09300, 09700
3M1A7J	09300			3GJ39DE	09300
3GDD14G	07701			3GZ43JE	09700, 09900, 09600
3GM12AV	07701			3GC42JE	07800, 08200
3KKLJC	08400			3GDDA3E	10300, 12500
3KKLJG	08400			3GDDA2E	10300
3KCL3G	08400				
3KCLA0	08400				
3KKA4G	08400				
3KSCPG	08300				
3KSCEB	08300				
3GTC3E	08300				
3KTC4G	08300				
3GJK2A	09400				
3TA240V	12000				



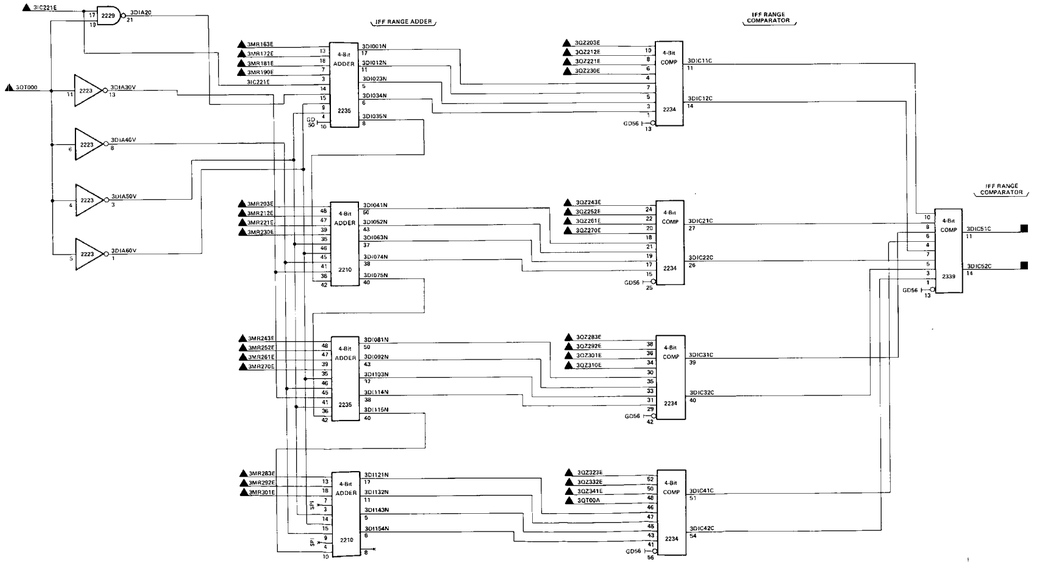
FO-85.TPU Quene Input Multiplexing and Registers Logic Diagram (Sheet 2 of 3)

INPUT SIGNAL	INPUT SOURCE FO-SH	INPUT SIGNAL	INPUT SOURCE FO-SH	OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
3BC003E	12500	922220E	27301	3QX181E	12200
3BSH10	12500	922231E	20300	3QY003E	12500
3BSH20	12500	922231E	26802	3QZ003E	10000, 11303, 11304, 12500
3QCL10	08400	922231E	27301	3QZ012E	10000, 10300, 11303, 11304
3QCL20	08400	922242E	20300	3QZ021E	10000, 11303, 11304
3QCL30	08400	922242E	26802	3QZ030E	10000, 10300, 11303, 11304
3QD1C	08300	922242E	27301	3QZ043E	10000, 11303, 11304
3QD1LA	08300	922253E	20300	3QZ052E	10000, 10300, 11303, 11304
3QD12S	08300	922253E	26802	3QZ061E	10000, 11303, 11304
3QSCEB	08300	922260E	20300	3QZ070E	10000, 10300, 11303, 11304
3TA190E	12000	922260E	26802	3QZ083E	10000, 11303, 11305
3TA261E	12000	922271E	20300	3QZ092E	10000, 11303, 11305
3TB012E	12100	922271E	26802	3QZ101E	10000, 11304, 11305
3TB043E	12100	922271E	27301	3QZ110E	10000, 11304, 11305
3TB052E	12100	922282E	20300	3QZ123E	10100, 11307
3TB061E	12100	922282E	26802	3QZ132E	11301, 11308
3TB070E	12100	922282E	27301	3QZ141E	10000, 11302, 11307
3TB083E	12100	922293E	20300	3QZ150E	09100, 09300
3TB243E	12100	922293E	26802	3QZ172E	10300, 10400
3TB252E	12100	922293E	27301	3QZ181E	09100, 09300, 09600
3TC100	12300	922300E	20300	3Q00000E	10300
3TLMCA	12400	922300E	26802	3Q00023E	10300
3TPCDA	12400	922300E	27301		
3TPGB0	12400	922322E	20300		
3TPM4A	12400	922322E	26802		
3TWIPJQ	12200	922322E	27302		
922150E	20300	922333E	20300		
922150E	26802	922333E	26802		
922150E	27301	922333E	27301		
922161E	20300	922353E	20300		
922161E	26802	922353E	26802		
922161E	27302	922353E	27301		
922180E	20300				
922180E	26802				
922180E	27301				
922191E	20300				
922191E	26802				
922191E	27301				
922202E	20300				
922202E	26802				
922202E	27301				
922213E	20300				
922213E	26802				
922213E	27301				
922220E	20300				
922220E	26802				



FO-85.TPU Quene Input Multiplexing and Registers Logic Diagram (Sheet 3 of 3)

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3IC221E	10100	3D1C51C	09D00
3MR163E	11500	3D1C52C	09D00
3MR172E	11500		
3MR181E	11500		
3MR190E	11500		
3MR203E	11500		
3MR212E	11500		
3MR221E	11500		
3MR230E	11500		
3MR243E	11500		
3MR252E	11500		
3MR261E	11500		
3MR270E	11500		
3MR283E	11500		
3MR292E	11500		
3MR301E	11500		
3QT00A	08700		
3QT000	08800		
3QZ203E	08501		
3QZ212E	08501		
3QZ221E	08501		
3QZ230E	08501		
3QZ243E	08501		
3QZ252E	08501		
3QZ261E	08501		
3QZ270E	08501		
3QZ283E	08501		
3QZ292E	08501		
3QZ301E	08501		
3QZ310E	08501		
3QZ323E	08501		
3QZ332E	08501		
3QZ341E	08501		



FO-86. TPU IFF RANGE COMPARE LOGIC DIAGRAM

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUITCATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

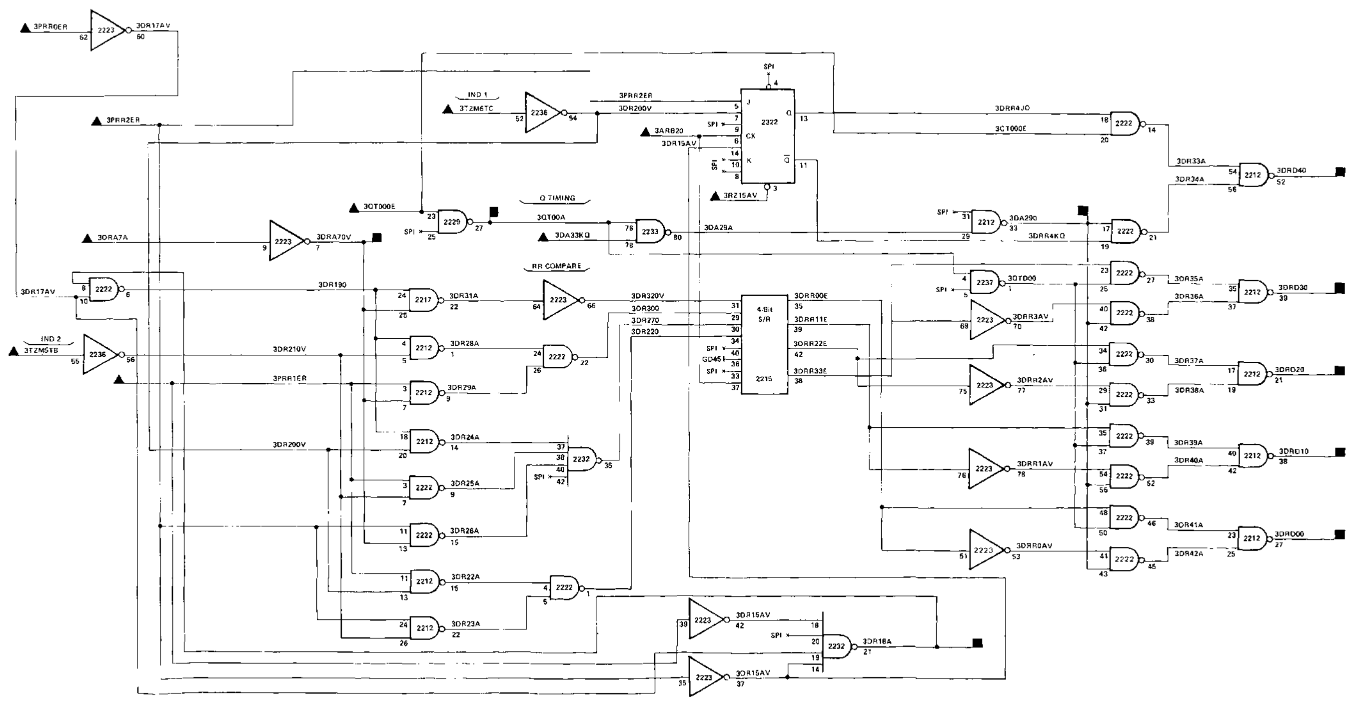
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FORMTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3ARB20	07800	3DA290	08800
3DA33KQ	09000	3DRA70V	08800, 08900
3DRA7A	11100	3DRD00	08800
3PRR0ER	12100	3DRD10	08800
3PRR1ER	12100	3DRD20	08800
3PRR2ER	12100	3DRD30	08800
3QT000E	08300	3DRD40	08800
3RZI5AV	11100	3DR18A	08900, 09000, 09400
3TZM5TB	12300	3QT00A	08600, 08800, 08900
3TZM6TC	12300		



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

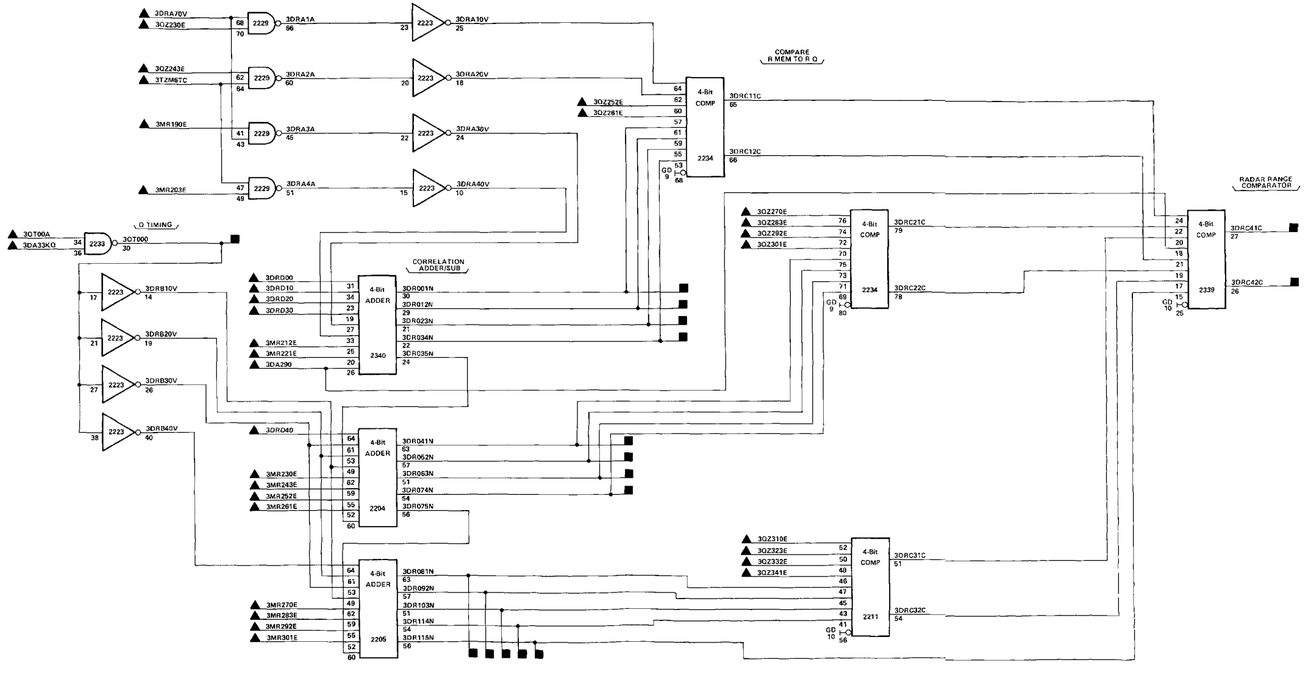
    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FORMTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

FO-87. TPU± Delta Generator Logic Diagram

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3DA290	08700	3DRC41C	09000
3DA33KQ	09000	3DRC42C	09000
3DRA70V	08700	3DR001N	08900, 11304
3DRD00	08700	3DR012N	08900, 11305
3DRD10	08700	3DR023N	08900, 11305
3DRD20	08700	3DR034N	08900, 11305
3DRD30	08700	3DR041N	08900, 11305
3DRD40	08700	3DR052N	08900, 11306
3MR190E	11500	3DR063N	08900, 11306
3MR203E	11500	3DR074N	08900, 11306
3MR212E	11500	3DR081N	08900, 11306
3MR221E	11500	3DR092N	08900, 11306
3MR230E	11500	3DR103N	08900, 11306
3MR243E	11500	3DR114N	08900, 11306
3MR252E	11500	3DR115N	08900
3MR261E	11500	3QT000	08600
3MR270E	11500		
3MR283E	11500		
3MR292E	11500		
3MR301E	11500		
3QT00A	08700		
3QZ230E	08501		
3QZ243E	08501		
3QZ252E	08501		
3QZ261E	08501		
3QZ270E	08501		
3QZ283E	08501		
3QZ292E	08501		
3QZ301E	08501		
3QZ310E	08501		
3QZ323E	08501		
3QZ332E	08501		
3QZ341E	08501		
3TZM6TC	12300		



FO-88. TPU Radar Range Compare Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

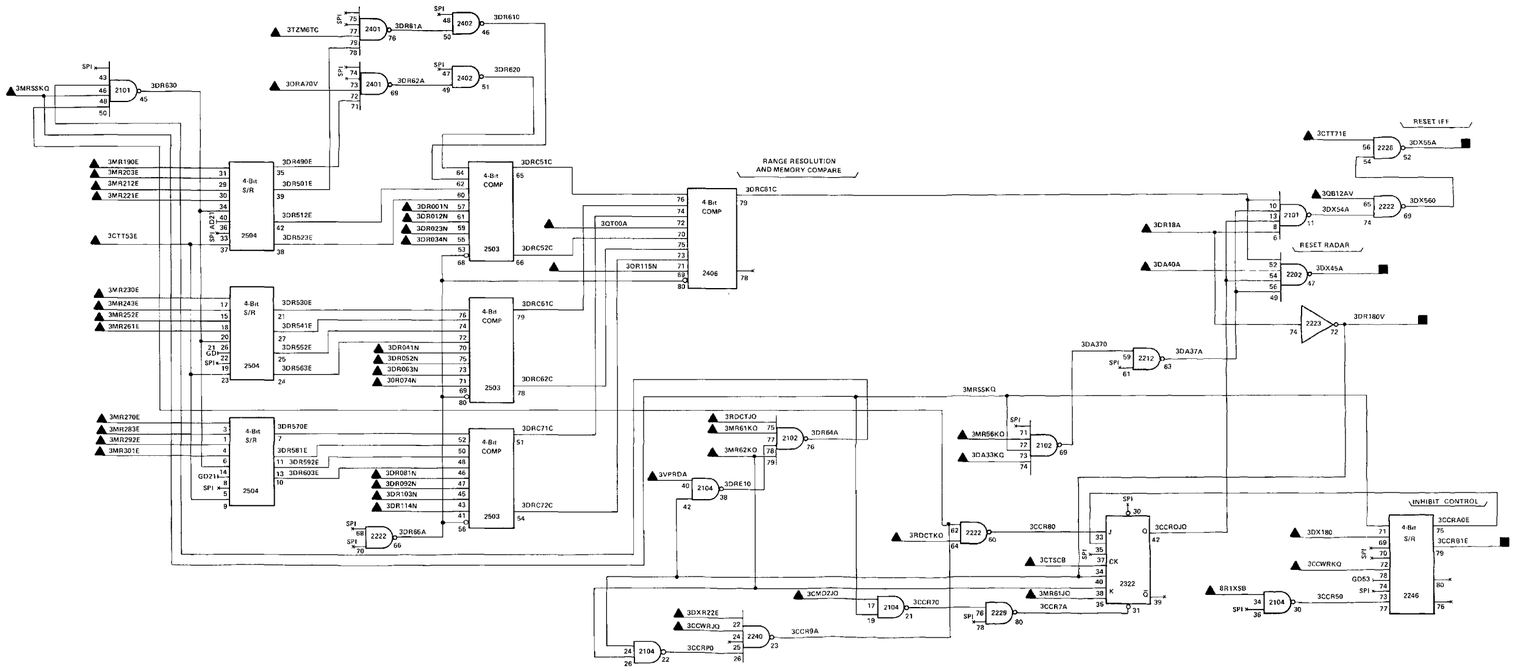
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT SIGNAL	INPUT SOURCE FO-SH	INPUT SIGNAL	INPUT SOURCE FO-SH	OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
3CCWRJQ	09500	3TZM6TC	12300	3CCRB1E	09100
3CCWRKQ	09500	3VPRDA	11200	3DR180V	09000, 09100
3CMD2JQ	08200	8R1XSB	00300	3DX45A	09000, 11700
3CTSCB	07600	8R1XSB	14900	3DX55A	11700
3CTT53E	07600	8R1XSB	26802		
3CTT71E	07600	8R1XSB	26803		
3DA33KQ	09000	8R1XSB	27501		
3DA40A	09600				
3DRA70V	08700				
3DR001N	08800				
3DR012N	08800				
3DR023N	08800				
3DR034N	08800				
3DR041N	08800				
3DR052N	08800				
3DR063N	08800				
3DR074N	08800				
3DR081N	08800				
3DR092N	08800				
3DR103N	08800				
3DR114N	08800				
3DR115N	08800				
3DR18A	08700				
3DXR22E	09400				
3DX180	09300				
3MRSSKQ	11700				
3MR190E	11500				
3MR203E	11500				
3MR212E	11500				
3MR221E	11500				
3MR230E	11500				
3MR243E	11500				
3MR252E	11500				
3MR261E	11500				
3MR270E	11500				
3MR283E	11500				
3MR292E	11500				
3MR301E	11500				
3MR56KQ	11700				
3MR61JQ	11700				
3MR61KQ	11700				
3MR62KQ	11700				
3QB12AV	07701				
3QT00A	08700				
3RDCTJQ	11100				
3RDCTKQ	11100				



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN; FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 2 CARD CAGE (1A1A1A5)
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE
   - △ INDICATES INPUT FROM THE SAME FIGURE
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE
   - □ INDICATES OUTPUT TO THE SAME FIGURE
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE.
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.
    - B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2130, A2230, A2330, A2430, AND A2530.

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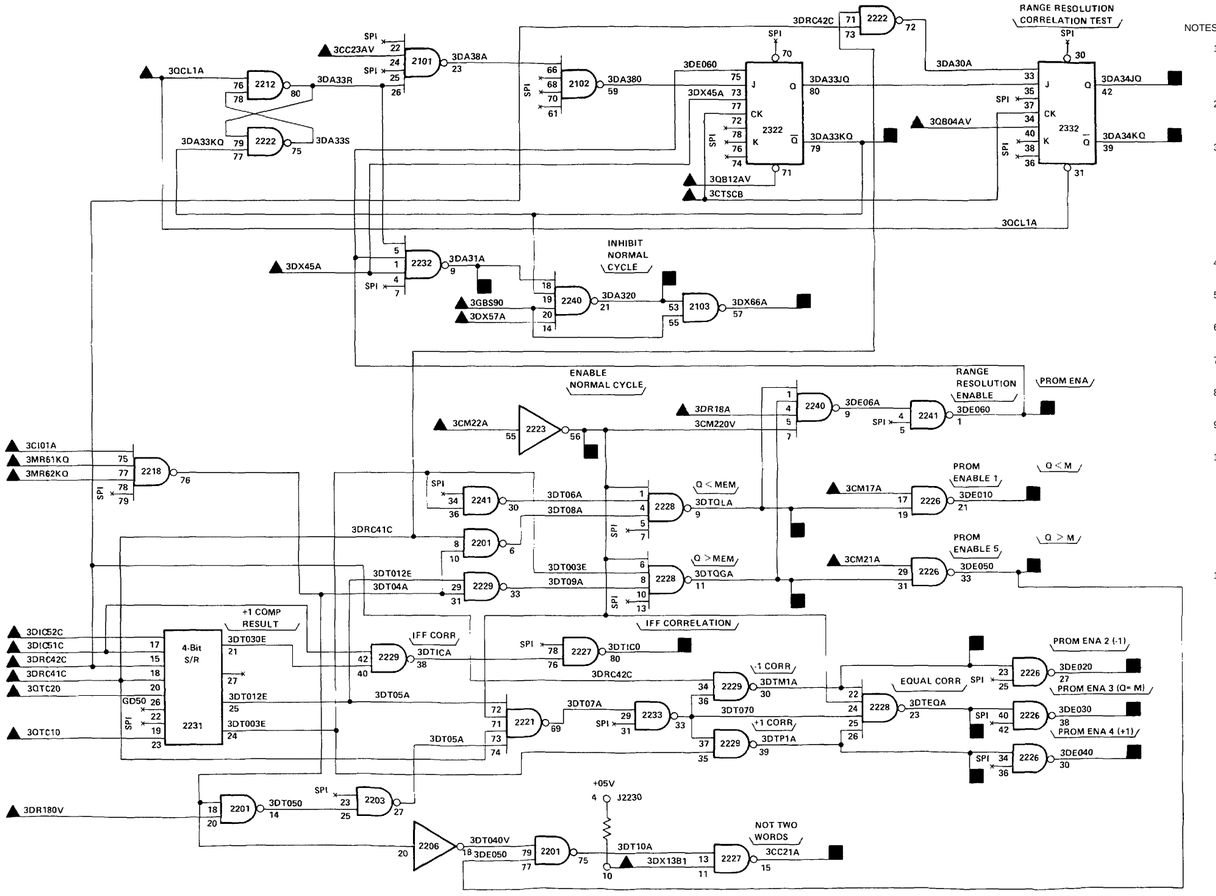
FO-89. TPU Range Resolution and Memory Compare Logic Diagram

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3CC23AV	09500	3CC21A	09500
3CI01A	09600	3CM220V	12500
3CM17A	08200	3DA31A	10600
3CM21A	08200	3DA320	09600
3CM22A	08200	3DA33KQ	08700, 08800, 08900, 10600
3CTSCB	07600	3DA34JQ	09400
3DIC51C	08600	3DA34KQ	09100, 09400
3DIC52C	08600	3DE010	09100, 09200, 26802, 29601
3DRC41C	08800	3DE020	09200, 09400, 26802, 29601
3DRC42C	08800	3DE030	09100, 09200, 09400, 26802, 29601
3DR18A	08700		
3DR180V	08900	3DE040	09200, 26802, 29601
3DX13B1	09200	3DE050	09200, 26802, 29601
3DX13B1	29601	3DE060	09200, 09400, 12500, 26802, 29602
3DX45A	08900	3DTEQA	09400, 12500
3DX57A	11700	3DTICO	09100, 12500
3GBS90	10600	3DTM1A	09100, 09400, 12500
3MR61KQ	11700	3DTP1A	09400, 12500
3MR62KQ	11700	3DTQGA	09600, 12500
3QB04AV	07701	3DTQLA	12500
3QB12AV	07701	3DX66A	09700
3QCL1A	08400		
3QTC10	08300		
3QTC20	08300		



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

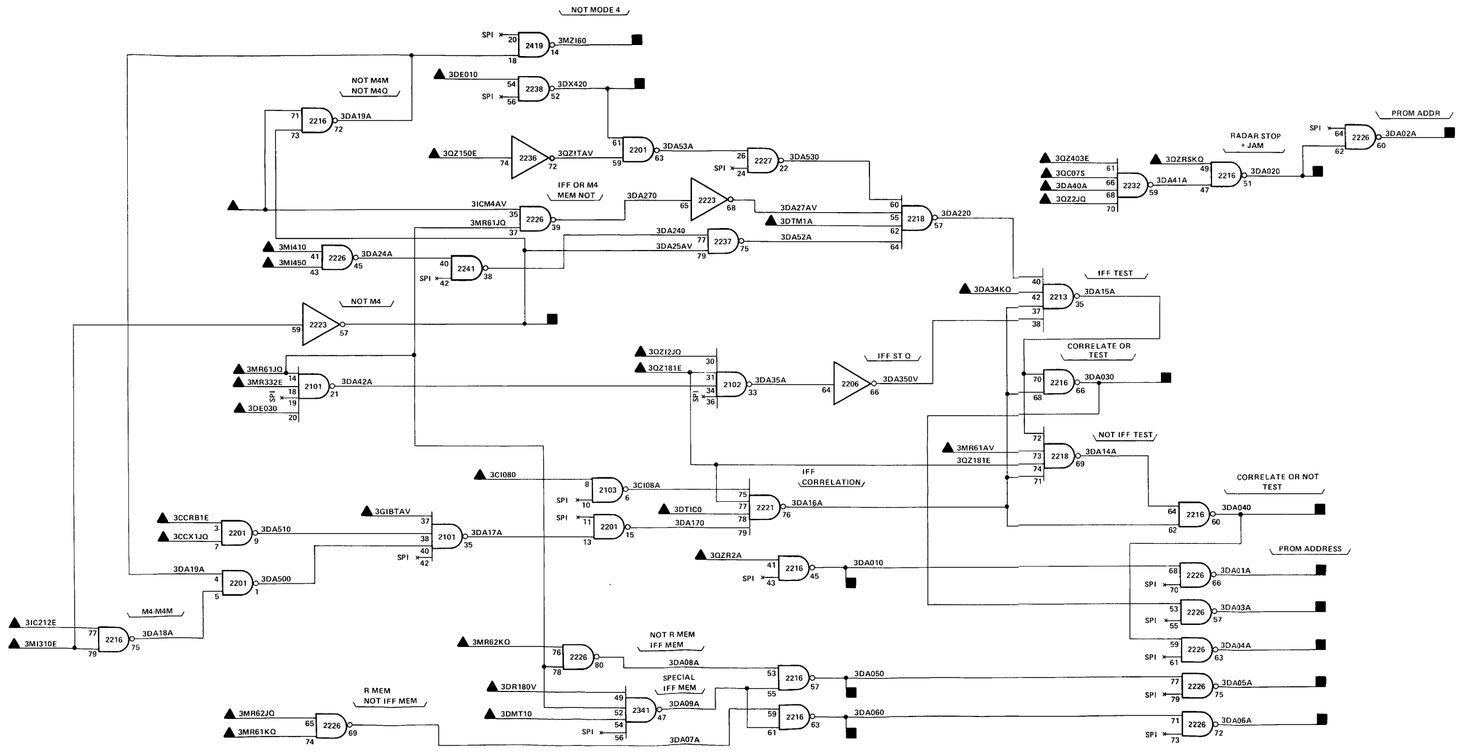
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

MS200683

FO-90. TPU Range Resolution Correlation Test and Compare Results Logic Diagram

INPUT	
SIGNAL	SOURCE FO-SH
3CCRB1E	08900
3CCX1JQ	09500
3C1080	09600
3DA34KQ	09000
3DA40A	09600
3DE010	09000
3DE010	29601
3DE030	09000
3DE030	29601
3DMT10	09400
3DR180V	08900
3DTIC0	09000
3DTM1A	09000
3GIBTAV	10600
3ICM4AV	09300
3IC212E	10100
3MI310E	11600
3MI410	11600
3MI450	11600
3MR332E	11500
3MR61AV	11700
3MR61JQ	11700
3MR61KQ	11700
3MR62JQ	11700
3MR62KQ	11700
3QC07S	08400
3QZ12JQ	08502
3QZRSKQ	08502
3QZR2A	09400
3QZR2JQ	08502
3QZ150E	08503
3QZ181E	08503
3QZ403E	08502

OUTPUT	
SIGNAL	DESTINATION FO-SH
3DA01A	09200, 26802, 29602
3DA010	09200, 26802, 29601
3DA02A	09200, 26802, 29602
3DA020	09200, 26802, 29601
3DA03A	09200, 26802, 29602
3DA030	09200, 26802, 29602
3DA04A	09200, 26802, 29602
3DA040	09200, 26802, 29602
3DA05A	09200, 26802, 29602
3DA050	09200, 26802, 29602
3DA06A	09200, 26802, 29602
3DA060	09200, 26802, 29602
3DA25AV	11309, 11700
3DX420	09400, 09600
3MZI60	09300



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN; FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 2 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE.
   - △ INDICATES INPUT FROM THE SAME FIGURE.
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE.
   - □ INDICATES OUTPUT TO THE SAME FIGURE.
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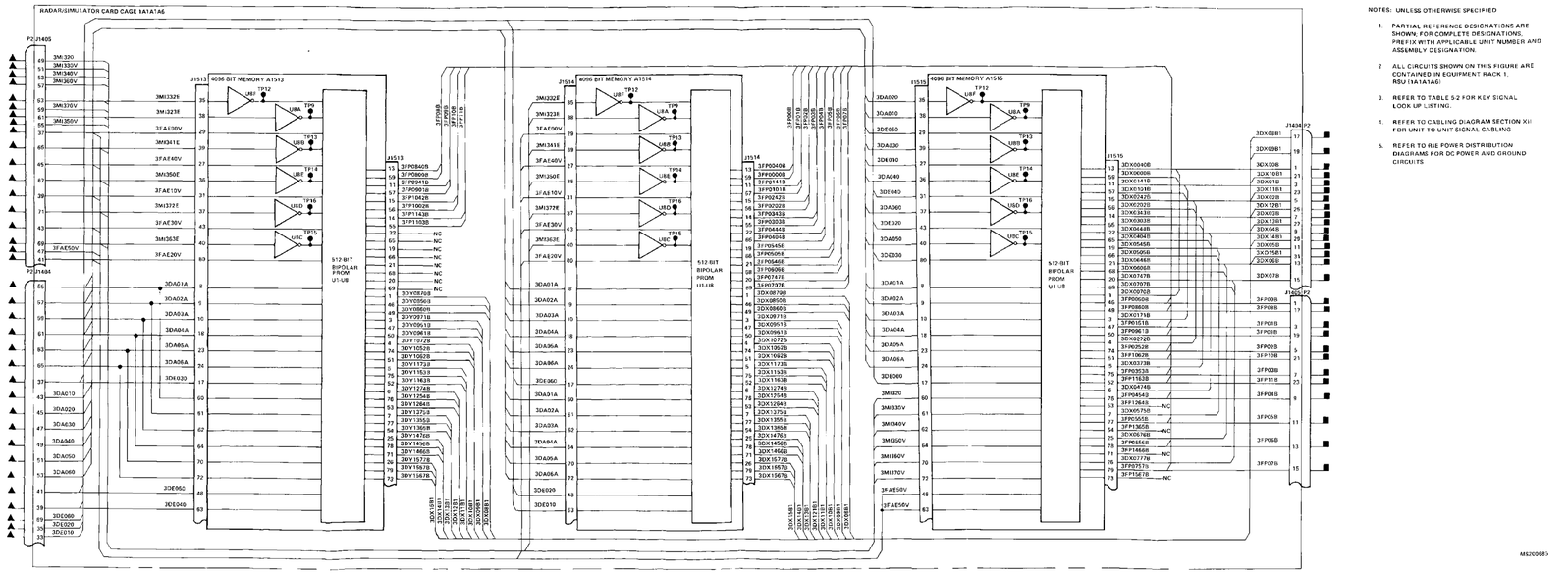
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.
    - B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2130, A2230, A2330, A2430, AND A2530.

MS200684

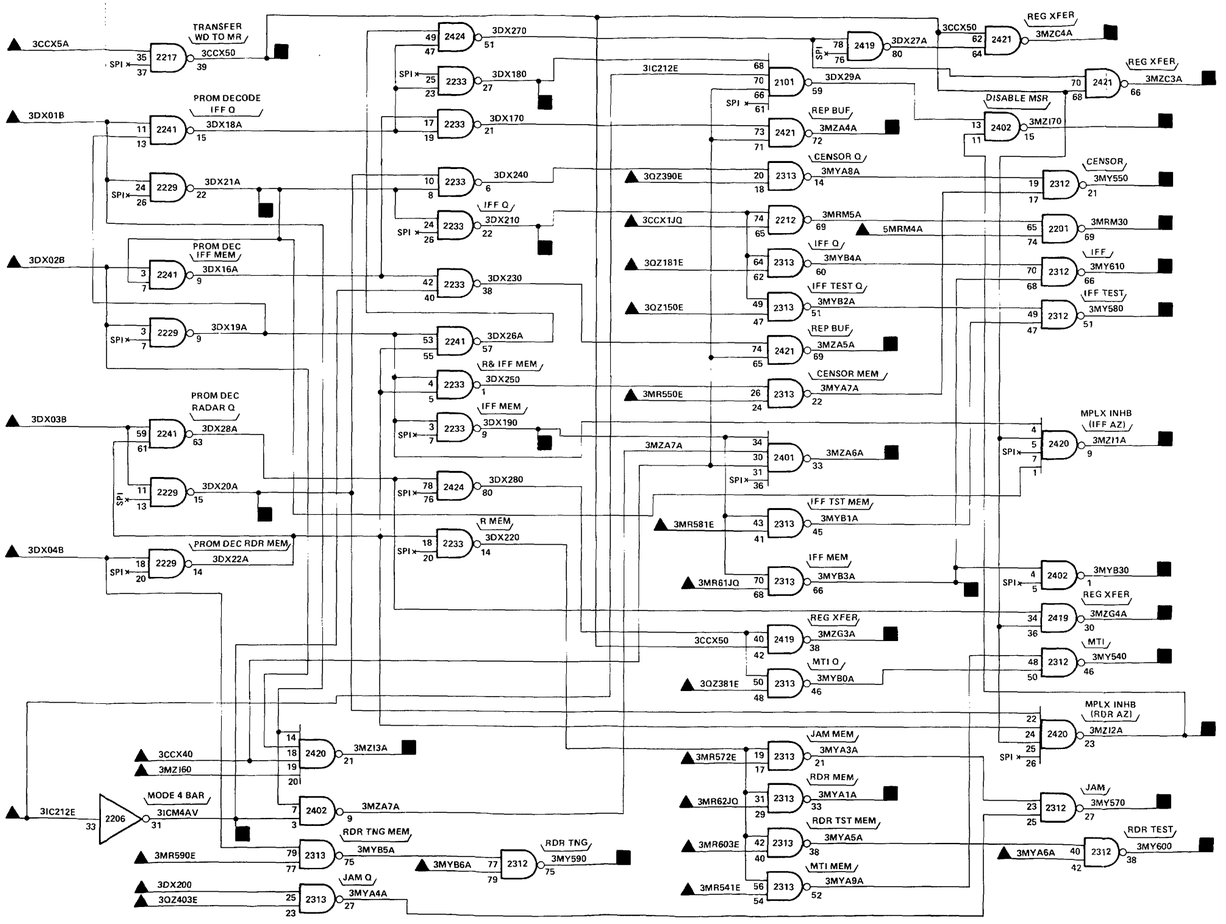
FO-91. TPU PROM Address-Encode Logic Diagram

INPUT		INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3DA01A	09100	3FAE30V	29501	3DX00B	09700, 12500, 26802, 29601
3DA01A	29602	3FAE30V	31401	3DX01B	09300, 12500, 26802
3DA010	09100	3FAE40V	07600	3DX02B	09300, 12500, 26802, 29601
3DA010	29601	3FAE40V	29501	3DX03B	08502, 09300, 12500, 26802,
3DA02A	09100	3FAE40V	31401		29601
3DA02A	29602	3FAE50V	07600	3DX04B	09300, 12500, 26802, 29601
3DA020	09100	3FAE50V	29501	3DX05B	09400, 09500, 12500, 26802,
3DA020	29601	3FAE50V	31401		29601
3DA03A	09100	3M1320	11600	3DX06B	09500, 12500, 26802, 29601
3DA03A	29602	3M1320	29501	3DX07B	09400, 12500, 26802, 29601
3DA030	09100	3M1320	31401	3DX08B1	09400, 12500, 26802, 29601
3DA030	29602	3M1323E	11600	3DX09B1	09400, 12500, 26802, 29601
3DA04A	09100	3M1323E	29502	3DX10B1	11306, 12500, 26802, 29601
3DA04A	29602	3M1330V	11600	3DX11B1	09500, 09600, 10600, 12500,
3DA040	09100	3M1330V	29501		26802, 29601
3DA040	29602	3M1330V	31401	3DX12B1	09500, 12500, 26802, 29601
3DA05A	09100	3M1332E	11600	3DX13B1	09000, 12500, 26802, 29601
3DA05A	29602	3M1332E	29502	3DX14B1	09700, 12500, 26802, 29601
3DA050	09100	3M1340V	11600	3DX15B1	09700, 09800, 12500, 26802,
3DA050	29602	3M1340V	29502		29601
3DA06A	09100	3M1340V	31401	3FP00B	10500, 29501, 31401
3DA06A	29602	3M1341E	11600	3FP01B	10500, 29501, 31401
3DA060	09100	3M1341E	29502	3FP02B	10500, 29501, 31401
3DA060	29602	3M1350E	11600	3FP03B	10500, 29501, 31401
3DE010	09000	3M1350E	29502	3FP04B	10500, 29501, 31401
3DE010	29601	3M1350V	11600	3FP05B	10500, 29501, 31401
3DE020	09000	3M1350V	29502	3FP06B	10500, 29501, 31401
3DE020	29601	3M1350V	31401	3FP07B	10500, 29501, 31401
3DE030	09000	3M1360V	11600	3FP08B	10500, 29501, 31401
3DE030	29601	3M1360V	29502	3FP09B	10500, 29501, 31401
3DE040	09000	3M1360V	31401	3FP10B	10500
3DE040	29601	3M1363E	11600	3FP11B	10500
3DE050	09000	3M1363E	29502		
3DE050	29601	3M1370V	11600		
3DE060	09000	3M1370V	29502		
3DE060	29602	3M1370V	31401		
3FAE00V	07600	3M1372E	11600		
3FAE00V	29501	3M1372E	29502		
3FAE00V	31401				
3FAE10V	07600				
3FAE10V	29501				
3FAE10V	31401				
3FAE20V	07600				
3FAE20V	29501				
3FAE20V	31401				
3FAE30V	07600				



FO-92. TPU PROM Logic Diagram

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3CCX1JQ	09500	3CCX5D	12500
3CCX40	11309	3DX18D	08900, 11309
3CCX5A	09500	3DX19D	09700
3DX01B	09200	3DX20A	08502, 09700, 11200
3DX02B	09200	3DX21A	09700, 11200
3DX02B	29601	3DX21D	09700
3DX03B	09200	3ICM4AV	09100
3DX03B	29601	3MRM3D	11700
3DX04B	09200	3MYA1A	08502, 11200, 12000
3DX04B	29601	3MYB3A	11200
3DX200	08502	3MYB3D	11200
3IC212E	10100	3MY54D	11308
3MRM4A	11700	3MY55D	11308
3MR541E	11500	3MY57D	11308
3MR550E	11500	3MY58D	11308
3MR572E	11500	3MY59D	11308
3MR581E	11500	3MY60D	11308
3MR590E	11500	3MY61D	11308
3MR603E	11500	3MZA4A	11305, 11306, 11307, 11308,
3MR61JQ	11700		11309
3MR62JQ	11700	3MZA5A	11306, 11307, 11308
3MYA6A	08502	3MZA6A	11305, 11306, 11309
3MYB6A	08502	3MZC3A	11305, 11306, 11307
3MZI6D	09100	3MZC4A	11305, 11306, 11307
3QZ150E	08503	3MZG3A	11309
3QZ181E	08503	3MZG4A	11309
3QZ381E	08502	3MZI1A	11306, 11307
3QZ390E	08502	3MZI2A	11303, 11309
3QZ403E	08502	3MZI3A	11306
		3MZI7D	11303, 11304



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2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
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   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
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5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

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    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

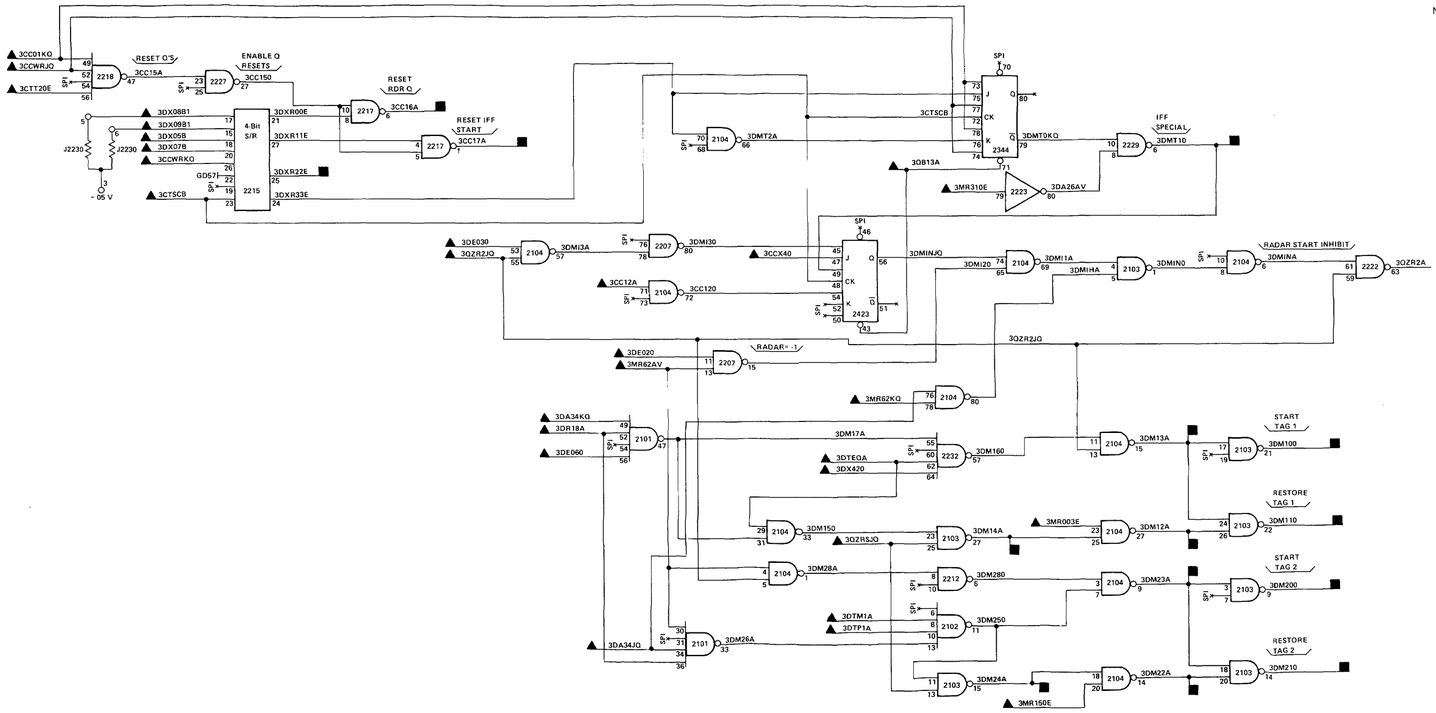
11. SPIXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

MS200686

FO-93. TPU PROM Decoding Logic Diagram

INPUT	
SIGNAL	SOURCE FO-SH
3CCWRJQ	09500
3CCWRKQ	09500
3CCX40	11309
3CC01KQ	09500
3CC12A	09500
3CTSCB	07600
3CTT20E	07600
3DA34JQ	09000
3DA34KQ	09000
3DE020	09000
3DE020	29601
3DE030	09000
3DE030	29601
3DE060	09000
3DE060	29602
3DR18A	08700
3DTEQA	09000
3DTM1A	09000
3DTP1A	09000
3DX05B	09200
3DX05B	29601
3DX07B	09200
3DX07B	29601
3DX08B1	09200
3DX08B1	29601
3DX09B1	09200
3DX09B1	29601
3DX420	09100
3MR003E	11500
3MR150E	11500
3MR310E	11500
3MR62AV	11700
3MR62KQ	11700
3QB13A	07701
3QZRSJQ	08502
3QZR2JQ	08502

OUTPUT	
SIGNAL	DESTINATION FO-SH
3CC16A	08502
3CC17A	08502
3DMT10	09100
3DM100	11303
3DM110	11303
3DM12A	09700
3DM13A	09700
3DM14A	09700
3DM200	11304
3DM210	11304
3DM22A	09700
3DM23A	09700
3DM24A	09700
3DXR22E	08900, 09500
3QZR2A	08502, 09100



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2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
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   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

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7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

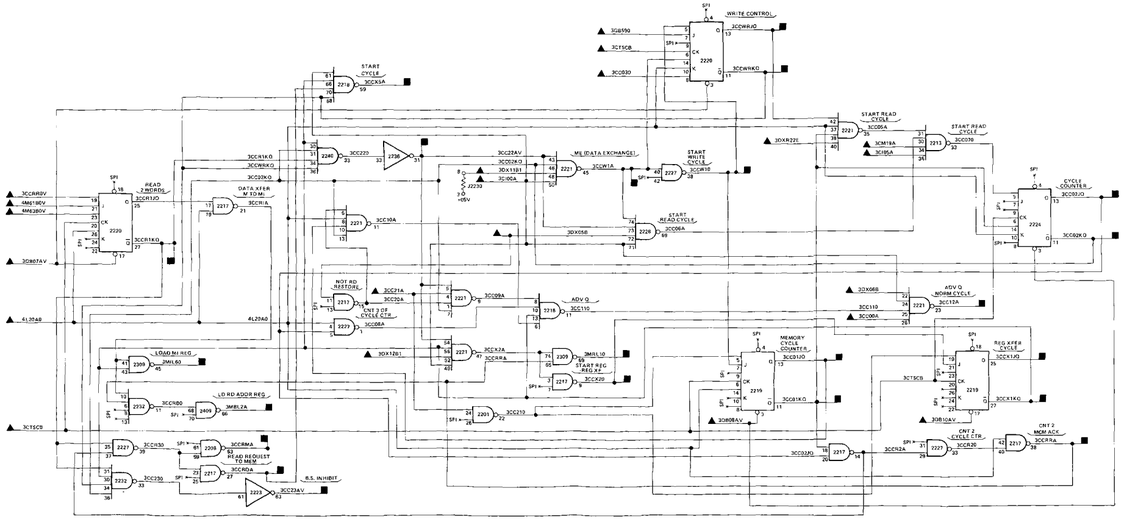
    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

MS200697

FO-94. TPU Radar Start and Restore Logic Diagram

INPUT		OUTPUT	
	SOURCE		DESTINATION
SIGNAL	FO-SH	SIGNAL	FO-SH
3GB52KG	10600	31CD1A	11302
31CC7A	11302	31CD2A	11302
31C230V	09900	31CV1A	11302
31C240V	09900	31CV2A	11302
3M1000	11600	31CV5A	11302
3M1012E	11600	31CV6A	11302
3M1020V	11600	31CD1TB	10200
3M1030E	11600	31CD2TC	10200
3M1040	11600	31CD3TG	10200
3M1052E	11600	31CD4TA	10200
3M1060V	11600	31CD5TB	10200
3M1070E	11600	31CD6TC	10200
3M1080	11600	31CD7TG	10200
3M1092E	11600	31CD8TA	10200
3M1100V	11600	31CD9TB	10200
3M1110E	11600	31C10TC	10200
3M1123E	11600	31C11TD	10200
3M1132E	11600		
3M1141E	11600		
3M1150E	11600		
3M1163E	11600		
3M1172E	11600		
3M1183E	11600		
3M1190E	11600		
3M1203E	11600		
3M1212E	11600		
3M1221E	11600		
3M1230E	11600		
3M1405E	11600		
3M1410	11600		
3M1445E	11600		
3M1450	11600		
3Q2003E	08503		
3Q2012E	08503		
3Q2021E	08503		
3Q2030E	08503		
3Q2043E	08503		
3Q2052E	08503		
3Q2061E	08503		
3Q2070E	08503		
3Q2083E	08503		
3Q2092E	08503		
3Q2101E	08503		
3Q2110E	08503		
3Q2141E	08503		



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN; FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 2 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE.
   - △ INDICATES INPUT FROM THE SAME FIGURE.
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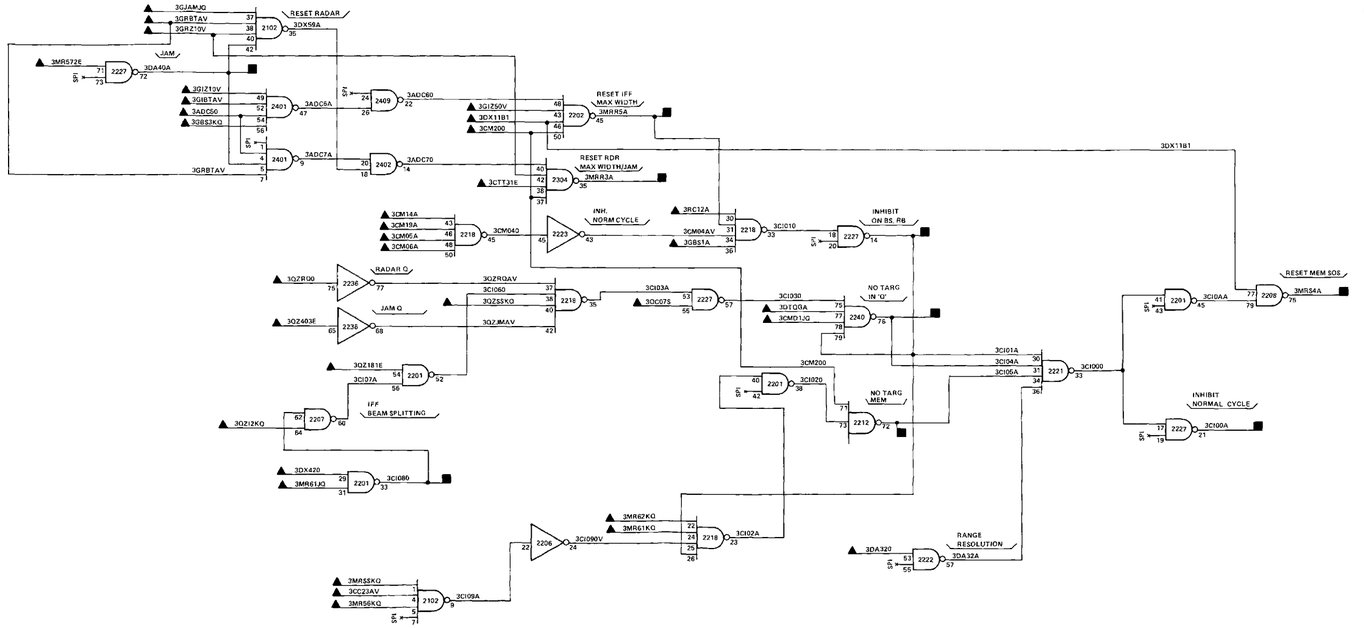
    - B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.
11. SPIXXX INDICATES +5V PULL UP THROUGH RESISTOR CARDS A21-30, A2220, A2230, A2430, AND A2530.

M2290G01

FO-95. TPU Read/Write Memory Cycle Control Logic Diagram

INPUT SIGNAL	SOURCE FO-SH
3ARB20	07800
3ARC7S	07800
3CM200	08200
3DX15B1	09200
3DX15B1	29601
3DX490	09700
3GIBD0	09700
3MR470E	11500
3MR483E	11500
3MR492E	11500
3MR501E	11500
3MR510E	11500
3MR523E	11500
3MR572E	11500
3MR62JQ	11700
3PIM0ER	12000
3PIM1ER	12000
3PIM2ER	12000
3PRM0ER	12000
3PRM1ER	12000
3PRM2ER	12000
3QM40TA	08502

OUTPUT SIGNAL	DESTINATION FO-SH
3DX15A	09700
3GIC02C	10600
3GIM0TA	11307
3GIM1TB	11307
3GIM2TC	11307
3GIZ10V	09600
3GJAMJQ	09600
3GJM3A	09700
3GRC02C	10600
3GRM0TA	11307
3GRM1TB	11307
3GRM2TC	11308
3GRZ1A	09700
3GRZ10V	09600, 09700



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN. FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VFU BAY 2 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE.
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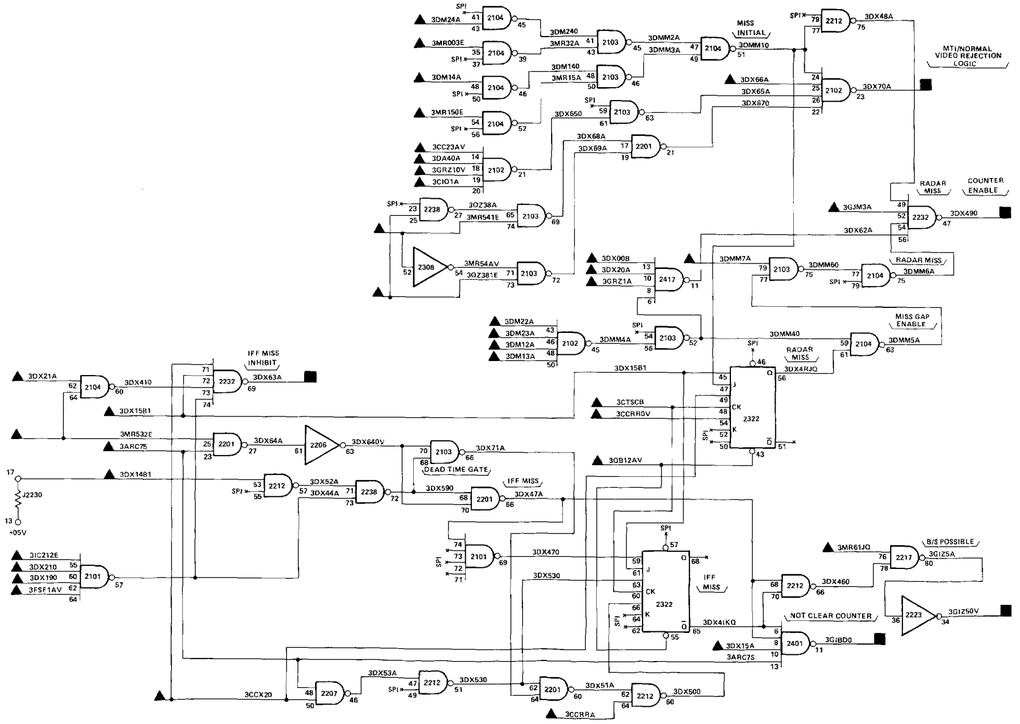
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.
    - B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2130, A2230, A2330, A2430, AND A2530.

MS200689

FO-96. TPU Inhibit Normal Cycle Logic Diagram

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3CCRR0V	07701	3CCRDA	11301, 11302, 11305, 11309, 11800
3CC00A	11700		
3CC030	11309	3CCRMA	06001
3CC21A	09000	3CCRRA	07701, 09700, 11800
3CI00A	09600	3CCWRJQ	08502, 08900, 09400, 11800, 11900
3CI05A	09600		
3CM19A	08200	3CCWRKQ	06001, 08900, 09400, 11900
3CTSCB	07600	3CCW1A	10600, 11400
3DXR22E	09400	3CCW10	11400
3DX05B	09200	3CCX1JQ	09100, 09300
3DX05B	29601	3CCX1KQ	11309
3DX06B	09200	3CCX20	08502, 09700
3DX06B	29601	3CCX5A	09300, 11308
3DX11B1	09200	3CC01JQ	11309, 11800, 11900
3DX11B1	29601	3CC01KQ	09400
3DX12B1	09200	3CC02JQ	11800
3DX12B1	29601	3CC02KQ	11309
3GBS90	10600	3CC12A	08400, 09400
3GB07AV	07701	3CC22AV	11700
3GB08AV	07701	3CC23AV	09000, 09600, 09700, 10600, 11100, 11700
3GB10AV	07701		
4L20A0	06001	3MBL2A	11800
4M61B0V	06402	3MIL60	11302, 11600
4M61B0V	26802	3MRL10	11500, 11700
4M63B0V	06402		



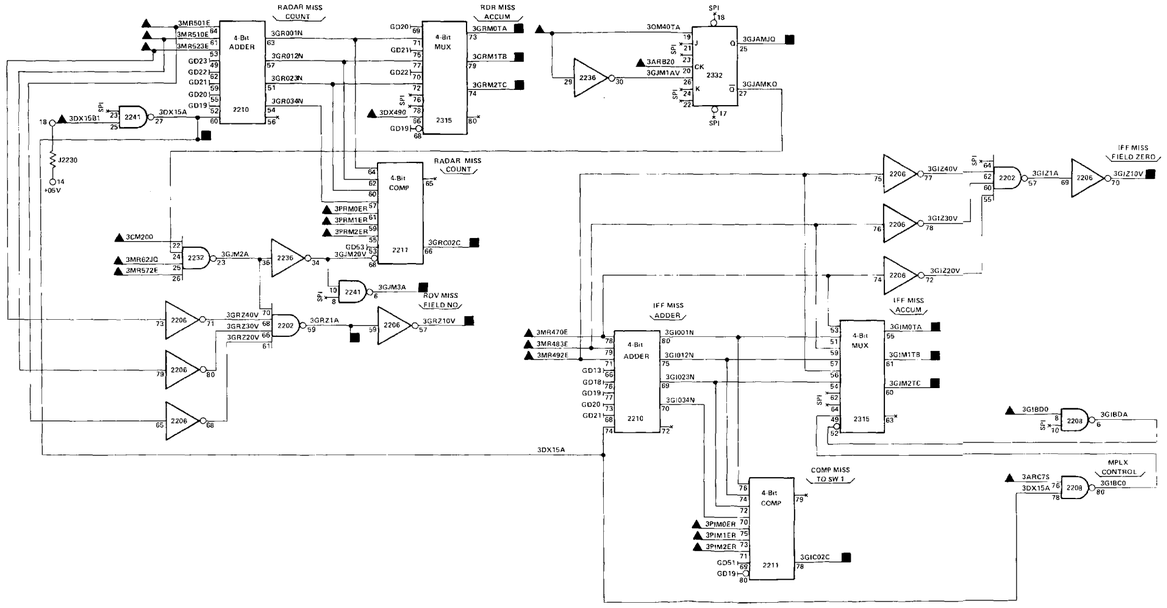
NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN; FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 2 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE.
   - △ INDICATES INPUT FROM THE SAME FIGURE.
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE.
   - □ INDICATES OUTPUT TO THE SAME FIGURE.
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE.
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.
    - B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2130, A2230, A2330, A2430, AND A2530.

MS200690

FO-97. TPU Radar/IFF Miss Logic Diagram Logic Diagram

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3ADC50	10900	3C100A	09500, 12500
3CC23AV	09500	3C101A	09000, 09700
3CMD1JQ	08200	3C104A	08400
3CM05A	08200	3C105A	09500
3CM06A	08200	3C108D	09100
3CM14A	08200	3DA40A	08900, 09100, 09700, 11100,
3CM19A	08200		11700
3CM200	08200	3MRR3A	11700
3CTT31E	07600	3MRR5A	11700
3DA320	09000	3MRS4A	11700
3DTQGA	09000		
3DX1181	09200		
3DX1191	29601		
3DX420	09100		
3GBS1A	10600		
3GBS3KQ	10600		
3G1BTAV	10600		
3G1210V	09800		
3G1250V	09700		
3GJAMJQ	09800		
3GRBTAV	10600		
3GRZ10V	09800		
3MRS5KQ	11700		
3MRS6KQ	11700		
3MR572E	11500		
3MR61JQ	11700		
3MR61KQ	11700		
3MR62KQ	11700		
3QC07S	08400		
3QZ12KQ	08502		
3QZRQD	08502		
3QZSSKQ	08502		
3QZ181E	08503		
3QZ403E	08502		
3RC12A	11100		
4L20A0	06001		



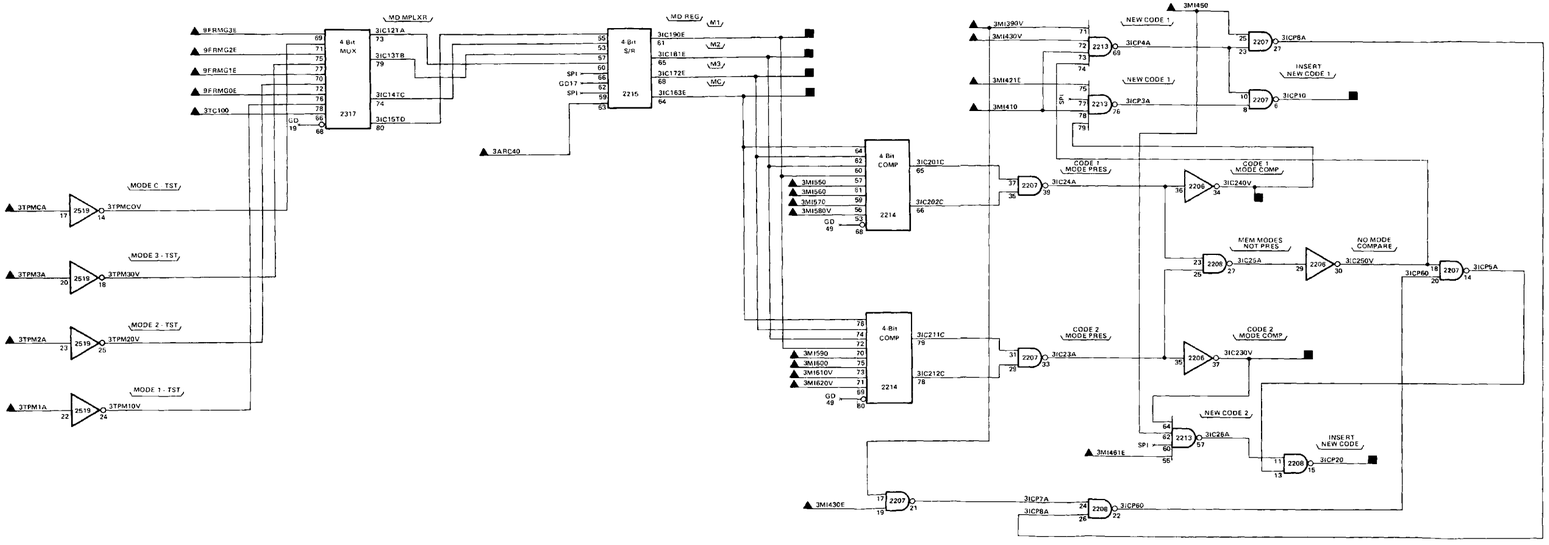
NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN; FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 2 CARD CAGE (1A1A1A6).
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE.
   - △ INDICATES INPUT FROM THE SAME FIGURE.
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE.
   - □ INDICATES OUTPUT TO THE SAME FIGURE.
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE.
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. *CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.*
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION *AND CIRCUIT CARD PIN NUMBER.*
    - B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-38 FOR CARD PIN/ *TEST POINT FOR MTS TESTABLE CARDS.*
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2130, A2230, A2330, A2430, AND A2530.

MS200601

FO-98. TPU Radar/IFF Miss Fields Logic Diagram

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3ARC7S	07800	3DX490	09800, 10600
3CCRRA	09500	3DX63A	11500
3CCRRDV	07701	3DX70A	11700
3CCX20	09500	3G1B00	09800
3CC23AV	09500	3G1250V	09600, 10600
3C101A	09600		
3CTSCB	07600		
3DA42A	09600		
3DMM7A	08502		
3DM12A	09400		
3DM13A	09400		
3DM14A	09400		
3DM22A	09400		
3DM23A	09400		
3DM24A	09400		
3DX00B	09200		
3DX00B	29601		
3DX14B1	09200		
3DX14B1	29601		
3DX15A	09800		
3DX15B1	09200		
3DX15B1	29601		
3DX19D	09300		
3DX20A	09300		
3DX21A	09300		
3DX21Q	09300		
3DX65A	09000		
3F5F1AV	10300		
3GJM3A	09800		
3GRZ1A	09800		
3GRZ10V	09800		
3IC212E	10100		
3MR003E	11500		
3MR150E	11500		
3MR532E	11500		
3MR541E	11500		
3MR61JQ	11700		
3QB12AV	07701		
3QZ381E	08502		



NOTES: UNLESS OTHERWISE SPECIFIED

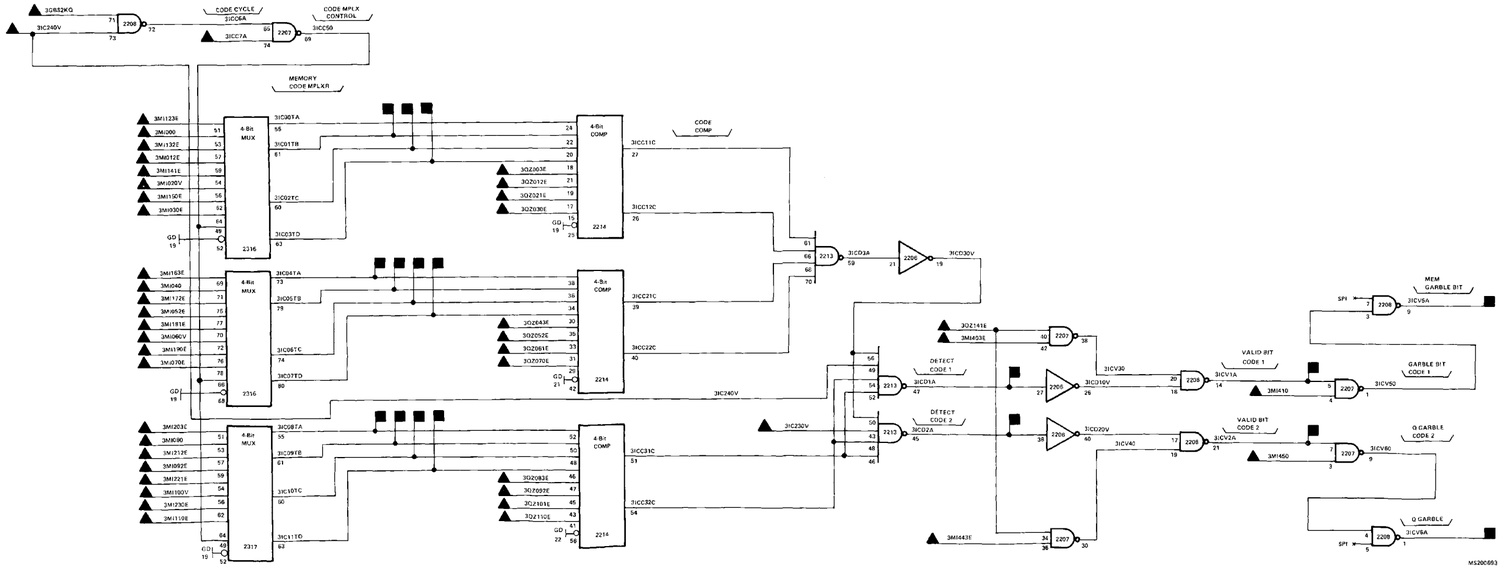
1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN. FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 2 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE.
   - △ INDICATES INPUT FROM THE SAME FIGURE.
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE.
   - □ INDICATES OUTPUT TO THE SAME FIGURE.
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE.
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.
    - B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2130, A2230, A2330, A2430, AND A2530.

MS200692

FO-99. TPU New Code Generation Logic Diagram

INPUT	
SIGNAL	SOURCE FO-SH
3ARC4G	07800
3MI390V	11600
3MI410	11600
3MI421E	11600
3MI430E	11600
3MI430V	11600
3MI450	11600
3MI461E	11600
3MI550	11600
3MI560	11600
3MI570	11600
3MI580V	11600
3MI590	11600
3MI600	11600
3MI610V	11600
3MI620V	11600
3TC100	12300
3TPM1A	12400
3TPM2A	12400
3TPM3A	12400
9FRMG0E	18700
9FRMG0E	26802
9FRMG0E	27302
9FRMG1E	18700
9FRMG1E	26802
9FRMG1E	27302
9FRMG2E	18700
9FRMG2E	26802
9FRMG2E	27302
9FRMG3E	18700
9FRMG3E	26802
9FRMG3E	27302

OUTPUT	
SIGNAL	DESTINATION FO-SH
3ICP10	11302, 11309
3ICP20	11302, 11309
3IC163E	11302, 11308
3IC172E	11302, 11308
3IC181E	11302, 11308
3IC190E	11302, 11308
3IC230V	10000
3IC240V	10000



NOTES: UNLESS OTHERWISE SPECIFIED

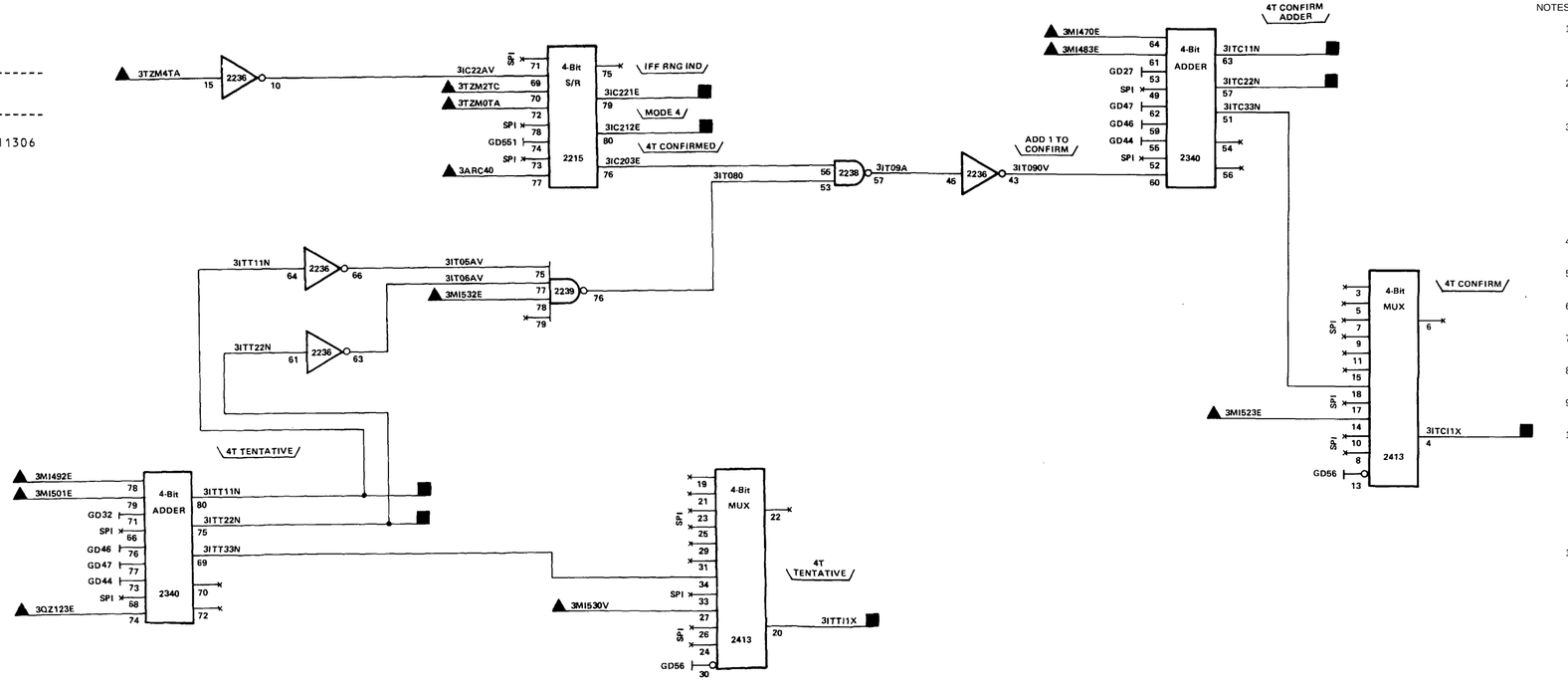
1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN; FOR COMPLETE DESIGNATIONS, PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 2 CARD CAGE 1(A1A1A5).
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INDICATES INPUT FROM ANOTHER FIGURE.
   - △ INDICATES INPUT FROM THE SAME FIGURE.
   - ■ INDICATES OUTPUT TO ANOTHER FIGURE.
   - □ INDICATES OUTPUT TO THE SAME FIGURE.
   - ◪ INDICATES OUTPUT TO THE SAME AND ANOTHER FIGURE.
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION III FOR CIRCUIT CARD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.
    - B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.
11. SP-XXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2130, A2230, A2330, A2430, AND A2530.

MS200693

FO-100. TPU IFF/SIF Code Validation Logic Diagram

INPUT	
SIGNAL	SOURCE FO-SH
3ARC40	07800
3MI470E	11600
3MI483E	11600
3MI492E	11600
3MI501E	11600
3MI523E	11600
3MI530V	11600
3MI532E	11600
3QZ123E	08503
3TZM0TA	08300
3TZM2TC	08300
3TZM4TA	12300

OUTPUT	
SIGNAL	DESTINATION FO-SH
3IC212E	09100, 09300, 09700, 11306
3IC221E	08600
3ITCI1X	11301
3ITC11N	11301
3ITC22N	11301
3ITTI1X	11301
3ITT11N	11301
3ITT22N	11301



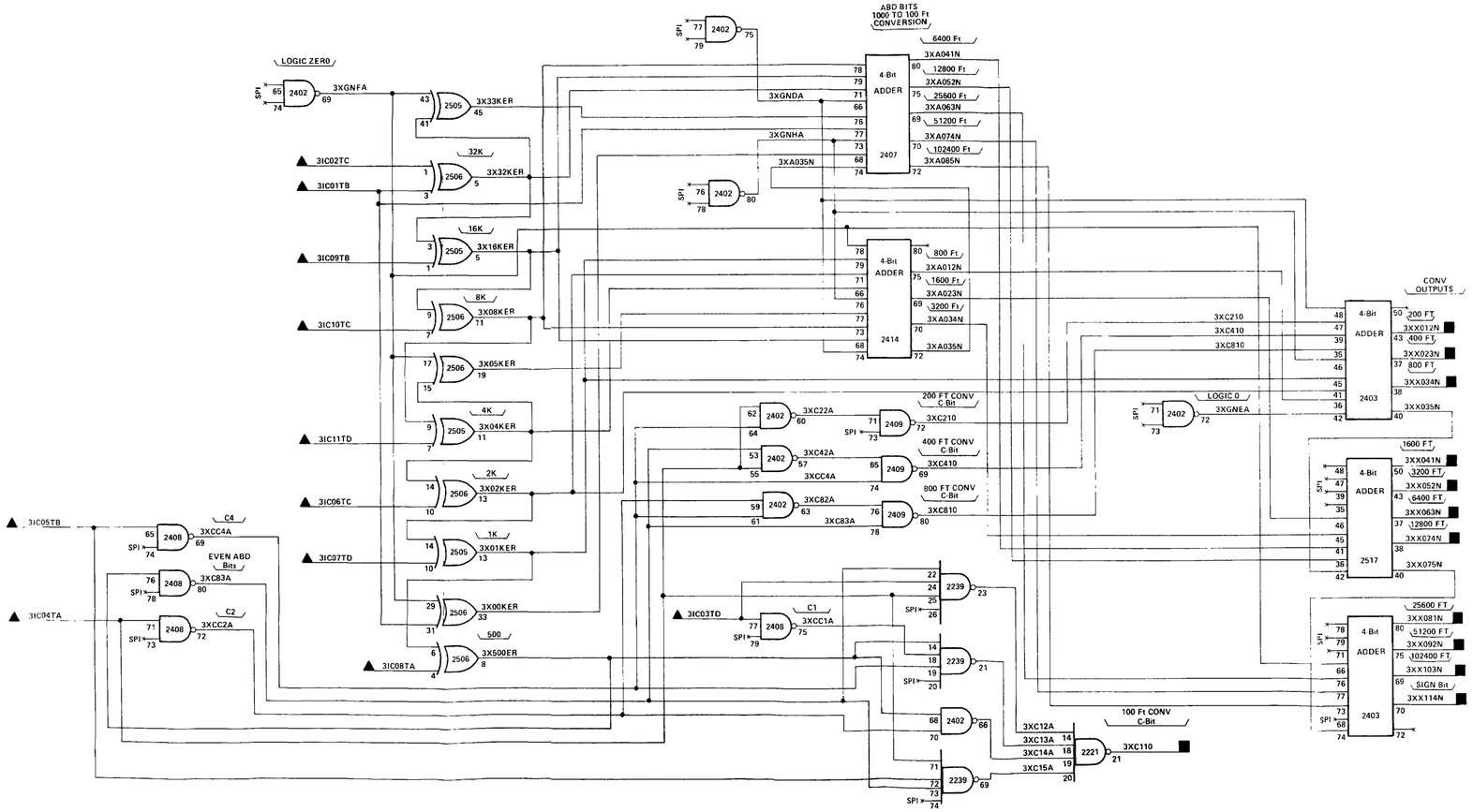
NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1AS).
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INPUT FROM ANOTHER FIGURE
   - △ INPUT FROM SAME FIGURE
   - ■ OUTPUT TO ANOTHER FIGURE
   - □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   - ◪ OUTPUT TO SAME FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.
    - B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2130, A2230, A2330, A2430, AND A2530.

MS200694

FO-101. TPU 4T Confirmation Logic Diagram

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3IC01TB	10000	3XC110	11303, 11304
3IC02TC	10000	3XX012N	11303, 11304
3IC03TD	10000	3XX023N	11303, 11304
3IC04TA	10000	3XX034N	11303, 11304
3IC05TB	10000	3XX041N	11303, 11304
3IC06TC	10000	3XX052N	11303, 11304
3IC07TD	10000	3XX063N	11303, 11304
3IC08TA	10000	3XX074N	11303, 11304
3IC09TB	10000	3XX081N	11303, 11305
3IC10TC	10000	3XX092N	11303, 11305
3IC11TD	10000	3XX103N	11304, 11305
		3XX114N	11304, 11305



FO-102. TPU C-BIT Conversion Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

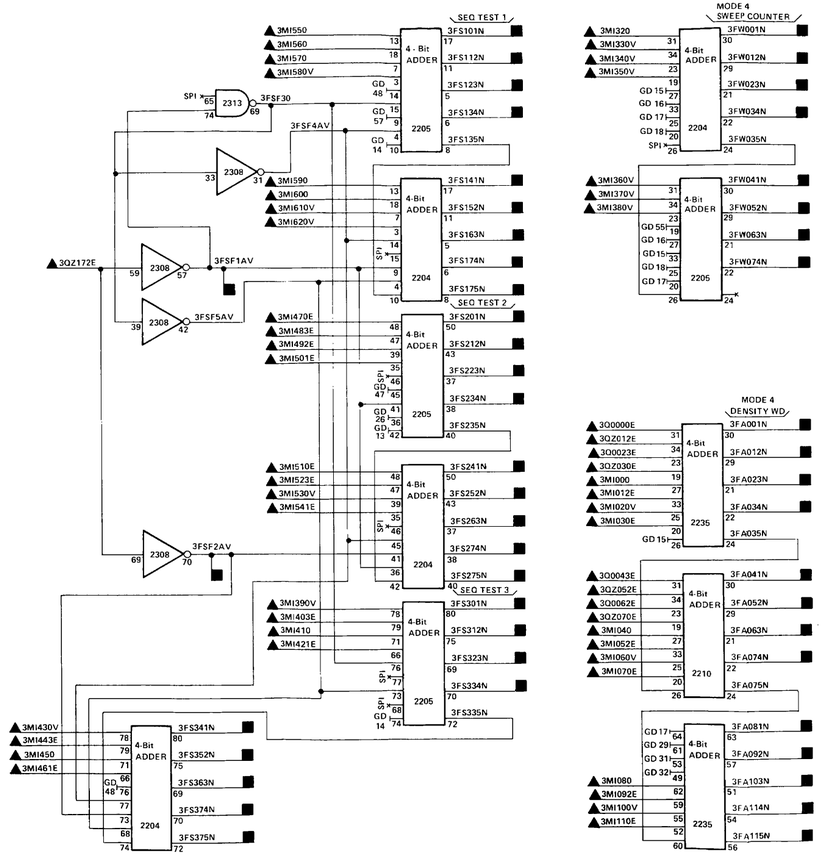
    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FORMTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2130, A2230, A2330, A2430, AND A2530.

MS200695

INPUT		INPUT		OUTPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH	SIGNAL	DESTINATION FO-SH
3MI000	11600	3MI541E	11600	3FA001N	11303	3FW041N	11306
3MI012E	11600	3MI550	11600	3FA012N	11303	3FW052N	11307
3MI020V	11600	3MI560	11600	3FA023N	11303	3FW063N	11307
3MI030E	11600	3MI570	11600	3FA034N	11303	3FW074N	10500
3MI040	11600	3MI580V	11600	3FA041N	11303		
3MI052E	11600	3MI590	11600	3FA052N	11303		
3MI060V	11600	3MI600	11600	3FA063N	11303		
3MI070E	11600	3MI610V	11600	3FA074N	11303		
3MI080	11600	3MI620V	11600	3FA081N	11303		
3MI092E	11600	3QZ012E	08503	3FA092N	11303		
3MI100V	11600	3QZ030E	08503	3FA103N	11304		
3MI110E	11600	3QZ052E	08503	3FA114N	11304		
3MI320	11600	3QZ070E	08503	3FA115N	10500		
3MI320	29501	3QZ172E	08503	3FSF1AV	09700		
3MI320	31401	3Q0000E	08503	3FSF2AV	10400		
3MI330V	11600	3Q0023E	08503	3FS101N	11308		
3MI330V	29501	3Q0043E	08502	3FS112N	11308		
3MI330V	31401	3Q0062E	08502	3FS123N	11308		
3MI340V	11600			3FS134N	11308		
3MI340V	29502			3FS141N	11308		
3MI340V	31401			3FS152N	11308		
3MI350V	11600			3FS163N	11308		
3MI350V	29502			3FS174N	11308		
3MI350V	31401			3FS175N	10400		
3MI360V	11600			3FS201N	11307		
3MI360V	29502			3FS212N	11307		
3MI360V	31401			3FS223N	11307		
3MI370V	11600			3FS234N	11307		
3MI370V	29502			3FS241N	11307		
3MI370V	31401			3FS252N	11308		
3MI380V	11600			3FS263N	11308		
3MI390V	11600			3FS274N	11308		
3MI403E	11600			3FS275N	10400		
3MI410	11600			3FS301N	11307		
3MI421E	11600			3FS312N	11307		
3MI430V	11600			3FS323N	11307		
3MI443E	11600			3FS334N	11307		
3MI450	11600			3FS341N	11307		
3MI461E	11600			3FS352N	11307		
3MI470E	11600			3FS363N	11307		
3MI483E	11600			3FS374N	11307		
3MI492E	11600			3FS375N	10400		
3MI501E	11600			3FW001N	11306		
3MI510E	11600			3FW012N	11306		
3MI523E	11600			3FW023N	11306		
3MI530V	11600			3FW034N	11306		



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2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INPUT FROM ANOTHER FIGURE
   - △ INPUT FROM SAME FIGURE
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   - □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   - ◪ OUTPUT TO SAME FIGURE
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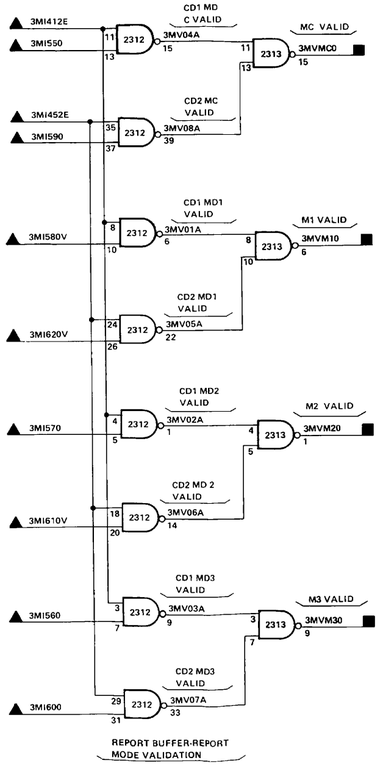
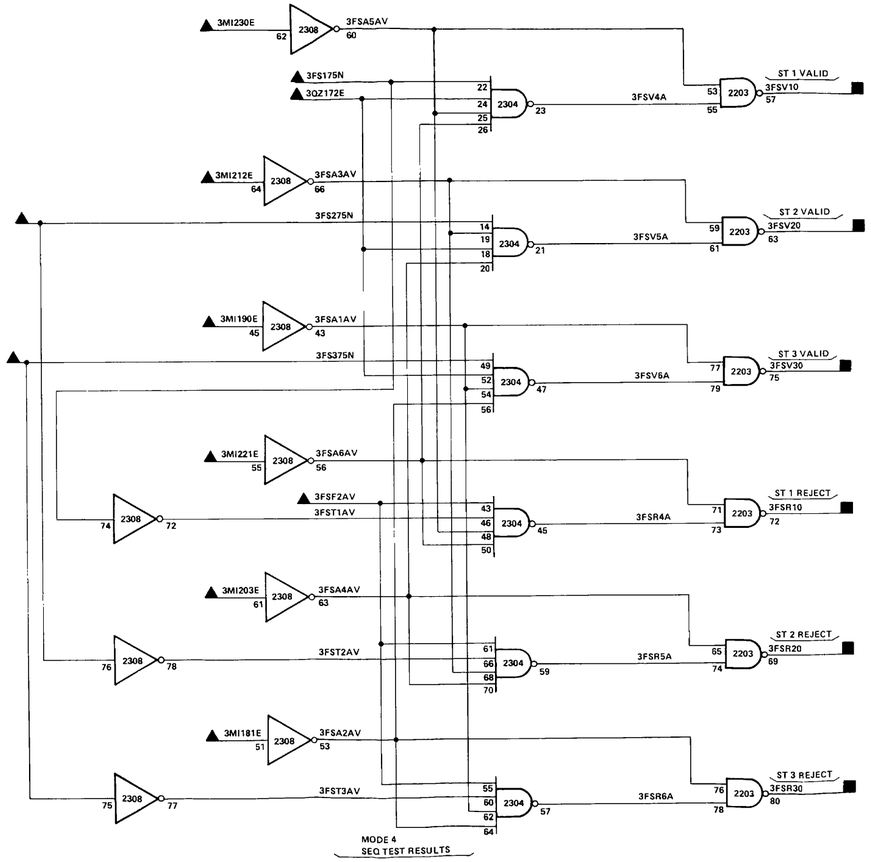
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.
    - B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2130, A2230, A2330, A2430, AND A2530.

MS200696

FO-103. TPU Sequential Test Adders and Mode 4 Processing Logic Diagram Logic Diagram

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3FSF2AV	10300	3FSR10	11305
3FS175N	10300	3FSR20	11305
3FS275N	10300	3FSR30	11304
3FS375N	10300	3FSV10	11305
3MI181E	11600	3FSV20	11305
3MI190E	11600	3FSV30	11304
3MI203E	11600	3MVMC0	11304
3MI212E	11600	3MVM10	11305
3MI221E	11600	3MVM20	11304
3MI230E	11600	3MVM30	11304
3MI412E	11600		
3MI452E	11600		
3MI550	11600		
3MI560	11600		
3MI570	11600		
3MI580V	11600		
3MI590	11600		
3MI600	11600		
3MI610V	11600		
3MI620V	11600		
3QZ172E	08503		





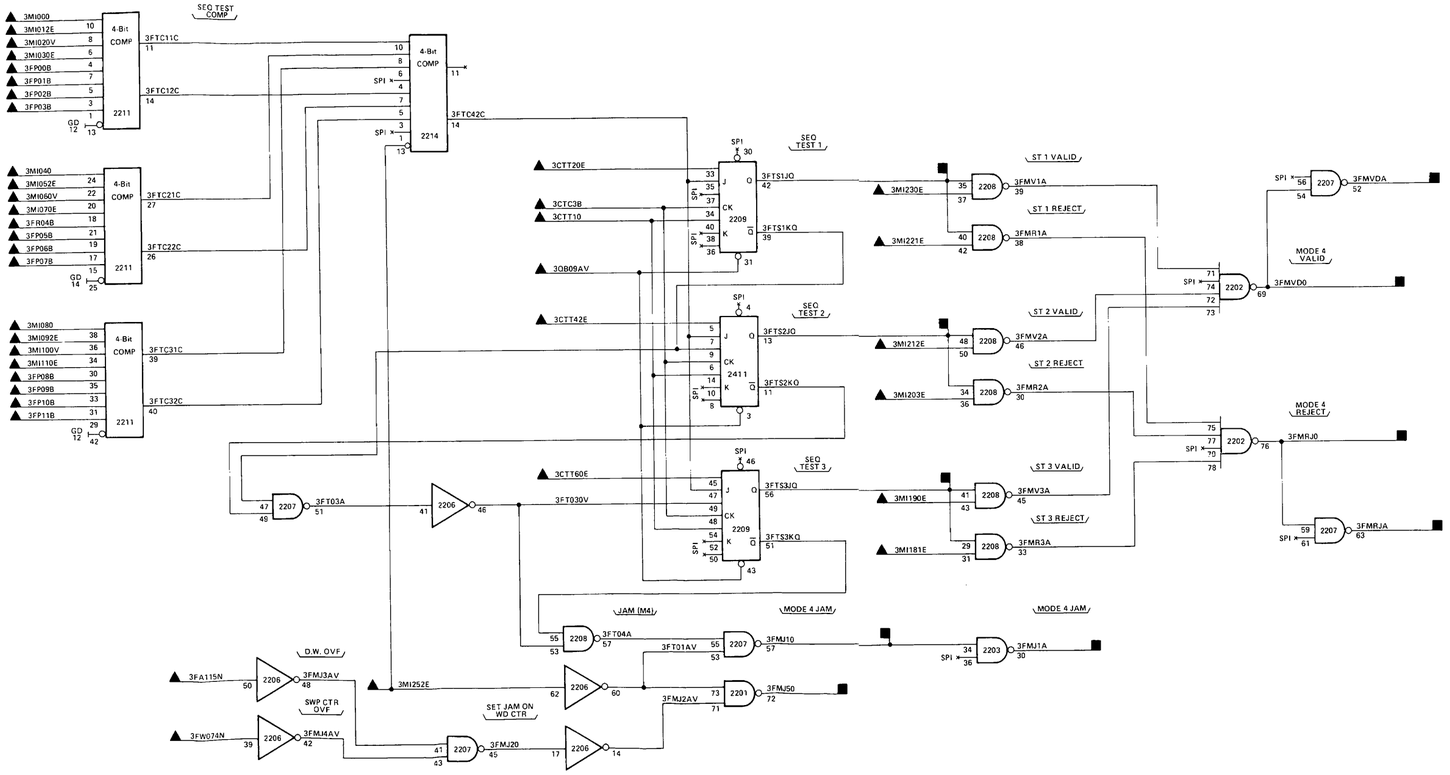
NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INPUT FROM ANOTHER FIGURE
   - △ INPUT FROM SAME FIGURE
   - ■ OUTPUT TO ANOTHER FIGURE
   - □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   - ◪ OUTPUT TO SAME FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.
    - B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2130, A2230, A2330, A2430, AND A2530.

MS200697

FO-104. TPU Report Buffer Mode Validation and Mode 4 Test Results Logic Diagram

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3CTC3B	07600	3FMJ1A	11700
3CTT10	07600	3FMJ10	11306
3CTT20E	07600	3FMJ50	11306
3CTT42E	07600	3FMRJA	11700
3CTT60E	07600	3FMRJ0	11306
3FA115N	10300	3FMVDA	11700
3FP00B	09200	3FMVD0	11306
3FP00B	26802	3FTS1JQ	12500
3FP01B	09200	3FTS2JQ	12500
3FP01B	26802	3FTS3JQ	12500
3FP02B	09200		
3FP02B	26802		
3FP03B	09200		
3FP03B	26802		
3FP04B	09200		
3FP04B	26802		
3FP05B	09200		
3FP05B	26802		
3FP06B	09200		
3FP06B	26802		
3FP07B	09200		
3FP07B	26802		
3FP08B	09200		
3FP08B	26802		
3FP09B	09200		
3FP09B	26802		
3FP10B	09200		
3FP10B	26802		
3FP11B	09200		
3FP11B	26802		
3FW074N	10300		
3MI000	11600		
3MI012E	11600		
3MI020V	11600		
3MI030E	11600		
3MI040	11600		
3MI052E	11600		
3MI060V	11600		
3MI070E	11600		
3MI080	11600		
3MI092E	11600		
3MI100V	11600		
3MI110E	11600		
3MI181E	11600		
3MI190E	11600		
3MI203E	11600		
3MI212E	11600		
3MI221E	11600		
3MI230E	11600		
3MI252E	11600		
3QB09AV	07701		



FO-105. TPU Mode 4 Valid, Reject and Jam Detect Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

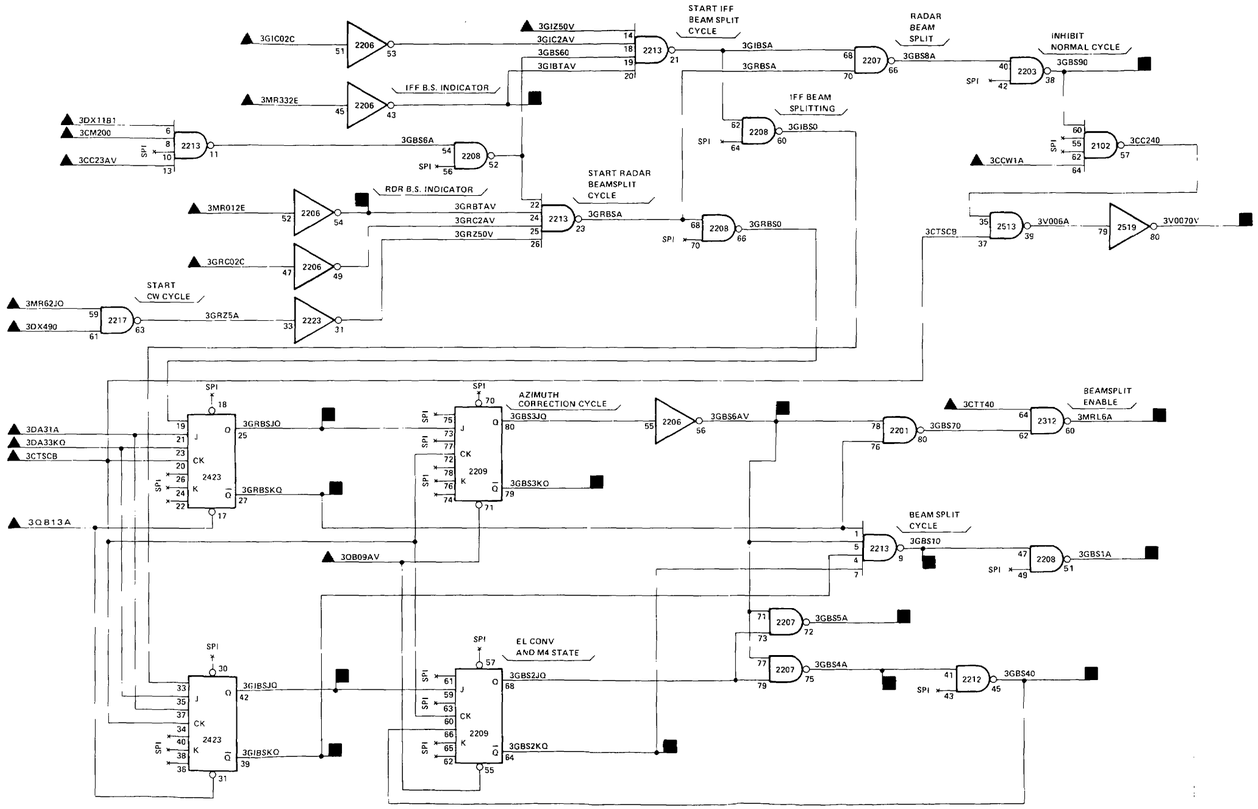
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2130, A2230, A2330, A2430, AND A2530.

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3CCW1A	09500	3GBS1A	09600, 11000, 11100, 12500
3CC23AV	09500	3GBS10	11000
3CM200	08200	3GBS2KQ	10000
3CTSCB	07600	3GBS3KQ	09600, 10700
3CTT40	11700	3GBS4A	11305, 11306
3DA31A	09000	3GBS40	11302, 11700, 12500
3DA33KQ	09000	3GBS5A	11309
3DX11B1	09200	3GBS6AV	11309
3DX11B1	29601	3GBS90	09000, 09500
3DX490	09700	3GIBSJQ	11500, 11700
3GIC02C	09800	3GIBSKQ	11306, 11307, 11308
3GIZ50V	09700	3GIBTAV	09100, 09600
3GRC02C	09800	3GRBSJQ	11700
3MR012E	11500	3GRBSKQ	11309
3MR332E	11500	3GRBTAV	09600
3MR62JQ	11700	3MRL6A	11500, 11700
3QB09AV	07701	3V0070V	11200
3QB13A	07701		



FO-106. Radar And IFF Beamsplit Cycle Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

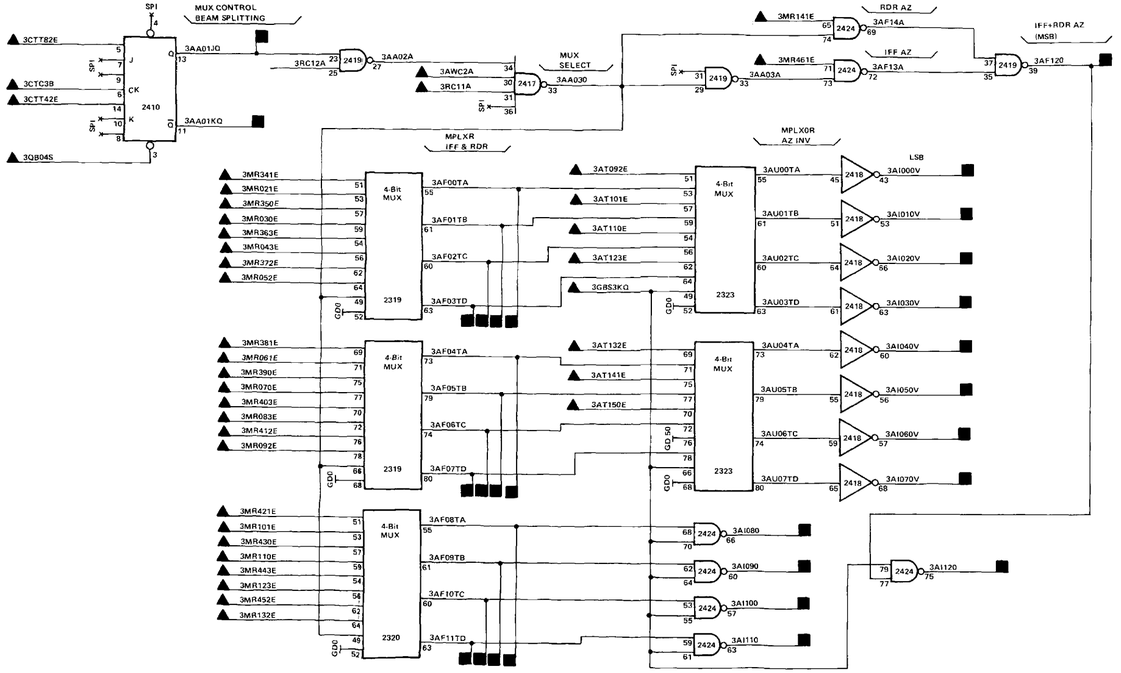
    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2130, A2230, A2330, A2430, AND A2530.

MS200699

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3AT092E	08100	3AA01JQ	10800, 10900
3AT101E	08100	3AA01KQ	10900
3AT110E	08100	3AF00TA	10800, 11303
3AT123E	08100	3AF01TB	10800, 11303
3AT132E	08100	3AF02TC	10800, 11303
3AT141E	08100	3AF03TD	10800, 11303
3AT150E	08100	3AF04TA	10800, 11303
3AWC2A	11000	3AF05TB	10800, 11303
3CTC3B	07600	3AF06TC	10800, 11303
3CTT42E	07600	3AF07TD	10800, 11303
3CTT82E	07600	3AF08TA	10800, 11304
3GBS3KQ	10600	3AF09TB	10800, 11304
3MR021E	11500	3AF10TC	10800, 11304
3MR030E	11500	3AF11TD	10800, 11304
3MR043E	11500	3AF120	10800, 11304
3MR052E	11500	3A1000V	10800, 11000, 11303
3MR061E	11500	3A1010V	10800, 11000, 11303
3MR070E	11500	3A1020V	10800, 11000, 11303
3MR083E	11500	3A1030V	10800, 11000, 11303
3MR092E	11500	3A1040V	10800, 11000, 11303
3MR101E	11500	3A1050V	10800, 11000, 11303
3MR110E	11500	3A1060V	10800, 11000, 11303
3MR123E	11500	3A1070V	10800, 11000, 11303
3MR132E	11500	3A1080	10800, 11000, 11304
3MR141E	11500	3A1090	10800, 11000, 11304
3MR341E	11500	3A1100	10800, 11000, 11304
3MR350E	11500	3A1110	10800, 11000, 11304
3MR363E	11500	3A1120	10800, 11000, 11304
3MR372E	11500		
3MR381E	11500		
3MR390E	11500		
3MR403E	11500		
3MR412E	11500		
3MR421E	11500		
3MR430E	11500		
3MR443E	11500		
3MR452E	11500		
3MR461E	11500		
3QB04S	07701		
3RC11A	11100		
3RC12A	11100		



FO-107. TPU IFF Radar Multiplexer Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

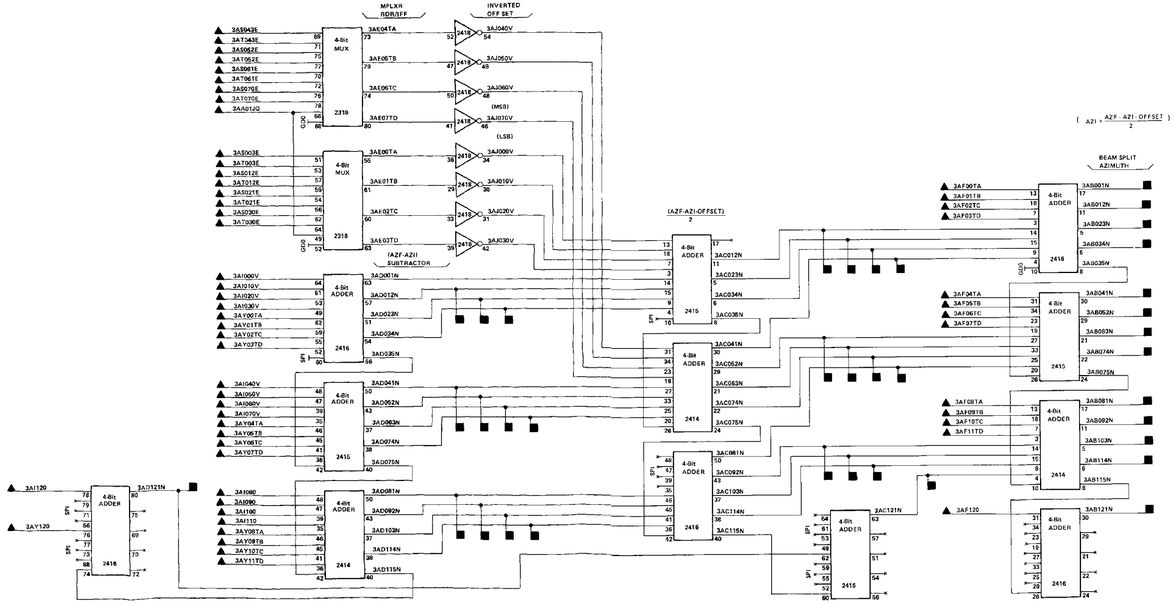
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

MS200700

INPUT SIGNAL	INPUT SOURCE FO-SH
3AA01JQ	10700
3AF007A	10700
3AF01TB	10700
3AF02TC	10700
3AF03TD	10700
3AF047A	10700
3AF05TB	10700
3AF06TC	10700
3AF07TD	10700
3AF087A	10700
3AF09TB	10700
3AF10TC	10700
3AF11TD	10700
3AF120	10700
3AI000V	10700
3AI010V	10700
3AI020V	10700
3AI030V	10700
3AI040V	10700
3AI050V	10700
3AI060V	10700
3AI070V	10700
3AI080	10700
3AI090	10700
3AI100	10700
3AI110	10700
3AI120	10700
3AS003E	08100
3AS012E	08100
3AS021E	08100
3AS030E	08100
3AS043E	08100
3AS052E	08100
3AS061E	08100
3AS070E	08100
3AT003E	08100
3AT012E	08100
3AT021E	08100
3AT030E	08100
3AT043E	08100
3AT052E	08100
3AT061E	08100
3AT070E	08100
3AY00TA	07900
3AY01TB	07900
3AY02TC	07900

INPUT SIGNAL	INPUT SOURCE FO-SH
3AY03TD	07900
3AY04TA	07900
3AY05TB	07900
3AY06TC	07900
3AY07TD	07900
3AY08TA	07900
3AY09TB	07900
3AY10TC	07900
3AY11TD	07900
3AY120	07900

OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
3AB001N	11303, 11306
3AB012N	11303, 11306
3AB023N	11303, 11306
3AB034N	11303, 11307
3AB041N	11303, 11307
3AB052N	11303, 11307
3AB063N	11303, 11307
3AB074N	11303, 11307
3AB081N	11304, 11307
3AB092N	11304, 11307
3AB103N	11304, 11307
3AB114N	11304, 11307
3AB121N	11304, 11307
3AC012N	10900
3AC023N	10900
3AC034N	10900
3AC041N	10900
3AC052N	10900
3AC063N	10900
3AC074N	10900
3AC081N	10900
3AC092N	10900
3AC103N	10900
3AC114N	10900
3AC121N	10900
3AD012N	10900
3AD023N	10900
3AD034N	10900
3AD041N	10900
3AD052N	10900
3AD063N	10900
3AD074N	10900
3AD081N	10900
3AD092N	10900
3AD103N	10900
3AD114N	10900
3AD121N	10900



FO-108. TPU Beamsplit Azimuth Multiplexer Subtractor Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

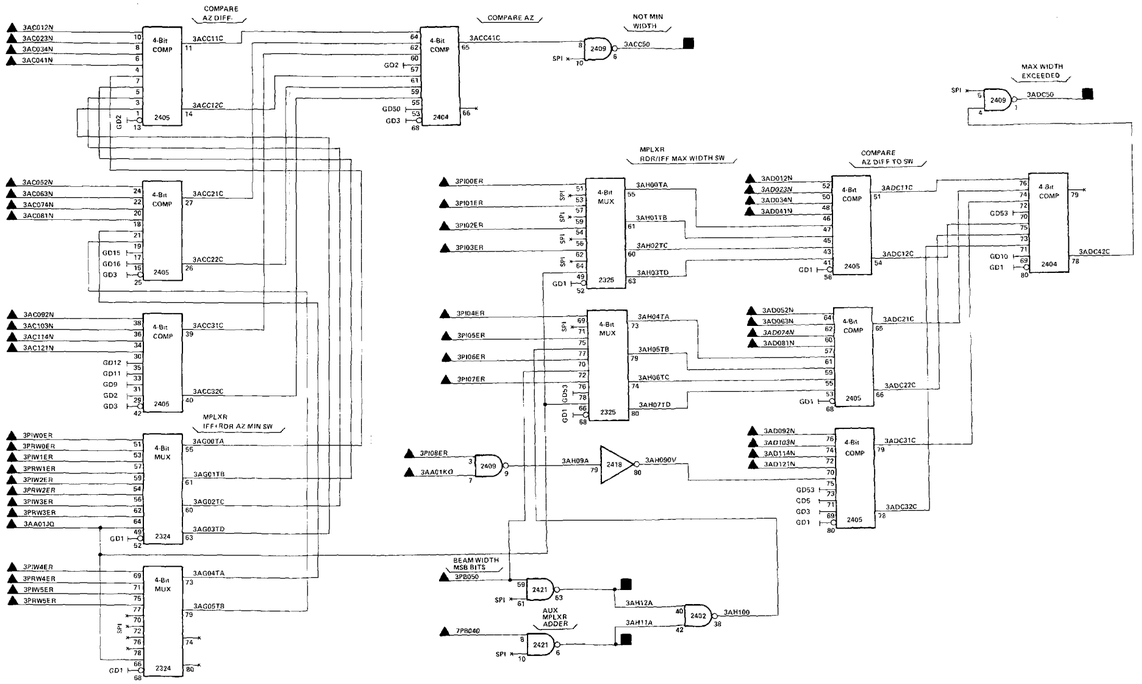
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2130, A2230, A2330, A2430, AND A2530..

INPUT SIGNAL	INPUT SOURCE FO-SH	INPUT SIGNAL	INPUT SOURCE FO-SH	OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
3AA01JQ	10700	3PRW4ER	12000	3ACC50	11700, 12500
3AA01KQ	10700	3PRW5ER	12000	3ADC50	09600, 12500
3AC012N	10800	7PB040	26802	3AH11A	12000
3AC023N	10800	7PB040	32000	3AH12A	12000
3AC034N	10800	7PB040	32101		
3AC041N	10800				
3AC052N	10800				
3AC063N	10800				
3AC074N	10800				
3AC081N	10800				
3AC092N	10800				
3AC103N	10800				
3AC114N	10800				
3AC121N	10800				
3AD012N	10800				
3AD023N	10800				
3AD034N	10800				
3AD041N	10800				
3AD052N	10800				
3AD063N	10800				
3AD074N	10800				
3AD081N	10800				
3AD092N	10800				
3AD103N	10800				
3AD114N	10800				
3AD121N	10800				
3PB050	26802				
3PIW0ER	12000				
3PIW1ER	12000				
3PIW2ER	12000				
3PIW3ER	12000				
3PIW4ER	12000				
3PIW5ER	12000				
3PI00ER	12100				
3PI01ER	12100				
3PI02ER	12100				
3PI03ER	12100				
3PI04ER	12100				
3PI05ER	12100				
3PI06ER	12100				
3PI07ER	12100				
3PI08ER	12100				
3PRW0ER	12000				
3PRW1ER	12000				
3PRW2ER	12000				
3PRW3ER	12000				



FO-109. TPU Azimuth Min/Max Width Comparator Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

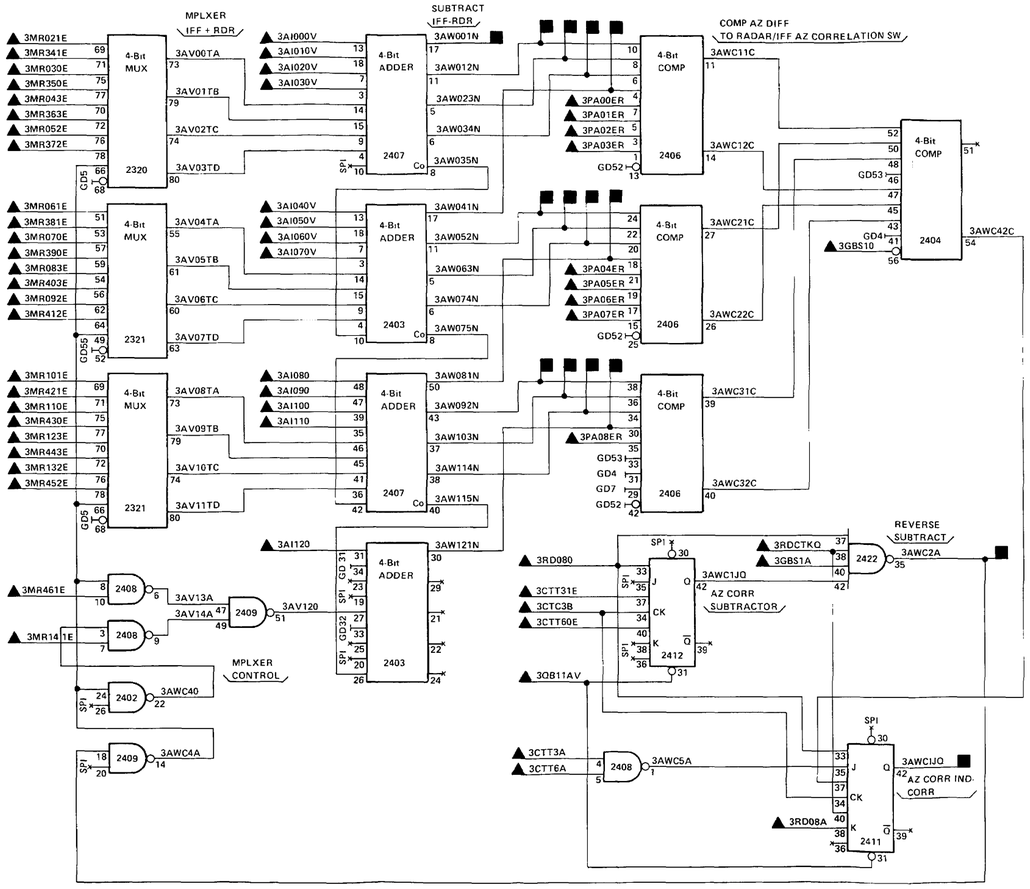
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2130, A2330, A2430, AND A2530.

M5200702

INPUT SIGNAL	SOURCE FO-SH
3AI000V	10700
3AI010V	10700
3AI020V	10700
3AI030V	10700
3AI040V	10700
3AI050V	10700
3AI060V	10700
3AI070V	10700
3AI080	10700
3AI090	10700
3AI100	10700
3AI110	10700
3AI120	10700
3CTC3B	07600
3CTT3A	07600
3CTT31E	07600
3CTT6A	07600
3CTT60E	07600
3GBS1A	10600
3GBS10	10600
3MR021E	11500
3MR030E	11500
3MR043E	11500
3MR052E	11500
3MR061E	11500
3MR070E	11500
3MR083E	11500
3MR092E	11500
3MR101E	11500
3MR110E	11500
3MR123E	11500
3MR132E	11500
3MR141E	11500
3MR341E	11500
3MR350E	11500
3MR363E	11500
3MR372E	11500
3MR381E	11500
3MR390E	11500
3MR403E	11500
3MR412E	11500
3MR421E	11500
3MR430E	11500
3MR443E	11500
3MR452E	11500
3MR461E	11500

INPUT SIGNAL	SOURCE FO-SH
3PA00ER	12100
3PA01ER	12100
3PA02ER	12100
3PA03ER	12100
3PA04ER	12100
3PA05ER	12100
3PA06ER	12100
3PA07ER	12100
3PA08ER	12100
3QB11AV	07701
3RDCTKQ	11100
3RD08A	11100
3RD080	11100

OUTPUT SIGNAL	DESTINATION FO-SH
3AWC1JQ	11100, 12500
3AWC2A	10700
3AW001N	11303
3AW012N	11303
3AW023N	11303
3AW034N	11303
3AW041N	11303
3AW052N	11303
3AW063N	11303
3AW074N	11303
3AW081N	11304
3AW092N	11304
3AW103N	11304
3AW114N	11304
3AW121N	11304



FO-110. TPU Radar/IFF Azimuth Correlation Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2130, A2330, A2430, AND A2530.

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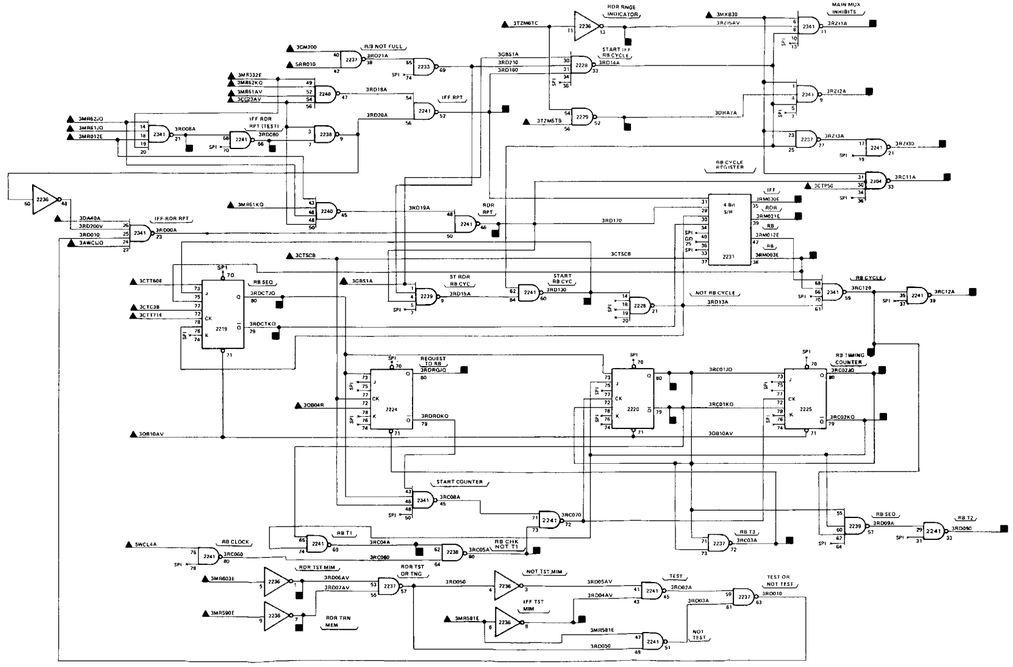
INPUT		OUTPUT	
	SOURCE		DESTINATION
SIGNAL	FO-SH	SIGNAL	FO-SH
3AWC1JQ	11000	3DRA7A	08700
3CC23AV	09500	3RC01JQ	11309
3CM200	08200	3RC01KQ	11309
3CTC3B	07600	3RC02JQ	11309
3CTF50	07600	3RC02KQ	11306, 11309
3CTSCB	07600	3RC03A	11309
3CTT60E	07600	3RC04A	11301
3CTT71E	07600	3RC05A	11400
3DA40A	09600	3RC11A	10700
3GBS1A	10600	3RC12A	09600, 10700, 12500
3MRC12E	11500	3RC120	11301, 11309
3MR332E	11500	3RDCTJQ	08900, 11200
3MR581E	11500	3RDCTKQ	08900, 11000, 11200, 11700
3MR590E	11500	3RDRQJQ	05900, 06600, 06800, 12500,
3MR603E	11500		26802
3MR61AV	11700	3RD04AV	11200
3MR61JQ	11700	3RD06AV	11200
3MR61KQ	11700	3RD07AV	11200
3MR62JQ	11700	3RD08A	11000
3MR62KQ	11700	3RD080	11000, 12500
3MXB30	11301	3RD090	11309
3QB04R	07701	3RD130	12500
3QB10AV	07701	3RD160	11301
3TZM5TB	12300	3RD170	11301
3TZM6TC	12300	3RM021E	11200, 11700
5RR010	07201	3RM030E	11200, 11309, 11700
5WCL4A	06600	3RZI1A	11305
		3RZI2A	11304
		3RZI30	11304
		3RZI5AV	08700



FO-111. TPU Report Buffer Interface Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2130, A2230, A2430, AND A2530.

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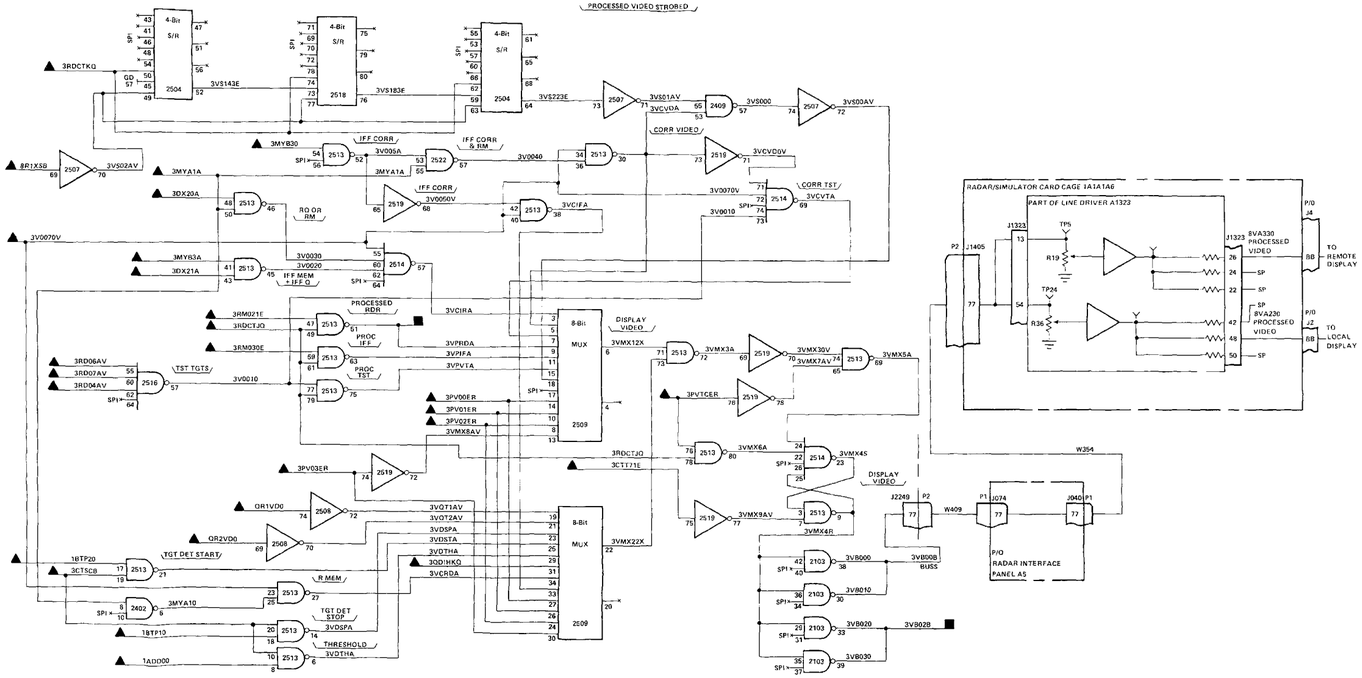
INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
QR1VD0	26802	3VB028	26802
QR2VD0	26802	3VPRDA	08900
1AD000	16300		
1BTP10	16300		
1BTP10	27502		
1BTP20	16300		
1BTP20	26802		
1BTP20	27502		
3CTSCB	07600		
3CTT71E	07600		
3DX20A	09300		
3DX21A	09300		
3MYA1A	09300		
3MYB3A	09300		
3MYB30	09300		
3PVTCER	12000		
3PV00ER	12000		
3PV01ER	12000		
3PV02ER	12000		
3PV03ER	12000		
3QD1HKQ	08300		
3RDCTJQ	11100		
3RDCTKQ	11100		
3RD04AV	11100		
3RD06AV	11100		
3RD07AV	11100		
3RM021E	11100		
3RM030E	11100		
3V0070V	10600		
8R1XSB	00300		
8R1XSB	14900		
8R1XSB	26802		
8R1XSB	26803		
8R1XSB	27501		



FO-112. TPU Processed/Display Video Logic Diagram

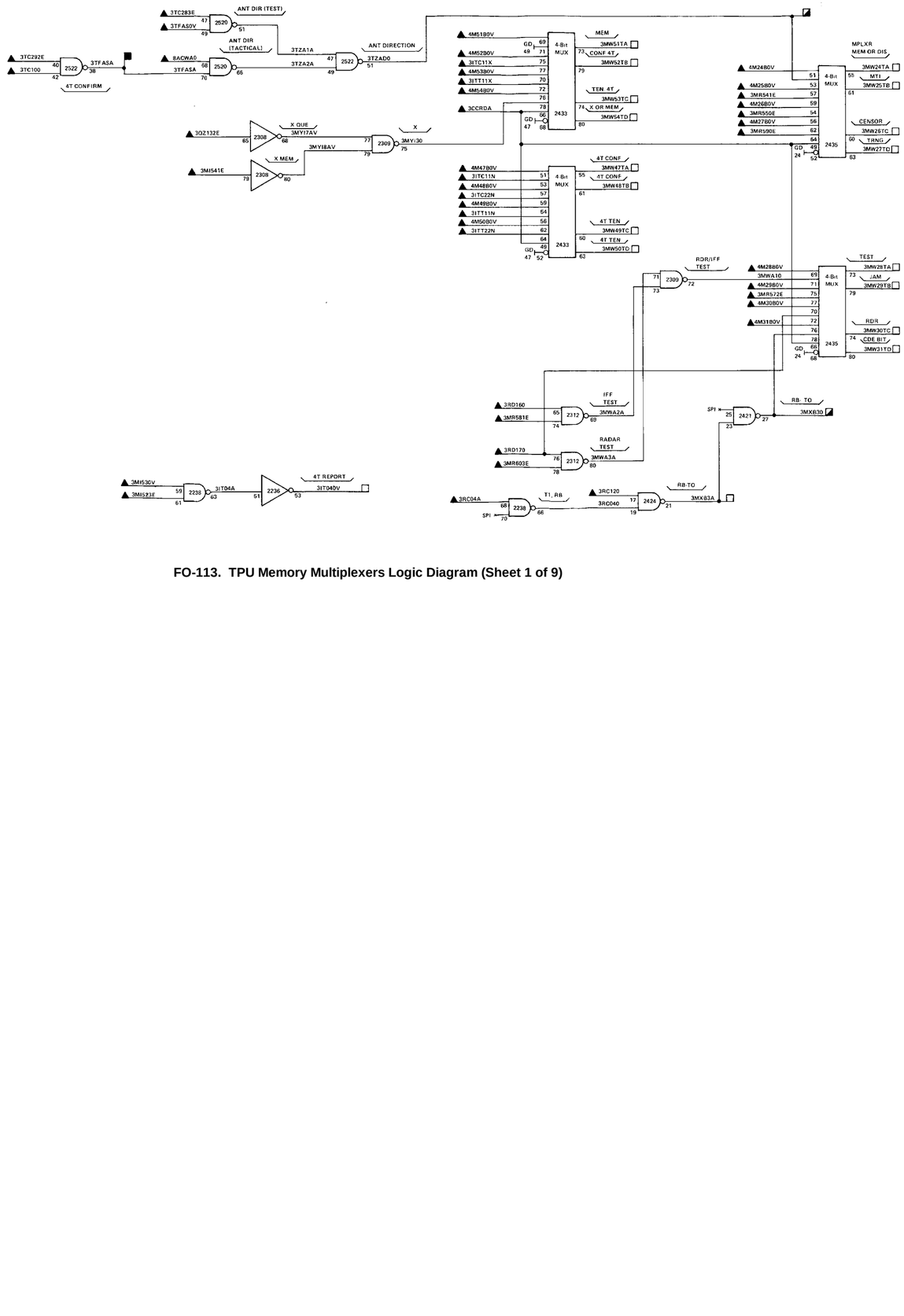
NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INPUT FROM ANOTHER FIGURE
   - △ INPUT FROM SAME FIGURE
   - ■ OUTPUT TO ANOTHER FIGURE
   - □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   - ◪ OUTPUT TO SAME FIGURE
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.
    - B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2130, A2230, A2430, AND A2530.

INPUT SIGNAL	SOURCE FO-SH
3CCRDA	09500
3ITC11X	10100
3ITC11N	10100
3ITC22N	10100
3ITT11X	10100
3ITT11N	10100
3ITT22N	10100
3MI523E	11600
3MI530V	11600
3MI541E	11600
3MR541E	11500
3MR550E	11500
3MR572E	11500
3MR581E	11500
3MR590E	11500
3MR603E	11500
3QZ132E	08503
3RC04A	11100
3RC120	11100
3RD160	11100
3RD170	11100
3TC100	12300
3TC283E	12300
3TC292E	12300
3TFAS0V	07900
4M24B0V	06401
4M24B0V	26802
4M25B0V	06401
4M25B0V	26802
4M26B0V	06401
4M26B0V	26802
4M27B0V	06401
4M27B0V	26802
4M28B0V	06401
4M28B0V	26802
4M29B0V	06401
4M29B0V	26802
4M30B0V	06401
4M31B0V	06401
4M47B0V	06402
4M48B0V	06402
4M48B0V	26802
4M49B0V	06402
4M49B0V	26802
4M50B0V	06402
4M50B0V	26802

INPUT SIGNAL	SOURCE FO-SH
4M51B0V	06402
4M51B0V	26802
4M52B0V	06402
4M52B0V	26802
4M53B0V	06402
4M53B0V	26802
4M54B0V	06402
BACWA0	16900
BACWA0	26802
BACWAD	28500

OUTPUT SIGNAL	DESTINATION FO-SH
3MXB3D	11100
3TFASA	07600, 07900
3TZAD0	07900



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

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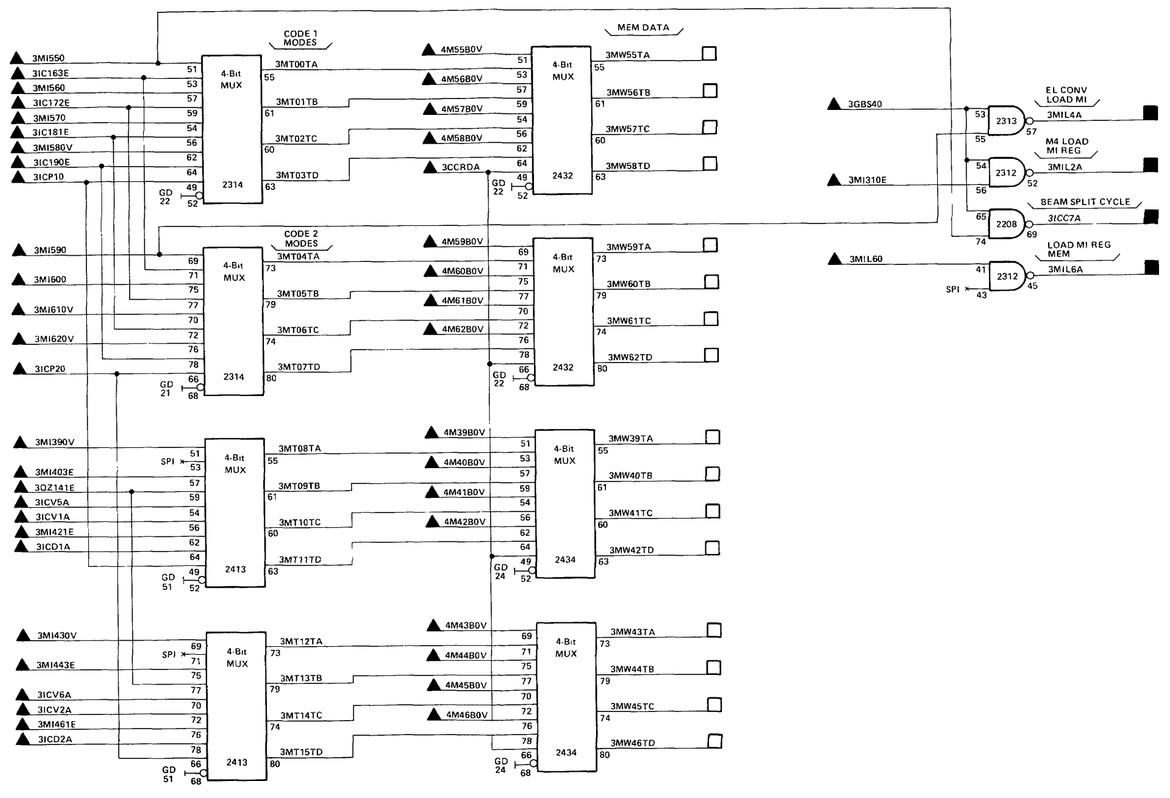
FO-113. TPU Memory Multiplexers Logic Diagram (Sheet 1 of 9)

INPUT		INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3CCRDA	09500	4M56B0V	06402	3ICC7A	10000, 11600
3GBS40	10600	4M56B0V	26802	3MIL2A	11600
3ICD1A	10000	4M57B0V	06402	3MIL4A	11600
3ICD2A	10000	4M57B0V	26802	3MIL6A	11600
3ICP10	09900	4M58B0V	06402		
3ICP20	09900	4M58B0V	26802		
3ICV1A	10000	4M59B0V	06402		
3ICV2A	10000	4M59B0V	26802		
3ICV5A	10000	4M60B0V	06402		
3ICV6A	10000	4M60B0V	26802		
3IC163E	09900	4M61B0V	06402		
3IC172E	09900	4M61B0V	26802		
3IC181E	09900	4M62B0V	06402		
3IC190E	09900				
3MIL60	09500				
3MI310E	11600				
3MI390V	11600				
3MI403E	11600				
3MI421E	11600				
3MI430V	11600				
3MI443E	11600				
3MI461E	11600				
3MI550	11600				
3MI560	11600				
3MI570	11600				
3MI580V	11600				
3MI590	11600				
3MI600	11600				
3MI610V	11600				
3MI620V	11600				
3QZ141E	08503				
4M39B0V	06402				
4M40B0V	06402				
4M40B0V	26802				
4M41B0V	06402				
4M41B0V	26802				
4M42B0V	06402				
4M42B0V	26802				
4M43B0V	06402				
4M43B0V	26802				
4M44B0V	06402				
4M44B0V	26802				
4M45B0V	06402				
4M45B0V	26802				
4M46B0V	06402				
4M55B0V	06402				



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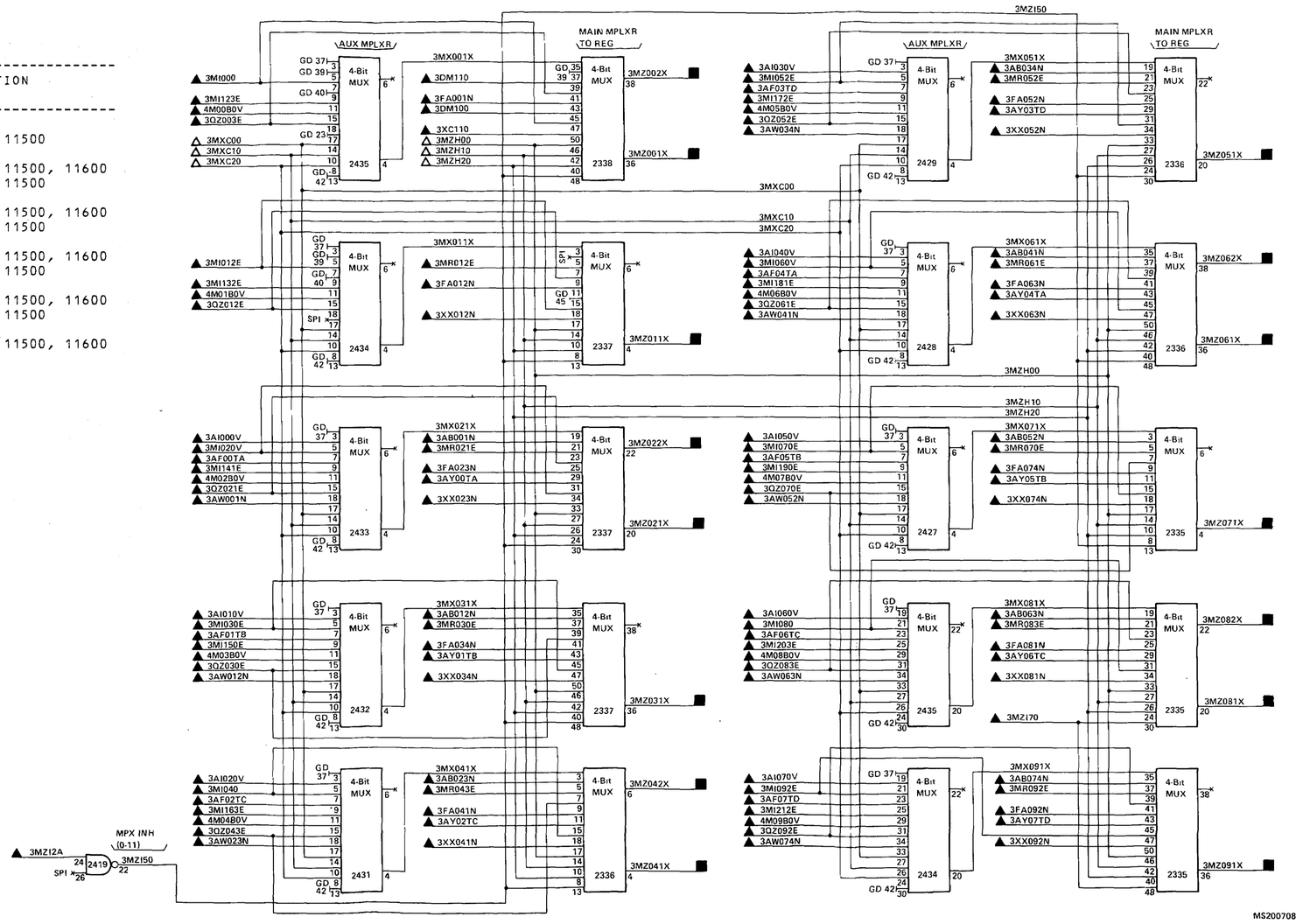
FO-113. TPU Memory Multiplexers Logic Diagram (Sheet 2 of 9)

INPUT SIGNAL	INPUT SOURCE FO-SH
3AB001N	10800
3AB012N	10800
3AB023N	10800
3AB034N	10800
3AB041N	10800
3AB052N	10800
3AB063N	10800
3AB074N	10800
3AF00TA	10700
3AF01TB	10700
3AF02TC	10700
3AF03TD	10700
3AF04TA	10700
3AF05TB	10700
3AF06TC	10700
3AF07TD	10700
3AI000V	10700
3AI010V	10700
3AI020V	10700
3AI030V	10700
3AI040V	10700
3AI050V	10700
3AI060V	10700
3AI070V	10700
3AW001N	11000
3AW012N	11000
3AW023N	11000
3AW034N	11000
3AW041N	11000
3AW052N	11000
3AW063N	11000
3AW074N	11000
3AY00TA	07900
3AY01TB	07900
3AY02TC	07900
3AY03TD	07900
3AY04TA	07900
3AY05TB	07900
3AY06TC	07900
3AY07TD	07900
3DM100	09400
3DM110	09400
3FA001N	10300
3FA012N	10300
3FA023N	10300
3FA034N	10300

INPUT SIGNAL	INPUT SOURCE FO-SH
3FA041N	10300
3FA052N	10300
3FA063N	10300
3FA074N	10300
3FA081N	10300
3FA092N	10300
3MI000	11600
3MI012E	11600
3MI020V	11600
3MI030E	11600
3MI040	11600
3MI052E	11600
3MI060V	11600
3MI070E	11600
3MI080	11600
3MI092E	11600
3MI123E	11600
3MI132E	11600
3MI141E	11600
3MI150E	11600
3MI163E	11600
3MI172E	11600
3MI181E	11600
3MI190E	11600
3MI203E	11600
3MI212E	11600
3MR012E	11500
3MR021E	11500
3MR030E	11500
3MR043E	11500
3MR052E	11500
3MR061E	11500
3MR070E	11500
3MR083E	11500
3MR092E	11500
3MZI2A	09300
3MZI70	09300
3QZ003E	08503
3QZ012E	08503
3QZ021E	08503
3QZ030E	08503
3QZ043E	08503
3QZ052E	08503
3QZ061E	08503
3QZ070E	08503
3QZ083E	08503

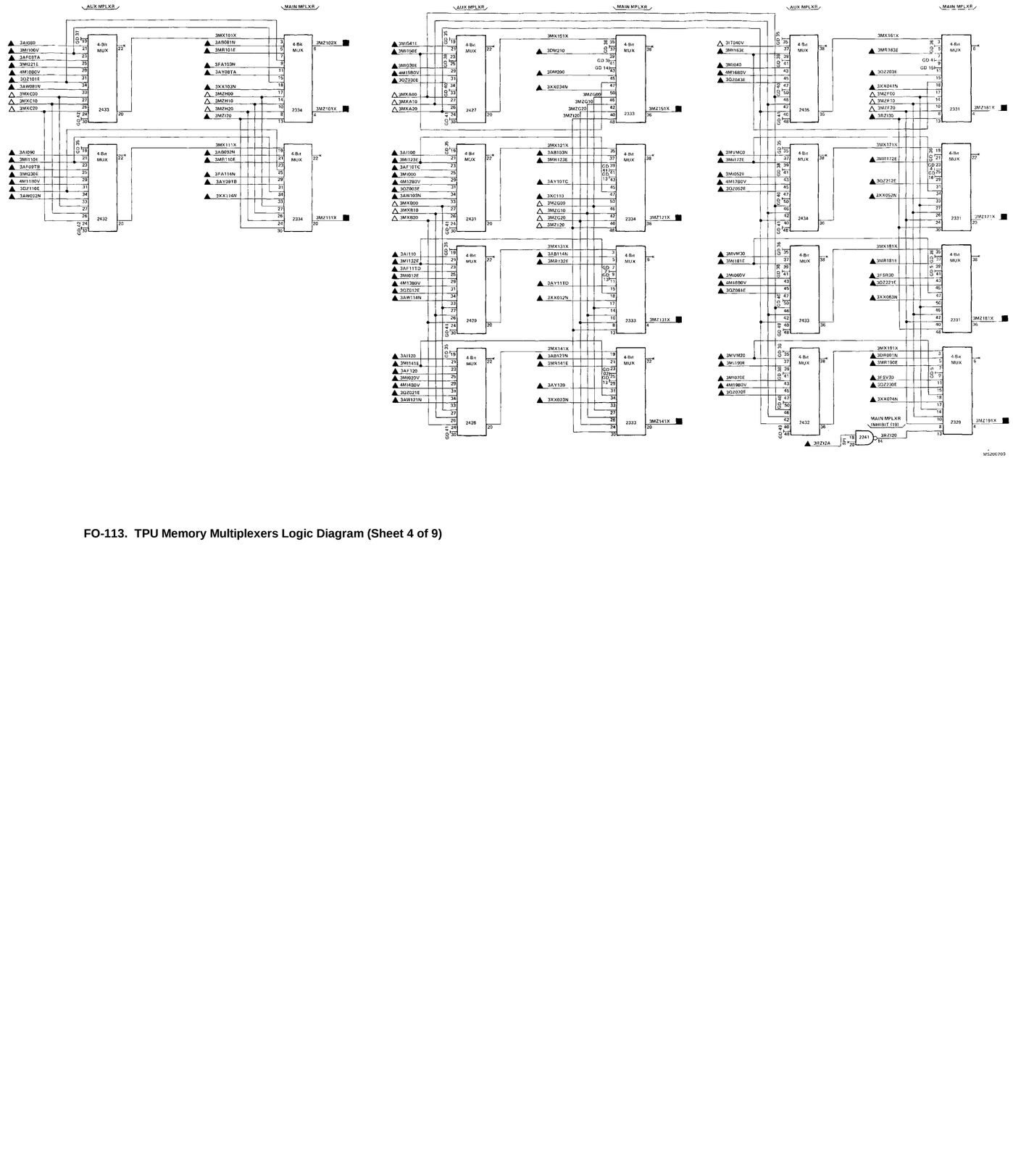
INPUT SIGNAL	INPUT SOURCE FO-SH
3QZ092E	08503
3XC110	10200
3XX012N	10200
3XX023N	10200
3XX034N	10200
3XX041N	10200
3XX052N	10200
3XX063N	10200
3XX074N	10200
3XX081N	10200
3XX092N	10200
4M00B0V	06401
4M00B0V	26802
4M01B0V	06401
4M01B0V	26802
4M02B0V	06401
4M02B0V	26802
4M03B0V	06401
4M03B0V	26802
4M04B0V	06401
4M04B0V	26802
4M05B0V	06401
4M05B0V	26802
4M06B0V	06401
4M07B0V	06401
4M08B0V	06401
4M08B0V	26802
4M09B0V	06401
4M09B0V	26802

OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
3MZ001X	11400, 11500
3MZ002X	11600
3MZ011X	11400, 11500, 11600
3MZ021X	11400, 11500
3MZ022X	11600
3MZ031X	11400, 11500, 11600
3MZ041X	11400, 11500
3MZ042X	11600
3MZ051X	11400, 11500, 11600
3MZ061X	11400, 11500
3MZ062X	11600
3MZ071X	11400, 11500, 11600
3MZ081X	11400, 11500
3MZ082X	11600
3MZ091X	11400, 11500, 11600



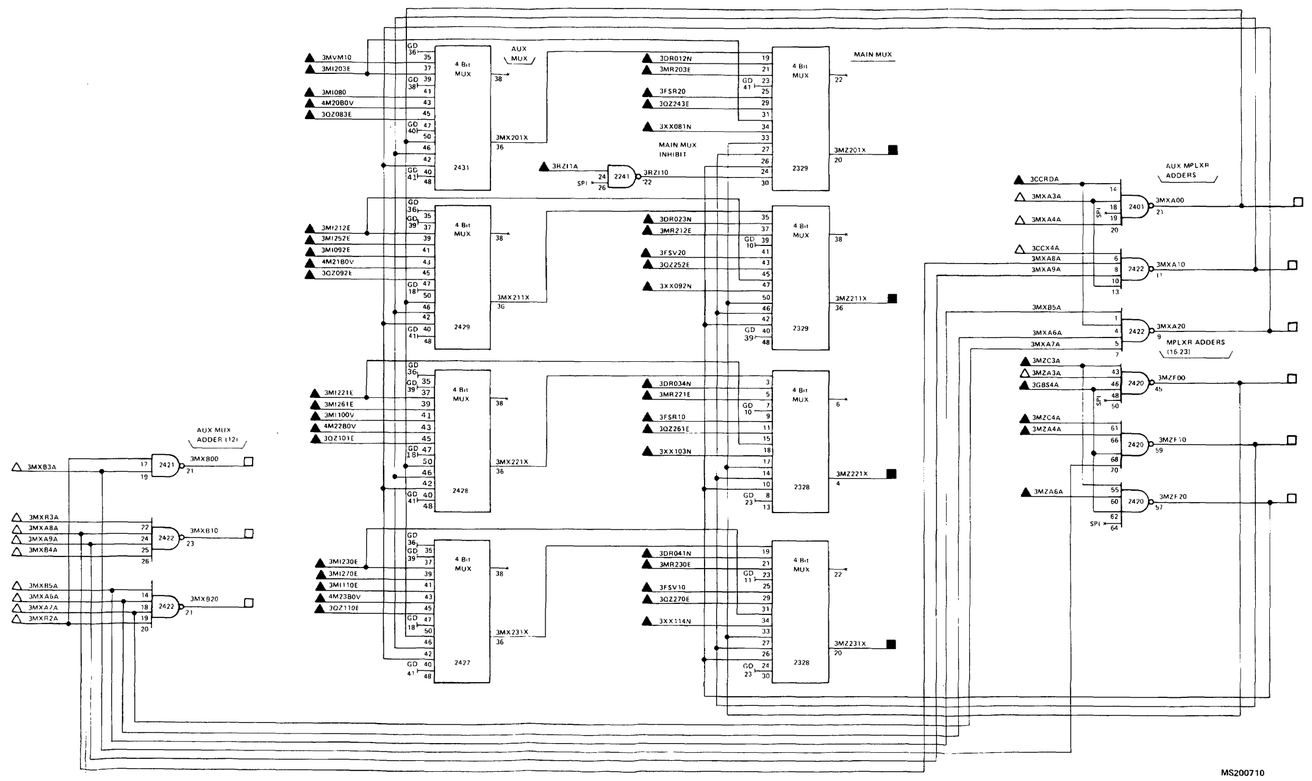
FO-113. TPU Memory Multiplexers Logic Diagram (Sheet 3 of 9)

INPUT SIGNAL	INPUT SOURCE FO-SH	INPUT SIGNAL	INPUT SOURCE FO-SH	OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
3AB081N	10800	3MR123E	11500	3MZ101x	11400, 11500
3AB092N	10800	3MR132E	11500	3MZ102x	11600
3AB103N	10800	3MR141E	11500	3MZ111X	11400, 11500, 11600
3AB114N	10800	3MR163E	11500	3MZ121X	11400, 11500, 11600
3AB121N	10800	3MR181E	11500	3MZ131X	11400, 11500, 11600
3AF08TA	10700	3MR190E	11500	3MZ141X	11400, 11500, 11600
3AF09TB	10700	3MVMC0	10400	3MZ161X	11400, 11500, 11600
3AF10TC	10700	3MVM20	10400	3MZ171X	11400, 11500, 11600
3AF11TD	10700	3MVM30	10400	3MZ181X	11400, 11500, 11600
3AF120	10700	3MZ170	09300	3MZ191X	11400, 11500, 11600
3AI080	10700	3QZ003E	08503		
3AI090	10700	3QZ012E	08503		
3AI100	10700	3QZ021E	08503		
3AI110	10700	3QZ030E	08503		
3AI120	10700	3QZ043E	08503		
3AW081N	11000	3QZ052E	08503		
3AW092N	11000	3QZ061E	08503		
3AW103N	11000	3QZ070E	08503		
3AW114N	11000	3QZ101E	08503		
3AW121N	11000	3QZ110E	08503		
3AY08TA	07900	3QZ203E	08501		
3AY09TB	07900	3QZ212E	08501		
3AY10TC	07900	3QZ221E	08501		
3AY11TD	07900	3QZ230E	08501		
3AY120	07900	3RZ12A	11100		
3DM200	09400	3RZ130	11100		
3DM210	09400	3XC110	10200		
3DR001N	08800	3XX012N	10200		
3FA103N	10300	3XX023N	10200		
3FA114N	10300	3XX034N	10200		
3FSR30	10400	3XX041N	10200		
3FSV30	10400	3XX052N	10200		
3M1000	11600	3XX063N	10200		
3M1012E	11600	3XX074N	10200		
3M1020V	11600	3XX103N	10200		
3M1030E	11600	3XX114N	10200		
3M1040	11600	4M10B0V	06401		
3M1052E	11600	4M10B0V	26802		
3M1060V	11600	4M11B0V	06401		
3M1070E	11600	4M11B0V	26802		
3M1100V	11600	4M12B0V	06401		
3M1110E	11600	4M12B0V	26802		
3M1123E	11600	4M13B0V	06401		
3M1132E	11600	4M13B0V	26802		
3M1141E	11600	4M14B0V	06401		
3M1150E	11600	4M15B0V	06401		
3M1163E	11600	4M16B0V	06401		
3M1172E	11600	4M16B0V	26802		
3M1181E	11600	4M17B0V	06401		
3M1190E	11600	4M17B0V	26802		
3M1221E	11600	4M18B0V	06401		
3M1230E	11600	4M18B0V	26802		
3M1541E	11600	4M19B0V	06401		
3MR101E	11500	4M19B0V	26802		
3MR110E	11500				



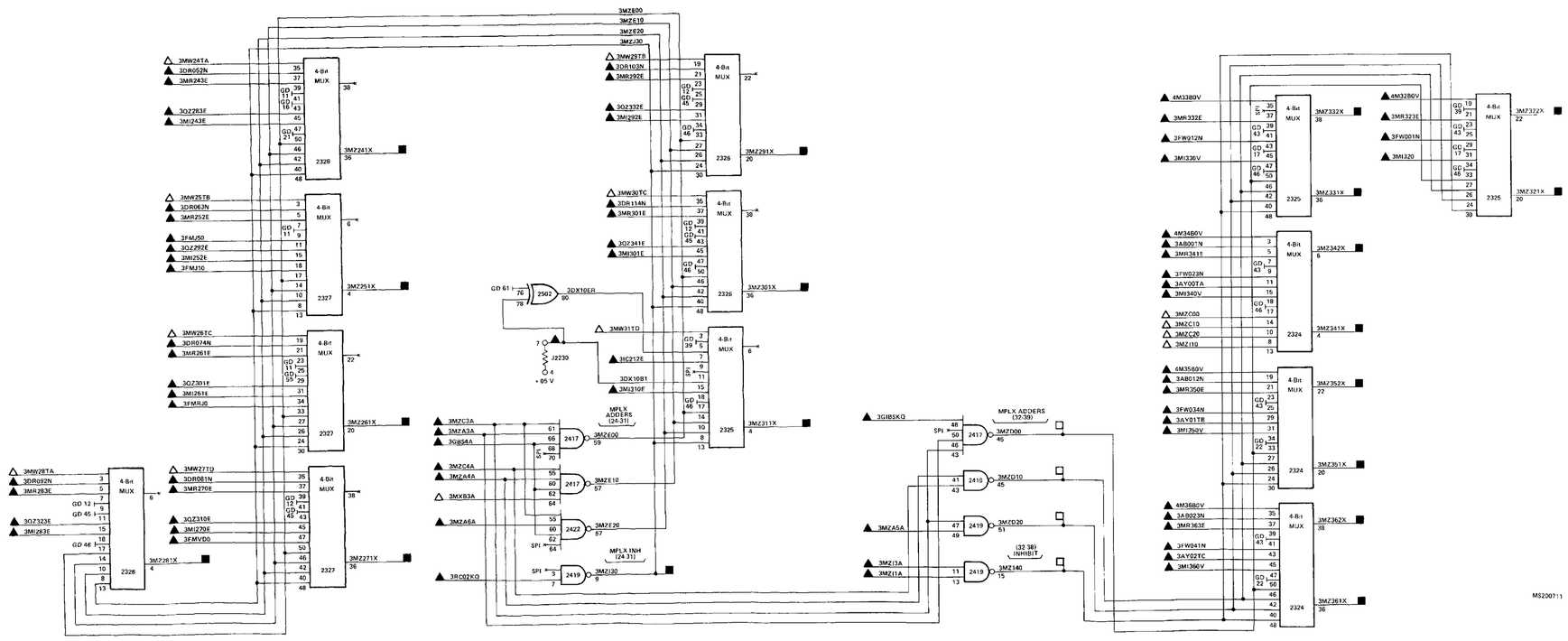
FO-113. TPU Memory Multiplexers Logic Diagram (Sheet 4 of 9)

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3CCRDA	09500	3MZ201X	11400, 11500, 11600
3DR012N	08800	3MZ211X	11400, 11500, 11600
3DR023N	08800	3MZ221X	11400, 11500, 11600
3DR034N	08800	3MZ231X	11400, 11500, 11600
3DR041N	08800		
3FSR10	10400		
3FSR20	10400		
3FSV10	10400		
3FSV20	10400		
3GBS4A	10600		
3MI080	11600		
3MI092E	11600		
3MI100V	11600		
3MI110E	11600		
3MI203E	11600		
3MI212E	11600		
3MI221E	11600		
3MI230E	11600		
3MI252E	11600		
3MI261E	11600		
3MI270E	11600		
3MR203E	11500		
3MR212E	11500		
3MR221E	11500		
3MR230E	11500		
3MVM10	10400		
3MZA4A	09300		
3MZA6A	09300		
3MZC3A	09300		
3MZC4A	09300		
3QZ083E	08503		
3QZ092E	08503		
3QZ101E	08503		
3QZ110E	08503		
3QZ243E	08501		
3QZ252E	08501		
3QZ261E	08501		
3QZ270E	08501		
3RZ11A	11100		
3XX081N	10200		
3XX092N	10200		
3XX103N	10200		
3XX114N	10200		
4M20B0V	06401		
4M20B0V	26802		
4M21B0V	06401		
4M21B0V	26802		
4M22B0V	06401		
4M23B0V	06401		



FO-113. TPU Memory Multiplexers Logic Diagram (Sheet 5 of 9)

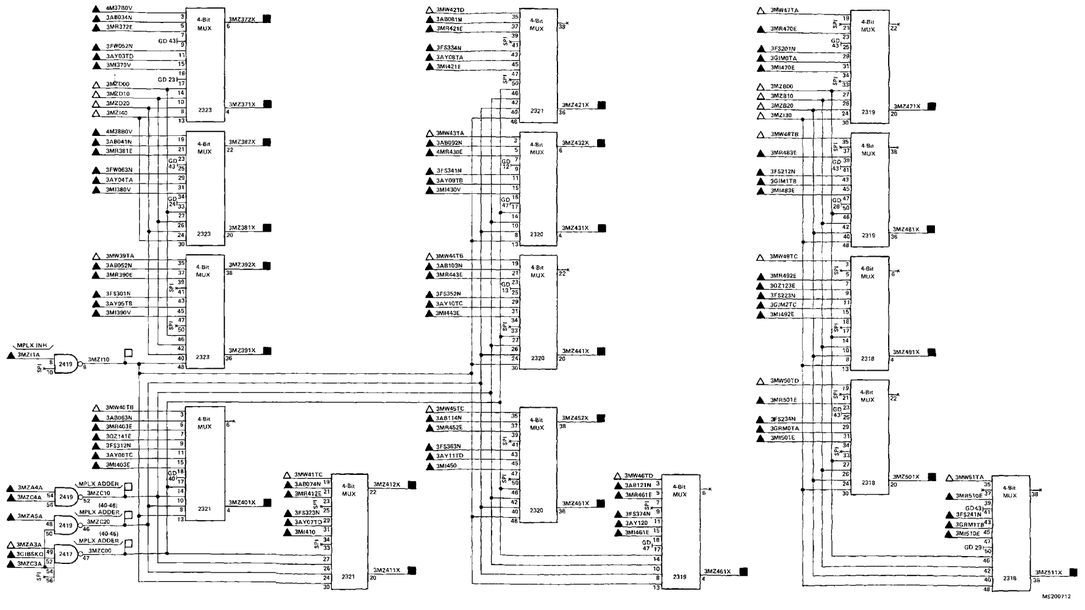
INPUT SIGNAL	INPUT SOURCE FO-SH	INPUT SIGNAL	OUTPUT SOURCE FO-SH	OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
3AB001N	10800	3MI350V	31401	3MZ241X	11400, 11500, 11600
3AB012N	10800	3MI360V	11600	3MZ251X	11400, 11500, 11600
3AB023N	10800	3MI360V	29502	3MZ261X	11400, 11500, 11600
3AY00TA	07900	3MI360V	31401	3MZ271X	11400, 11500, 11600
3AY01TB	07900	3MR243E	11500	3MZ281X	11400, 11500, 11600
3AY02TC	07900	3MR252E	11500	3MZ291X	11400, 11500, 11600
3DR052N	08800	3MR261E	11500	3MZ301X	11400, 11500, 11600
3DR063N	08800	3MR270E	11500	3MZ311X	11400, 11500, 11600
3DR074N	08800	3MR283E	11500	3MZ321X	11400, 11500
3DR081N	08800	3MR292E	11500	3MZ322X	11600
3DR092N	08800	3MR301E	11500	3MZ331X	11400, 11500
3DR103N	08800	3MR323E	11500	3MZ332X	11600
3DR114N	08800	3MR332E	11500	3MZ341X	11400, 11500
3DX10B1	09200	3MR341E	11500	3MZ342X	11600
3DX10B1	29601	3MR350E	11500	3MZ351X	11400, 11500
3FMJ10	10500	3MR363E	11500	3MZ352X	11600
3FMJ50	10500	3MZA4A	09300	3MZ361X	11400, 11500
3FMR10	10500	3MZA5A	09300	3MZ362X	11600
3FMVD0	10500	3MZA6A	09300		
3FW001N	10300	3MZC3A	09300		
3FW012N	10300	3MZC4A	09300		
3FW023N	10300	3MZI1A	09300		
3FW034N	10300	3MZI3A	09300		
3FW041N	10300	3QZ283E	08501		
3GBS4A	10600	3QZ292E	08501		
3GIBSKQ	10600	3QZ301E	08501		
3IC212E	10100	3QZ310E	08501		
3MI243E	11600	3QZ323E	08501		
3MI252E	11600	3QZ332E	08501		
3MI261E	11600	3QZ341E	08501		
3MI270E	11600	3RC02KQ	11100		
3MI283E	11600	4M32B0V	06402		
3MI292E	11600	4M32B0V	26802		
3MI301E	11600	4M33B0V	06402		
3MI310E	11600	4M33B0V	26802		
3MI320	11600	4M34B0V	06402		
3MI320	29501	4M34B0V	26802		
3MI330V	31401	4M35B0V	06402		
3MI330V	11600	4M35B0V	26802		
3MI330V	29501	4M36B0V	06402		
3MI340V	31401	4M36B0V	26802		
3MI340V	11600				
3MI340V	29502				
3MI350V	31401				
3MI350V	11600				
3MI360V	29502				



FO-113. TPU Memory Multiplexers Logic Diagram (Sheet 6 of 9)

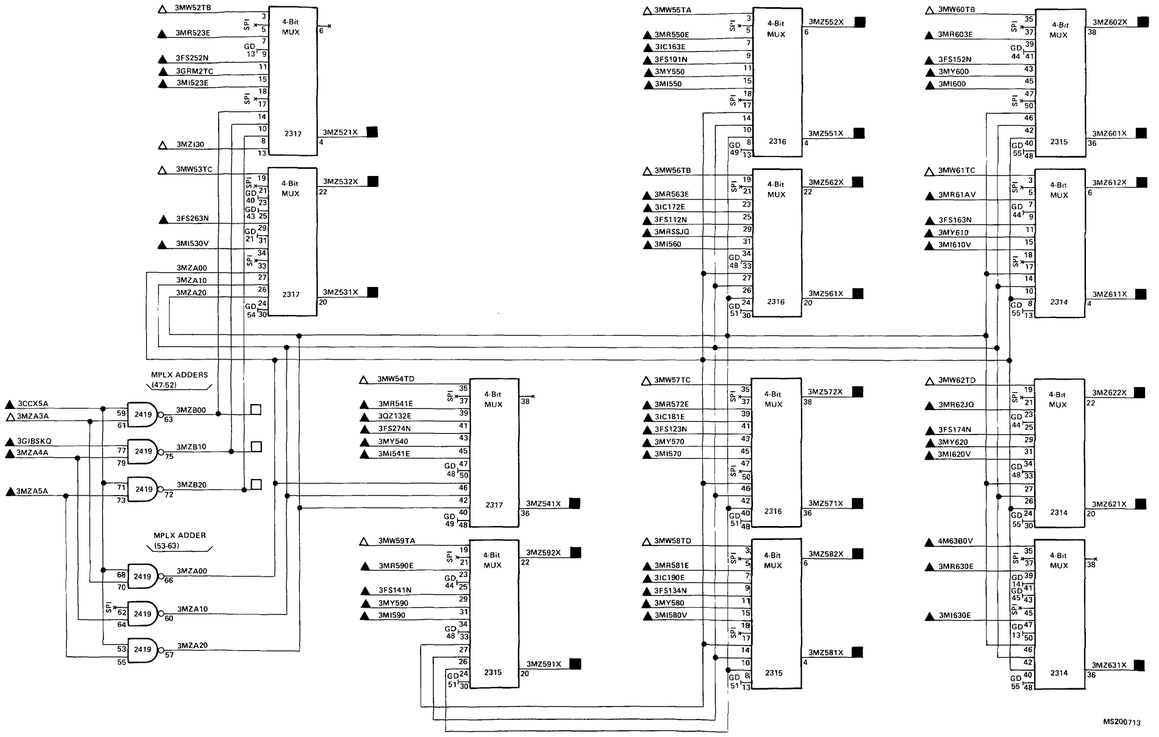
INPUT SIGNAL	INPUT SOURCE FO-SH	INPUT SIGNAL	INPUT SOURCE FO-SH	OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
3ICC7A	11302	3M2412X	11307	3M1000	10000, 10300, 10500, 11303, 11304
3M1L2A	11302	3M2421X	11307		
3M1L4A	11302	3M2432X	11307	3M1003E	12500
3M1L6A	11302	3M2441X	11307	3M1012E	10000, 10300, 10500, 11303, 11304
3M1L60	09500	3M2452X	11307		
3MZ002X	11303	3M2461X	11307	3M1020V	10000, 10300, 10500, 11303, 11304
3MZ011X	11303	3M2471X	11307		
3MZ022X	11303	3M2481X	11307	3M1030E	10000, 10300, 10500, 11303, 11304
3MZ031X	11303	3M2491X	11307		
3MZ042X	11303	3M2501X	11307	3M1040	10000, 10300, 10500, 11303, 11304
3MZ051X	11303	3M2511X	11307		
3MZ062X	11303	3M2521X	11308	3M1052E	10000, 10300, 10500, 11303, 11304
3MZ071X	11303	3M2532X	11308		
3MZ082X	11303	3M2541X	11308	3M1060V	10000, 10300, 10500, 11303, 11304
3MZ091X	11303	3M2552X	11308		
3MZ102X	11304	3M2562X	11308	3M1070E	10000, 10300, 10500, 11303, 11304
3MZ111X	11304	3M2572X	11308		
3MZ121X	11304	3M2582X	11308	3M1080	10000, 10300, 10500, 11303, 11305
3MZ131X	11304	3M2592X	11308		
3MZ141X	11304	3M2602X	11308	3M1092E	10000, 10300, 10500, 11303, 11305
3MZ151X	11304	3M2612X	11308		
3MZ161X	11304	3M2622X	11308	3M1100V	10000, 10300, 10500, 11304, 11305
3MZ171X	11304	3M2631X	11308		
3MZ181X	11304	6MCB1A	05600	3M1110E	10000, 10300, 10500, 11304, 11305
3MZ191X	11304	6MCB1A	27201		
3MZ201X	11305	6MGB1A	05600	3M1123E	10000, 11303, 11304
3MZ211X	11305	6MGB1A	27201	3M1132E	10000, 11303, 11304
3MZ221X	11305	8R1XSB	00300	3M1141E	10000, 11303, 11304
3MZ231X	11305	8R1XSB	14900	3M1150E	10000, 11303, 11304
3MZ241X	11306	8R1XSB	26802	3M1163E	10000, 11303, 11304
3MZ251X	11306	8R1XSB	26803	3M1172E	10000, 11303, 11304
3MZ261X	11306	8R1XSB	27501	3M1181E	10000, 10400, 10500, 11303, 11304
3MZ271X	11306				
3MZ281X	11306			3M1190E	10000, 10400, 10500, 11303, 11304
3MZ291X	11306				
3MZ301X	11306			3M1203E	10000, 10400, 10500, 11303, 11305
3MZ311X	11306				
3MZ322X	11306			3M1212E	10000, 10400, 10500, 11303, 11305
3MZ332X	11306				
3MZ342X	11306			3M1221E	10000, 10400, 10500, 11304, 11305
3MZ352X	11306				
3MZ362X	11306			3M1230E	10000, 10400, 10500, 11304, 11305
3MZ372X	11307				
3MZ382X	11307			3M1243E	11306
3MZ392X	11307			3M1252E	10500, 11305, 11306
3MZ401X	11307			3M1261E	11305, 11306
				3M1270E	11305, 11306
				3M1283E	11306
				3M1292E	11306
				3M1301E	11306
				3M1310E	09100, 11302, 11306, 11309, 11700

OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
3M1320	09200, 10300, 11306, 26802
3M1323E	09200, 12500, 26802, 31402
3M1330V	09200, 10300, 11306, 26802
3M1332E	09200, 26802, 31402
3M1340V	09200, 10300, 11306, 26802
3M1341E	09200, 26802, 31402
3M1350E	09200, 26802, 31402
3M1350V	09200, 10300, 11306, 26802
3M1360V	09200, 10300, 11306, 26802
3M1363E	09200, 26802, 31402
3M1370V	09200, 10300, 11307, 26802
3M1372E	09200, 26802, 31402
3M1380V	07600, 10300, 11307
3M1381E	07600
3M1390E	09900, 10300, 11302, 11307
3M1403E	10000, 10300, 11302, 11307
3M1410	09100, 09900, 10000, 10300, 11307, 11700
3M1412E	10400
3M1421E	09900, 10300, 11302, 11307, 11700
3M1430E	09900
3M1430V	09900, 10300, 11302, 11307
3M1443E	10000, 10300, 11302, 11307
3M1450	09100, 09900, 10000, 10300, 11307, 11700
3M1452E	10400
3M1461E	09900, 10300, 11302, 11307, 11700
3M1470E	10100, 10300, 11307
3M1483E	10100, 10300, 11307
3M1492E	10100, 10300, 11307
3M1501E	10100, 10300, 11307
3M1510E	10300, 11307
3M1523E	10100, 10300, 11301, 11308
3M1530V	10100, 10300, 11301, 11308
3M1532E	10100
3M1541E	10300, 11301, 11304, 11308
3M1550	09900, 10300, 10400, 11302, 11308, 11309
3M1560	09900, 10300, 10400, 11302, 11308, 11309
3M1570	09900, 10300, 10400, 11302, 11308, 11309
3M1580V	09900, 10300, 10400, 11302, 11308, 11309
3M1590	09900, 10300, 10400, 11302, 11308, 11309
3M1600	09900, 10300, 10400, 11302, 11308, 11309
3M1610V	09900, 10300, 10400, 11302, 11308, 11309
3M1620V	09900, 10300, 10400, 11302, 11308, 11309
3M1630E	11308



FO-113. TPU Memory Multiplexers Logic Diagram (Sheet 7 of 9)

INPUT		INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3AB034N	10800	3MI403E	11600	3MZ371X	11400, 11500
3AB041N	10800	3MI410	11600	3MZ372X	11600
3AB052N	10800	3MI421E	11600	3MZ381X	11400, 11500
3AB063N	10800	3MI430V	11600	3MZ382X	11600
3AB074N	10800	3MI443E	11600	3MZ391X	11400, 11500
3AB081N	10800	3MI450	11600	3MZ392X	11600
3AB092N	10800	3MI461E	11600	3MZ401X	11400, 11500, 11600
3AB103N	10800	3MI470E	11600	3MZ411X	11400, 11500
3AB114N	10800	3MI483E	11600	3MZ412X	11600
3AB121N	10800	3MI492E	11600	3MZ421X	11400, 11500, 11600
3AY03TD	07900	3MI501E	11600	3MZ431X	11400, 11500
3AY04TA	07900	3MI510E	11600	3MZ432X	11600
3AY05TB	07900	3MR372E	11500	3MZ441X	11400, 11500, 11600
3AY06TC	07900	3MR381E	11500	3MZ451X	11400, 11500
3AY07TD	07900	3MR390E	11500	3MZ452X	11600
3AY08TA	07900	3MR403E	11500	3MZ461X	11400, 11500, 11600
3AY09TB	07900	3MR412E	11500	3MZ471X	11400, 11500, 11600
3AY10TC	07900	3MR421E	11500	3MZ481X	11400, 11500, 11600
3AY11TD	07900	3MR430E	11500	3MZ491X	11400, 11500, 11600
3AY120	07900	3MR443E	11500	3MZ501X	11400, 11500, 11600
3FS201N	10300	3MR452E	11500	3MZ511X	11400, 11500, 11600
3FS212N	10300	3MR461E	11500		
3FS223N	10300	3MR470E	11500		
3FS234N	10300	3MR483E	11500		
3FS241N	10300	3MR492E	11500		
3FS301N	10300	3MR501E	11500		
3FS312N	10300	3MR510E	11500		
3FS323N	10300	3MZA4A	09300		
3FS334N	10300	3MZA5A	09300		
3FS341N	10300	3MZC3A	09300		
3FS352N	10300	3MZC4A	09300		
3FS363N	10300	3MZI1A	09300		
3FS374N	10300	3QZ123E	08503		
3FW052N	10300	3QZ141E	08503		
3FW063N	10300	4M37B0V	06402		
3GIBSKQ	10600	4M37B0V	26802		
3GIM0TA	09800	4M38B0V	06402		
3GIM1TB	09800				
3GIM2TC	09800				
3GRM0TA	09800				
3GRM1TB	09800				
3MI370V	11600				
3MI370V	29502				
3MI370V	31401				
3MI380V	11600				
3MI390V	11600				

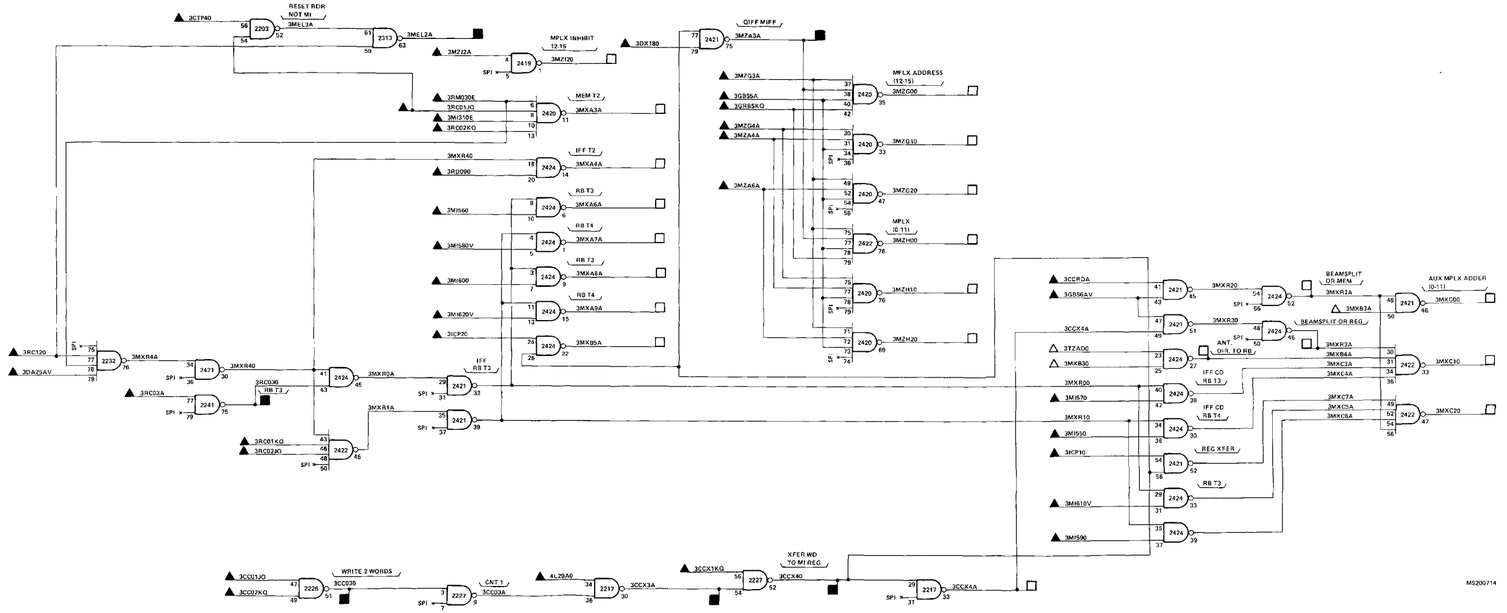


FO-113. TPU Memory Multiplexers Logic Diagram (Sheet 8 of 9)

INPUT	
SIGNAL	SOURCE FO-SH
3CCX5A	09500
3FS101N	10300
3FS112N	10300
3FS123N	10300
3FS134N	10300
3FS141N	10300
3FS152N	10300
3FS163N	10300
3FS174N	10300
3FS252N	10300
3FS263N	10300
3FS274N	10300
3G185KQ	10600
3GRM2TC	09800
3IC163E	09900
3IC172E	09900
3IC181E	09900
3IC190E	09900
3MI523E	11600
3MI530V	11600
3MI541E	11600
3MI550	11600
3MI560	11600
3MI570	11600
3MI580V	11600
3MI590	11600
3MI600	11600
3MI610V	11600
3MI620V	11600
3MI630E	11600
3MRSSJQ	11700
3MR523E	11500
3MR541E	11500
3MR550E	11500
3MR563E	11500
3MR572E	11500
3MR581E	11500
3MR590E	11500
3MR603E	11500
3MR61AV	11700
3MR62JQ	11700
3MR630E	11500
3MY540	09300
3MY550	09300
3MY570	09300
3MY580	09300

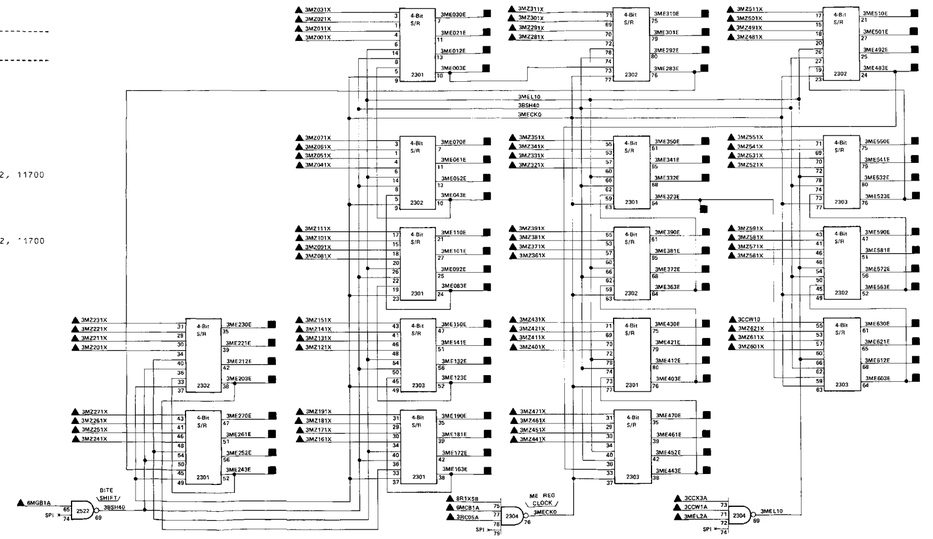
INPUT	
SIGNAL	SOURCE FO-SH
3MY590	09300
3MY600	09300
3MY610	09300
3MY620	08502
3MZA4A	09300
3MZA5A	09300
3QZ132E	08503
4M63B0V	06402

OUTPUT	
SIGNAL	DESTINATION FO-SH
3MZ521X	11400, 11500, 11600
3MZ531X	11400
3MZ532X	11600
3MZ541X	11400, 11500, 11600
3MZ551X	11400, 11500
3MZ552X	11600
3MZ561X	11400, 11500, 11700
3MZ562X	07701, 11600
3MZ571X	11400, 11500
3MZ572X	11600
3MZ581X	11400, 11500
3MZ582X	11600
3MZ591X	11400, 11500
3MZ592X	11600
3MZ601X	11400, 11500
3MZ602X	11600
3MZ611X	11400, 11500, 11700
3MZ612X	07701, 11600
3MZ621X	11400, 11500, 11700
3MZ622X	07701, 11600
3MZ631X	07701, 11500, 11600, 11700



FO-113. TPU Memory Multiplexers Logic Diagram (Sheet 9 of 9)

INPUT		INPUT		OUTPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH	SIGNAL	DESTINATION FO-SH
3CCW1A	09500	3M2421X	11307	3ME003E	06101, 06800, 12500	3ME461E	06104
3CCW1D	09500	3M2431X	11307	3ME012E	06101, 06800	3ME470E	06104
3CCX3A	11309	3M2441X	11307	3ME021E	06101, 06800	3ME483E	06104
3MEL2A	11309	3M2451X	11307	3ME030E	06101, 06800	3ME492E	06104
3MZ001X	11303	3M2461X	11307	3ME043E	06101, 06800	3ME501E	06104
3MZ011X	11303	3M2471X	11307	3ME052E	06101, 06800	3ME510E	06104
3MZ021X	11303	3M2481X	11307	3ME061E	06101, 06800	3ME523E	06104
3MZ031X	11303	3M2491X	11307	3ME070E	06101, 06800	3ME532E	06104
3MZ041X	11303	3M2501X	11307	3ME085E	06101, 06800	3ME541E	06104
3MZ051X	11303	3M2511X	11307	3ME092E	06101, 06800	3ME550E	06104
3MZ061X	11303	3M2521X	11308	3ME101E	06101, 06800	3ME563E	06105 08502, 11700
3MZ071X	11303	3M2531X	11308	3ME110E	06101, 06800	3ME572E	06105
3MZ081X	11303	3M2541X	11308	3ME123E	06101, 06800	3ME581E	06105
3MZ091X	11303	3M2551X	11308	3ME132E	06101, 06800	3ME590E	06105
3MZ101X	11304	3M2561X	11308	3ME141E	06102, 06800	3ME603E	06105
3MZ111X	11304	3M2571X	11308	3ME150E	06102, 06800	3ME612E	06105
3MZ121X	11304	3M2581X	11308	3ME163E	06102, 06800	3ME621E	06105
3MZ131X	11304	3M2591X	11308	3ME172E	06102, 06800	3ME630E	06105, 08502, 11700
3MZ141X	11304	3M2601X	11308	3ME181E	06102, 06800		
3MZ151X	11304	3M2611X	11308	3ME190E	06102, 06800		
3MZ161X	11304	3M2621X	11308	3ME203E	06102, 06800		
3MZ171X	11304	3RC05A	11100	3ME212E	06102, 06800		
3MZ181X	11304	6MC81A	05600	3ME221E	06102, 06800		
3MZ191X	11304	6MGB1A	27201	3ME230E	06102, 06800		
3MZ201X	11305	6MGB1A	05600	3ME243E	06102, 06800		
3MZ211X	11305	6MGB1A	27201	3ME252E	06102, 06800		
3MZ221X	11305	8R1X5B	00300	3ME261E	06102, 06800		
3MZ231X	11305	8R1X5B	14900	3ME270E	06102, 06800		
3MZ241X	11306	8R1X5B	26802	3ME283E	06103, 06800		
3MZ251X	11306	8R1X5B	26803	3ME292E	06103, 06800		
3MZ261X	11306	8R1X5B	27501	3ME301E	06103, 06800		
3MZ271X	11306			3ME310E	06103		
3MZ281X	11306			3ME323E	06103, 12500		
3MZ291X	11306			3ME332E	06103		
3MZ301X	11306			3ME341E	06103		
3MZ311X	11306			3ME350E	06103		
3MZ321X	11306			3ME363E	06103		
3MZ331X	11306			3ME372E	06103		
3MZ341X	11306			3ME381E	06103		
3MZ351X	11306			3ME390E	06103		
3MZ361X	11306			3ME403E	06103		
3MZ371X	11307			3ME412E	06103		
3MZ381X	11307			3ME421E	06104		
3MZ391X	11307			3ME430E	06104		
3MZ401X	11307			3ME443E	06104		
3MZ411X	11307			3ME452E	06104		



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

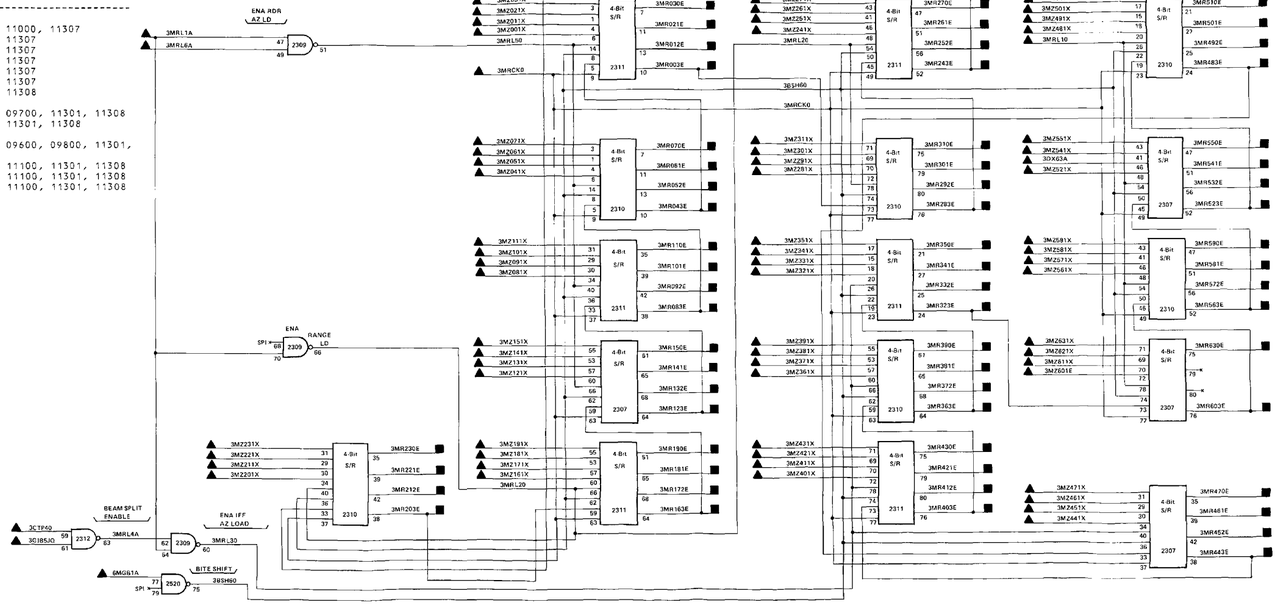
    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

FO-114. TPU Data Exchange Register Logic Diagram

INPUT SIGNAL	INPUT SOURCE FO-SH	INPUT SIGNAL	INPUT SOURCE FO-SH	OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
3CTP40	07600	3MZ391X	11307	3MR003E	09400, 09700, 12500
3DX63A	09700	3MZ401X	11307	3MR012E	10600, 11100, 11303
3G1BSJ0	10600	3MZ411X	11307	3MR021E	10700, 11000, 11303
3MRCK0	11700	3MZ421X	11307	3MR030E	10700, 11000, 11303
3MRL1A	11700	3MZ431X	11307	3MR043E	10700, 11000, 11303
3MRL10	09500	3MZ441X	11307	3MR052E	10700, 11000, 11303
3MRL6A	10600	3MZ451X	11307	3MR061E	10700, 11000, 11303
3MZ001X	11303	3MZ461X	11307	3MR070E	10700, 11000, 11303
3MZ011X	11303	3MZ471X	11307	3MR083E	10700, 11000, 11303
3MZ021X	11303	3MZ481X	11307	3MR092E	10700, 11000, 11303
3MZ031X	11303	3MZ491X	11307	3MR101E	10700, 11000, 11304
3MZ041X	11303	3MZ501X	11307	3MR110E	10700, 11000, 11304
3MZ051X	11303	3MZ511X	11307	3MR123E	10700, 11000, 11304
3MZ061X	11303	3MZ521X	11308	3MR132E	10700, 11000, 11304
3MZ071X	11303	3MZ541X	11308	3MR141E	10700, 11000, 11304
3MZ081X	11303	3MZ551X	11308	3MR150E	09400, 09700
3MZ091X	11303	3MZ561X	11308	3MR163E	08600, 11304
3MZ101X	11304	3MZ571X	11308	3MR172E	08600, 11304
3MZ111X	11304	3MZ581X	11308	3MR181E	08600, 11304
3MZ121X	11304	3MZ591X	11308	3MR190E	08600, 08800, 08900, 11304
3MZ131X	11304	3MZ601X	11308	3MR203E	08600, 08800, 08900, 11305
3MZ141X	11304	3MZ611X	11308	3MR212E	08600, 08800, 08900, 11305
3MZ151X	11304	3MZ621X	11308	3MR221E	08600, 08800, 08900, 11305
3MZ161X	11304	3MZ631X	11308	3MR230E	08600, 08800, 08900, 11305
3MZ171X	11304	6MGB1A	05600	3MR243E	08600, 08800, 08900, 11306
3MZ181X	11304	6MGB1A	27201	3MR252E	08600, 08800, 08900, 11306
3MZ191X	11304			3MR261E	08600, 08800, 08900, 11306
3MZ201X	11305			3MR270E	08600, 08800, 08900, 11306
3MZ211X	11305			3MR283E	08600, 08800, 08900, 11306
3MZ221X	11305			3MR292E	08600, 08800, 08900, 11306
3MZ231X	11305			3MR301E	08600, 08800, 08900, 11306
3MZ241X	11306			3MR310E	09400
3MZ251X	11306			3MR323E	11306, 12500
3MZ261X	11306			3MR332E	09100, 10600, 11100, 11306
3MZ271X	11306			3MR341E	10700, 11000, 11306
3MZ281X	11306			3MR350E	10700, 11000, 11306
3MZ291X	11306			3MR363E	10700, 11000, 11306
3MZ301X	11306			3MR372E	10700, 11000, 11307
3MZ311X	11306			3MR381E	10700, 11000, 11307
3MZ321X	11306			3MR390E	10700, 11000, 11307
3MZ331X	11306			3MR403E	10700, 11000, 11307
3MZ341X	11306			3MR412E	10700, 11000, 11307
3MZ351X	11306			3MR421E	10700, 11000, 11307
3MZ361X	11306			3MR430E	10700, 11000, 11307
3MZ371X	11307			3MR443E	10700, 11000, 11307
3MZ381X	11307			3MR452E	10700, 11000, 11307

INPUT SIGNAL	INPUT SOURCE FO-SH	OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
3MR461E	10700, 11000, 11307		
3MR470E	09800, 11307		
3MR483E	09800, 11307		
3MR492E	09800, 11307		
3MR501E	09800, 11307		
3MR510E	09800, 11307		
3MR523E	09800, 11308		
3MR532E	09700		
3MR541E	09300, 09700, 11301, 11308		
3MR550E	09300, 11301, 11308		
3MR563E	11308		
3MR572E	09300, 09600, 09800, 11301,		
	11308		
3MR581E	09300, 11100, 11301, 11308		
3MR590E	09300, 11100, 11301, 11308		
3MR603E	09300, 11100, 11301, 11308		
3MR630E	11308		



FO-115. TPU Memory Register Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

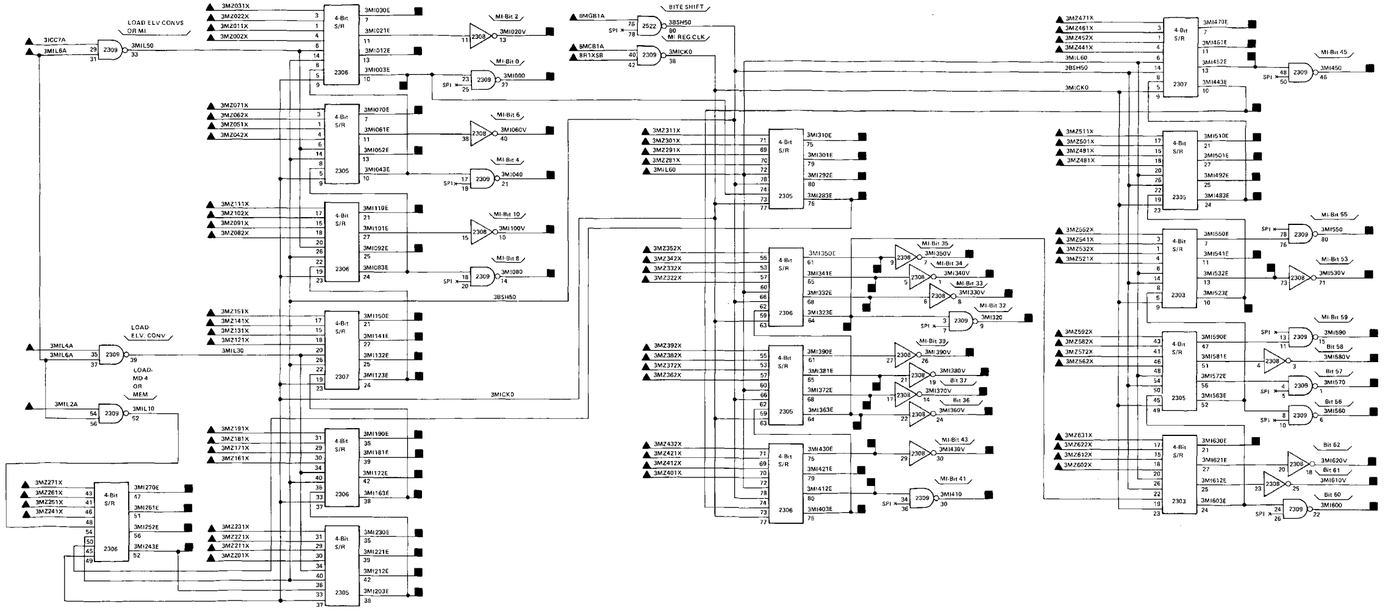
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

MS2307/16

INPUT SIGNAL	INPUT SOURCE FO-SH
3CCRDA	09500
3CCX1KQ	09500
3CCO1JQ	09500
3CCO2KQ	09500
3CTP4D	07600
3DA25AV	09100
3DX18D	09300
3GBS5A	10600
3GBS6AV	10600
3GRBSKQ	10600
3ICP1D	09900
3ICP2D	09900
3MI310E	11600
3MI550	11600
3MI560	11600
3MI570	11600
3MI580V	11600
3MI590	11600
3MI600	11600
3MI610V	11600
3MI620V	11600
3MZA4A	09300
3MZA6A	09300
3MZG3A	09300
3MZG4A	09300
3MZ12A	09300
3RC01JQ	11100
3RC01KQ	11100
3RC02JQ	11100
3RC02KQ	11100
3RC03A	11100
3RC12D	11100
3RD09D	11100
3RM030E	11100
4L20AD	06001

OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
3CCX3A	11400
3CCX4D	09300, 09400, 12500
3CC03D	09500
3MEL2A	11400
3RC03D	11700



FO-116. TPU Memory Data Register Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

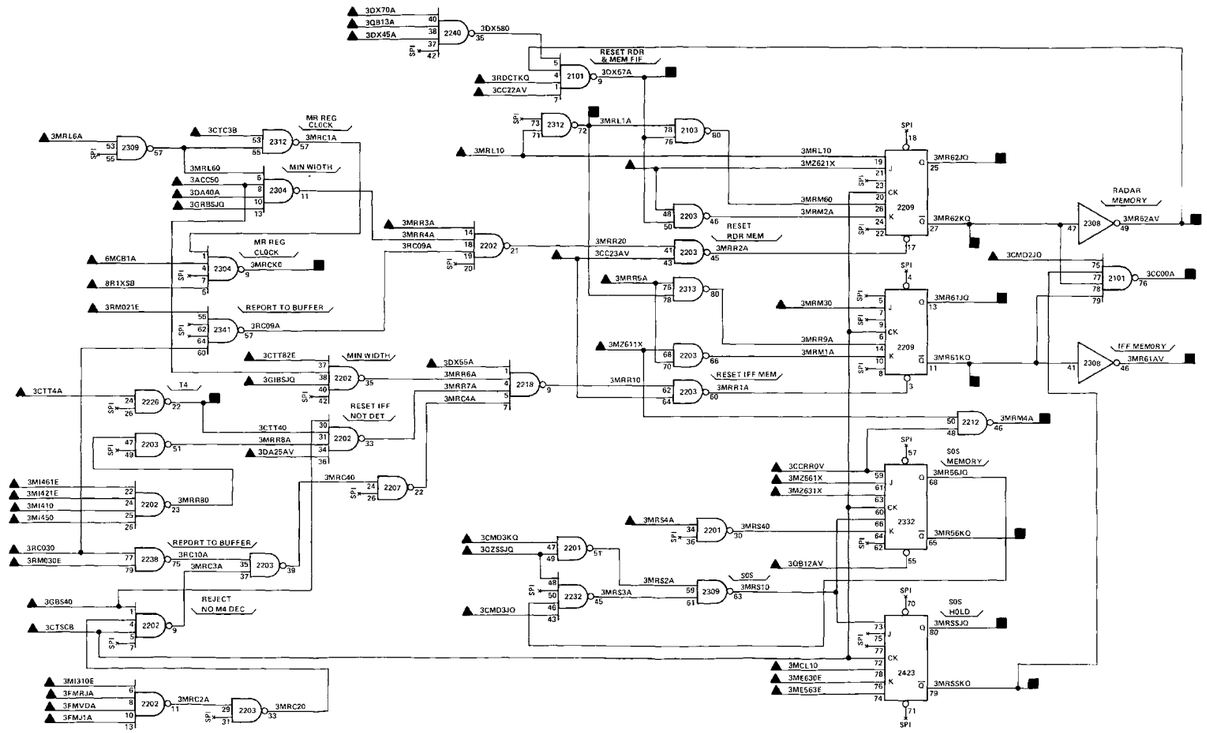
    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT SIGNAL	SOURCE FO-SH
3ACC50	10900
3CCRR0V	07701
3CC22AV	09500
3CC23AV	09500
3CMD2JQ	08200
3CMD3JQ	08200
3CMD3KQ	08200
3CTC3B	07600
3CTSCB	07600
3CTT4A	07600
3CTT82E	07600
3DA25AV	09100
3DA40A	09600
3DX45A	08900
3DX55A	08900
3DX70A	09700
3FMJ1A	10500
3FMRJA	10500
3FMVDA	10500
3GBS40	10600
3GIBSJQ	10600
3GRBSJQ	10600
3MCL10	11900
3ME563E	11400
3ME630E	11400
3MI310E	11600
3MI410	11600
3MI421E	11600
3MI450	11600
3MI461E	11600
3MRL10	09500
3MRL6A	10600
3MRM30	09300
3MRR3A	09600
3MRR5A	09600
3MRS4A	09600
3MZ561X	11308
3MZ611X	11308
3MZ621X	11308
3MZ631X	11308
3QB12AV	07701
3QB13A	07701
3QZSSJQ	08502
3RC030	11309
3RDCTKQ	11100
3RM021E	11100

INPUT SIGNAL	SOURCE FO-SH
3RM030E	11100
6MCB1A	05600
6MCB1A	27201
8R1XSB	00300
8R1XSB	14900
8R1XSB	26802
8R1XSB	26803
8R1XSB	27501

OUTPUT SIGNAL	DESTINATION FO-SH
3CC00A	09500
3CTT40	10600
3DX57A	09000
3MRCK0	11500
3MRL1A	11500
3MRM4A	09300
3MRSSJQ	11308
3MRSSKQ	08900, 09600
3MR56KQ	08200, 08900, 09600
3MR61AV	09100, 11100, 11308
3MR61JQ	08900, 09100, 09300, 09600, 09700, 11100
3MR61KQ	08900, 09000, 09100, 09600, 11100
3MR62AV	09400
3MR62JQ	09100, 09300, 09800, 10600, 11100, 11308
3MR62KQ	08900, 09000, 09100, 09400, 09600, 11100



FO-117. TPU Memory Register Extension Bits and Cancel Target Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND AN FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

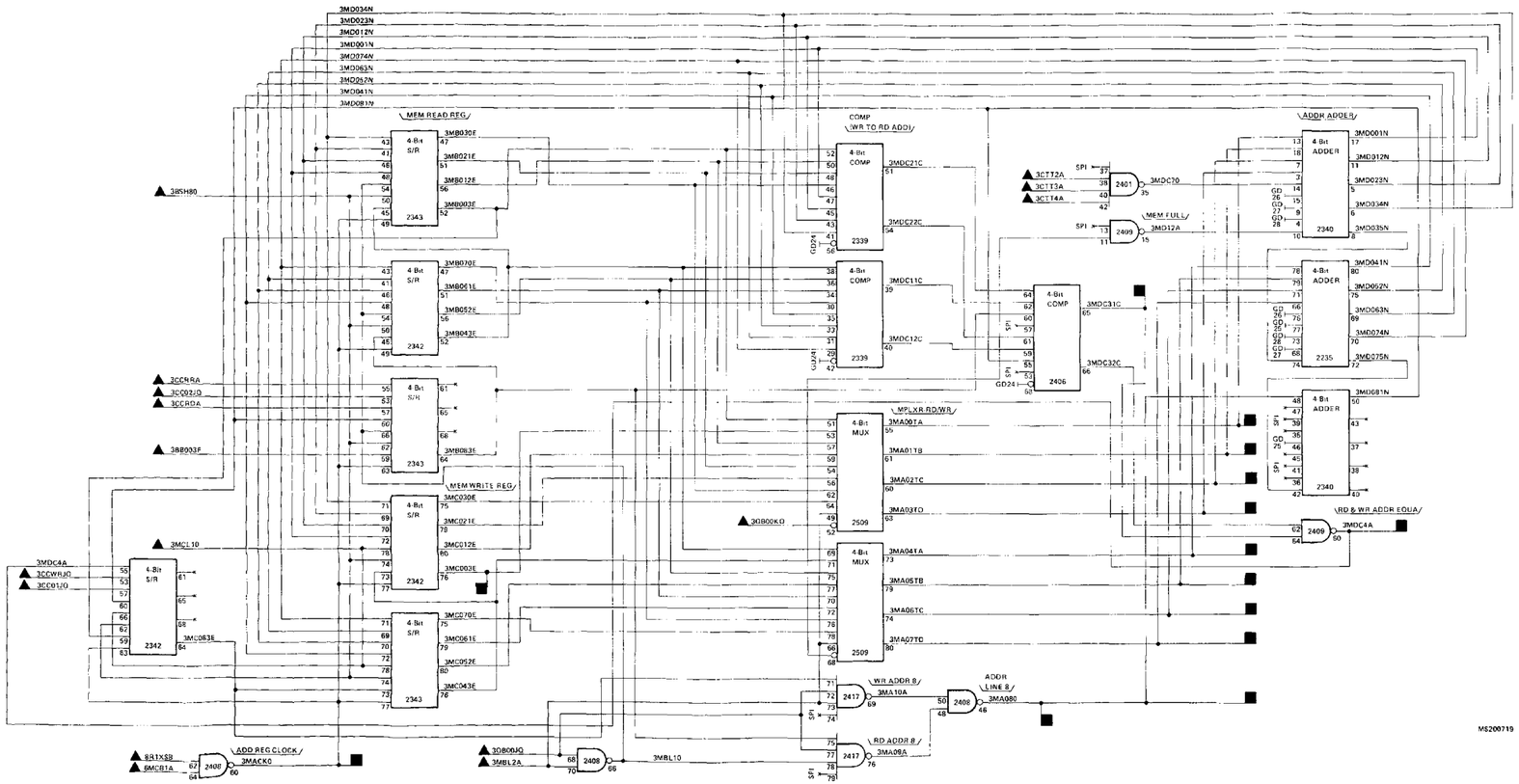
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3BB003E	12500	3MACK0	12500
3BSH80	12500	3MA00TA	06300
3CCRDA	09500	3MA01TB	06300
3CCRRA	09500	3MA02TC	06300
3CCWRJQ	09500	3MA03TD	06300
3CC01JQ	09500	3MA04TA	06300
3CC02JQ	09500	3MA05TB	06300
3CTT2A	07600	3MA06TC	06300
3CTT3A	07600	3MA07TD	06300
3CTT4A	07600	3MA080	06300
3MBL2A	09500	3MC003E	12500
3MCL10	11900	3MDC31C	11900
3QB00JQ	07701	3MDC4A	11900
3QB00KQ	07701		
6MCB1A	05600		
6MCB1A	27201		
8R1XSB	00300		
8R1XSB	14900		
8R1XSB	26802		
8R1XSB	26803		
8R1XSB	27501		



FO-118. TPU Memory Address Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

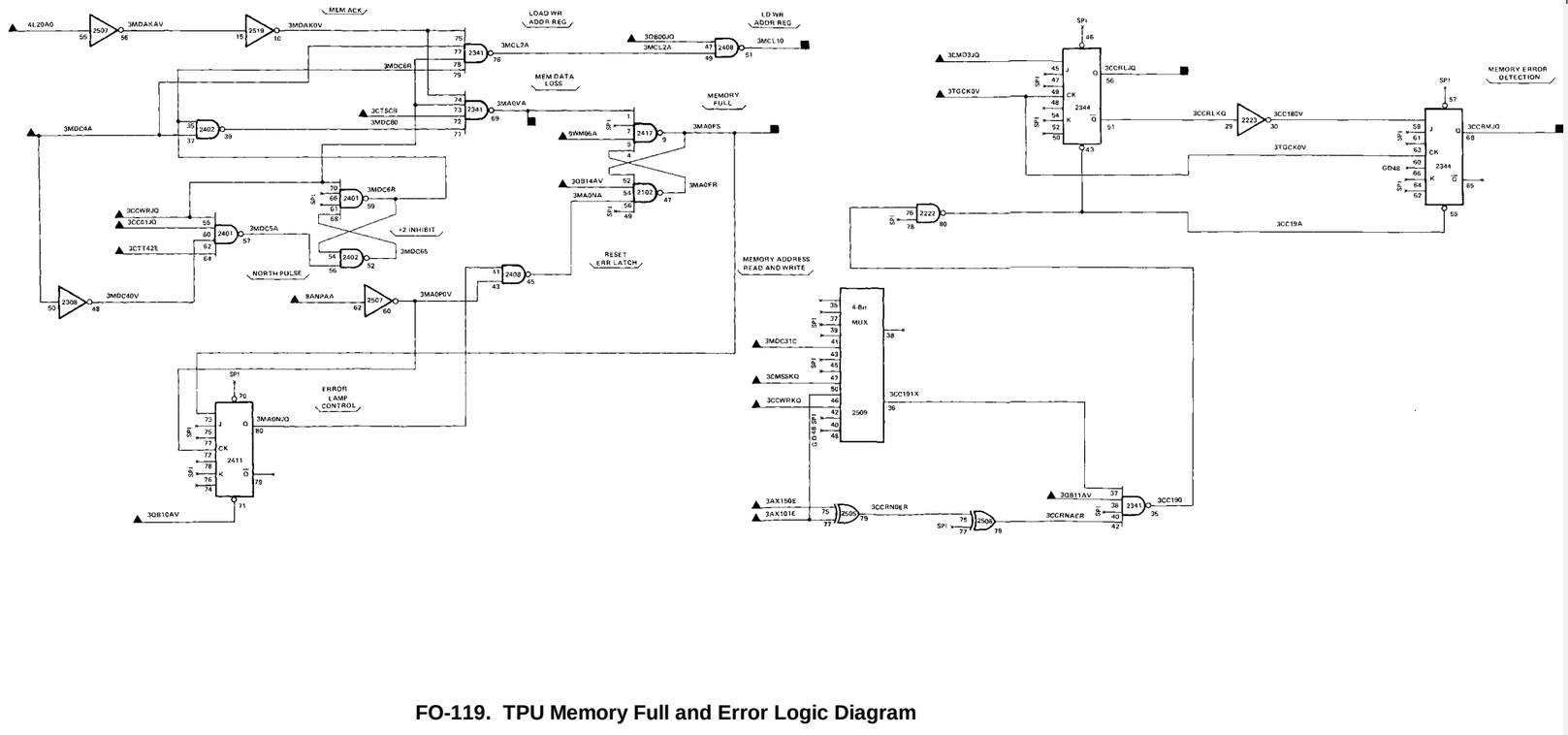
    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT	
SIGNAL	SOURCE FO-SH
3AX101E	07900
3AX150E	07900
3CCWRJQ	09500
3CCWRKQ	09500
3CCD1JQ	09500
3CMD3JQ	08200
3CMSSKQ	08200
3CTSCB	07600
3CTT42E	07600
3MDC31C	11800
3MDC4A	11800
3QB00JQ	07701
3QB10AV	07701
3QB11AV	07701
3QB14AV	07701
3TGCK0V	12200
4L20AD	06001
8ANPAA	17200
8ANPAA	26802
8ANPAA	28500
9WM06A	19903
9WM06A	26802
9WM06A	27302

OUTPUT	
SIGNAL	DESTINATION FO-SH
3CCRLJQ	07701
3CCRMJQ	07701
3MA0FS	13101, 26802, 31301
3MA0VA	07701
3MCL10	11700, 11800



FO-119. TPU Memory Full and Error Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGUI
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGUR
   □ OUTPUT TO BOTH SAME AND FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

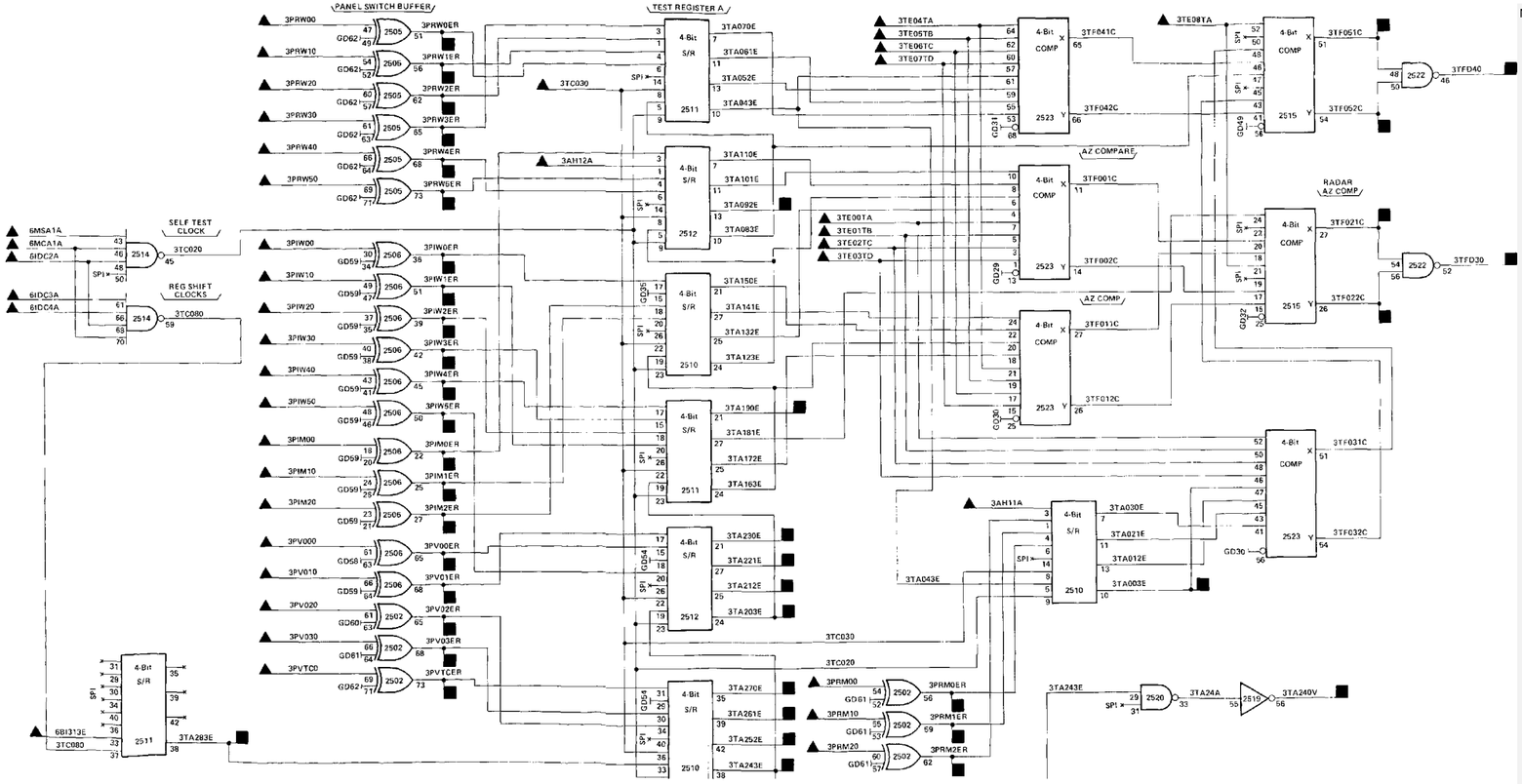
    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT SIGNAL	INPUT SOURCE FO-SH
3AH11A	10900
3AH12A	10900
3MYA1A	09300
3PIM00	26802
3PIM00	32000
3PIM10	26802
3PIM10	32000
3PIM20	26802
3PIM20	32000
3PIW00	26802
3PIW00	32000
3PIW10	26802
3PIW10	32000
3PIW20	26802
3PIW20	32000
3PIW30	26802
3PIW30	32000
3PIW40	26802
3PIW40	32000
3PIW50	26802
3PIW50	32000
3PRM00	26802
3PRM00	32000
3PRM10	26802
3PRM10	32000
3PRM20	32000
3PRW00	32000
3PRW10	32000
3PRW20	26802
3PRW20	32000
3PRW30	26802
3PRW30	32000
3PRW40	26802
3PRW40	32000
3PRW50	26802
3PRW50	32000
3PVTC0	26802
3PVTCG	26803
3PVTC0	31301
3PV000	26802
3PV000	26803
3PV000	31301
3PV010	26802
3PV010	26803
3PV010	31301
3PV020	26802

INPUT SIGNAL	INPUT SOURCE FO-SH
3PV020	26803
3PV020	31301
3PV030	26802
3PV030	26803
3PV030	31301
3TC030	12100
3TE00TA	07900
3TE01TB	07900
3TE02TC	07900
3TE03TD	07900
3TE04TA	07900
3TE05TB	07900
3TE06TC	07900
3TE07TD	07900
3TE08TA	07900
6BI313E	05300
6BI313E	26802
6IDC2A	05500
6IDC3A	05500
6IDC4A	05500
6MCA1A	05600
6MCA1A	26803
6MCA1A	27901
6MSA1A	05600
6MSA1A	26803
6MSA1A	27201

OUTPUT SIGNAL	DESTINATION FO-SH
3PIM0ER	08000, 09800
3PIM1ER	08000, 09800
3PIM2ER	08000, 09800
3PIW0ER	10900
3PIW1ER	10900
3PIW2ER	10900
3PIW3ER	10900
3PIW4ER	10900
3PIW5ER	10900
3PRM0ER	08000, 09800
3PRM1ER	08000, 09800
3PRM2ER	08000, 09800
3PRW0ER	10900
3PRW1ER	10900
3PRW2ER	10900
3PRW3ER	10900
3PRW4ER	10900
3PRW5ER	10900
3PVTCER	11200
3PV00ER	11200
3PV01ER	11200
3PV02ER	11200
3PV03ER	11200
3TA003E	12500
3TA092E	07600
3TA190E	08503
3TA203E	12400
3TA212E	12400
3TA221E	12400
3TA230E	12200, 12400
3TA240V	08502
3TA243E	08502
3TA252E	12200
3TA261E	08503
3TA270E	12200
3TA283E	12300
3TF030	12200
3TFD40	12200
3TF021C	12200
3TF022C	12200
3TF051C	12200
3TF052C	12200



FO-120. TPU Azimuth Compare Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER F
   △ INPUT FROM SAME FIGU
   ■ OUTPUT TO ANOTHER F
   □ OUTPUT TO BOTH SAME FIGURE
   ◪ OUTPUT TO SAME FIGUR

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

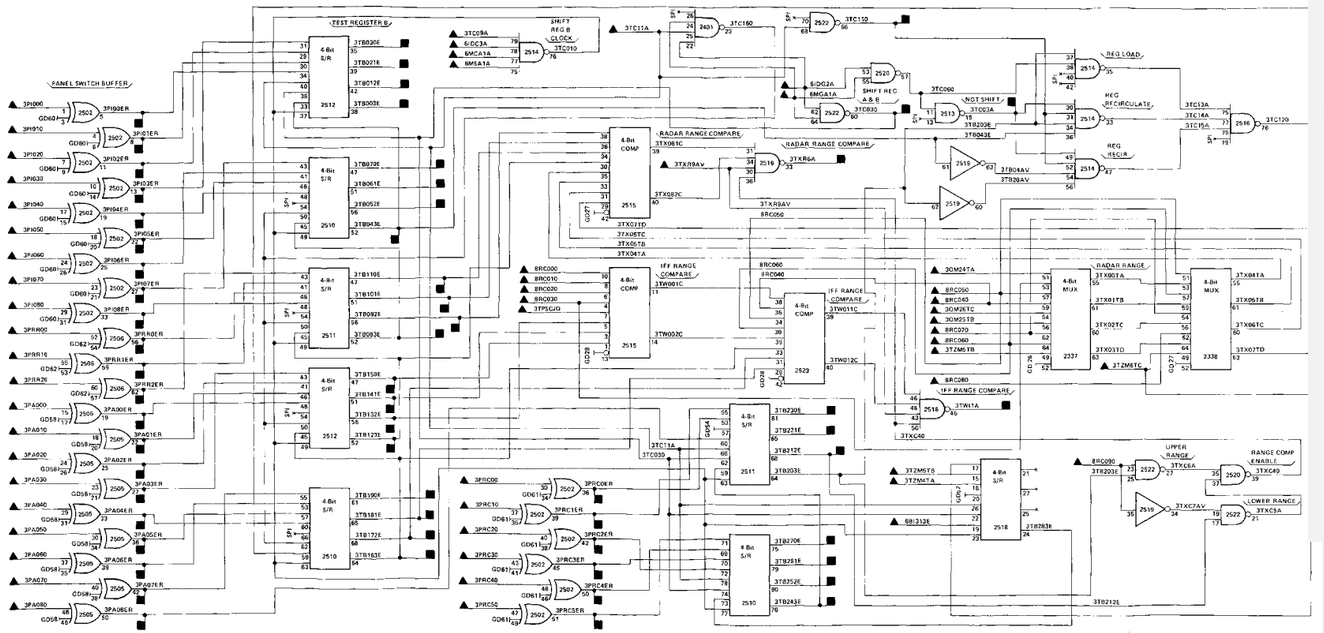
INPUT SIGNAL	INPUT SOURCE FO-SH
3PA000	26802
3PA000	32000
3PA010	26802
3PA010	32000
3PA020	26802
3PA020	32000
3PA030	26802
3PA030	32000
3PA040	26802
3PA040	32000
3PA050	26802
3PA050	32000
3PA060	26802
3PA060	32000
3PA070	26802
3PA070	32000
3PA080	26802
3PA080	32000
3P1000	26802
3P1000	32000
3P1010	26802
3P1010	32000
3P1020	26802
3P1020	32000
3P1030	26802
3P1030	32000
3P1040	26802
3P1040	32000
3P1050	26802
3P1050	32000
3P1060	26802
3P1060	32000
3P1070	26802
3P1070	32000
3P1080	26802
3P1080	32000
3PRC00	26802
3PRC00	32000
3PRC10	26802
3PRC10	32000
3PRC20	26802
3PRC20	32000
3PRC30	26802
3PRC30	32000
3PRC40	26802
3PRC40	32000

INPUT SIGNAL	INPUT SOURCE FO-SH
3PRC50	26802
3PRC50	32000
3PRR00	26802
3PRR00	32000
3PRR10	26802
3PRR10	32000
3PRR20	26802
3PRR20	32000
3QM24TA	08501
3QM25TB	08501
3QM261C	08501
3TC09A	12200
3TC11A	12300
3TC170	12300
3TPSCJG	12400
3TXR9AV	12400
3TZM47A	12300
3TZM57B	12300
3TZM61C	12300
6B1313E	05300
6B1313E	26802
6J0C3A	05500
6J0G2A	05500
6MCA1A	05600
6MCA1A	26803
6MCA1A	27201
6MGA1A	05600
6MGA1A	26803
6MGA1A	27201
6MSA1A	05600
6MSA1A	26803
6MSA1A	27201
8RC000	14900
8RC000	27501
8RC010	14900
8RC010	27501
8RC020	14900
8RC020	26803
8RC020	27501
8RC030	14900
8RC030	27501
8RC040	14900
8RC040	26803
8RC040	27501
8RC050	14900

INPUT SIGNAL	INPUT SOURCE FO-SH
8RC050	26803
8RC050	27501
8RC060	14900
8RC060	26803
8RC060	27501
8RC070	14900
8RC070	26803
8RC070	27501
8RC080	14900
8RC080	26803
8RC080	27501
8RC090	14900
8RC090	26803
8RC090	27501

OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
3P1000E	11000
3P1010E	11000
3P1020E	11000
3P1030E	11000
3P1040E	11000
3P1050E	11000
3P1060E	11000
3P1070E	11000
3P1080E	11000
3PA00ER	11000
3PA01ER	11000
3PA02ER	11000
3PA03ER	11000
3PA04ER	11000
3PA05ER	11000
3PA06ER	11000
3PA07ER	11000
3PA08ER	11000
3PI00ER	10900
3PI01ER	10900
3PI02ER	10900
3PI03ER	10900
3PI04ER	10900
3PI05ER	10900
3PI06ER	10900
3PI07ER	10900
3PI08ER	10900
3PRC0ER	08000
3PRC1ER	08000
3PRC2ER	08000
3PRC3ER	08000
3PRC4ER	08000
3PRC5ER	08000
3PRR0ER	08700
3PRR1ER	08700
3PRR2ER	08700
3TB003E	12400, 12500
3TB012E	08503, 12400
3TB021E	12400
3TB030E	12400
3TB043E	08501, 08503
3TB052E	08501, 08503
3TB061E	08501, 08503
3TB070E	08501, 08503
3TB083E	08501, 08503
3TB092E	08503
3TB101E	08501
3TB110E	08501
3TB123E	08501
3TB132E	08501
3TB141E	08501
3TB150E	08501
3TB163E	08501
3TB172E	08501
3TB181E	08501

OUTPUT SIGNAL	OUTPUT DESTINATION FO-SH
3TB190E	12400
3TB212E	12300
3TB221E	12200, 12300, 12400
3TB230E	12300
3TB243E	08300, 08502, 08503
3TB252E	08502, 08503
3TB261E	08300, 12400
3TB270E	12200
3TC03A	12300
3TC030	12000
3TC110	12200, 12400
3TW11A	12200
3TXR6A	12200



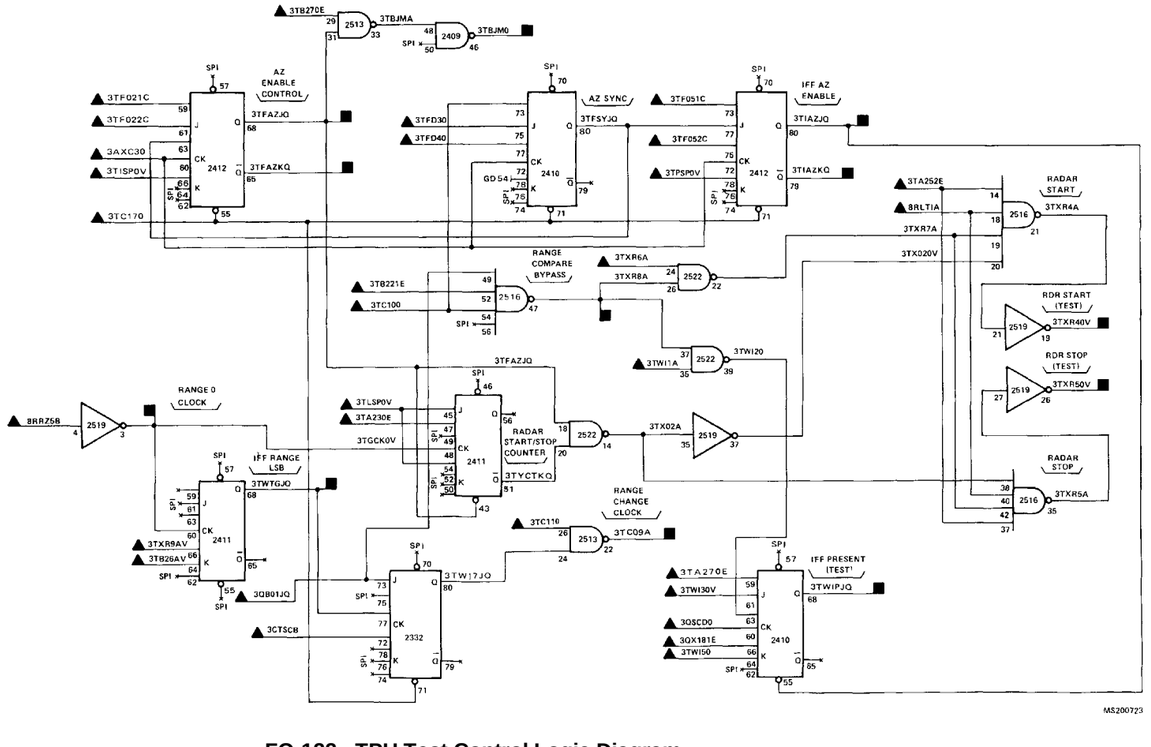
FO-121. TPU Radar/IFF Range Compare Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:
   - ▲ INPUT FROM ANOTHE
   - △ INPUT FROM SAME FI
   - ■ OUTPUT TO ANOTHEF
   - □ OUTPUT TO BOTH SAI FIGURE
   - ◪ OUTPUT TO SAME FIG
4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:
    - A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.
    - B. REFER TO TABLE 5037 FOR CARD PART NUMBER.
    - C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.
11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT SIGNAL	SOURCE FO-SH
3AXC30	07600
3CTSC8	07600
3QB01JQ	07701
3QSCD0	08300
3QX181E	08503
3TA230E	12000
3TA252E	12000
3TA270E	12000
3TB221E	12100
3TB26AV	12400
3TB270E	12100
3TC100	12300
3TC110	12100
3TC170	12300
3TFD30	12000
3TFD40	12000
3TF021C	12000
3TF022C	12000
3TF051C	12000
3TF052C	12000
3TISP0V	12300
3TLSP0V	12400
3TPSP0V	12300
3TWI1A	12100
3TWI30V	12400
3TWI50	08400
3TXR6A	12100
3TXR9AV	12400
8RLTIA	14500
8RLTIA	26802
8RLTIA	26901
8RRZSB	14600
8RRZSB	26803
8RRZSB	27502

OUTPUT DESTINATION SIGNAL	FO-SH
3TBJM0	08502
3TC09A	12100
3TFAZJQ	12300
3TFAZKQ	12400
3TGCK0V	11900, 12400
3TIAZJQ	12400
3TIAZKQ	12300, 12400
3TWIPJQ	08300, 08400, 08503
3TWTGJQ	08300, 12400
3TXR40V	08502
3TXR50V	08502
3TXR8A	08501



FO-122. TPU Test Control Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

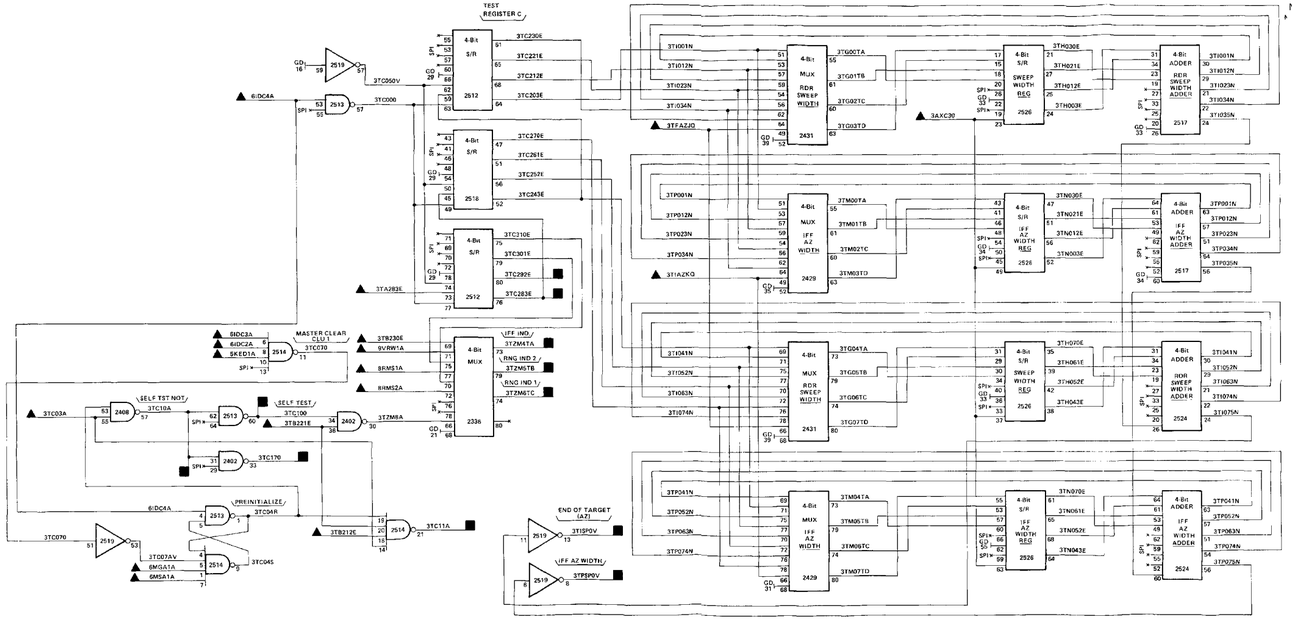
    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5037 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A1224, A1230, AND A1438.

INPUT		OUTPUT	
SIGNAL	SOURCE FO-SH	SIGNAL	DESTINATION FO-SH
3AXC30	07600	3TC10A	08400
3TA283E	12000	3TC100	08300, 08400, 08502, 08503, 09900, 11301, 12200
3TB212E	12100		12100
3TB221E	12100	3TC11A	12100
3TB230E	12100	3TC170	07600, 12100, 12200
3TC03A	12100	3TC283E	11301
3TFAZJQ	12200	3TC292E	11301
3TJAZKQ	12200	3TPSPQV	12200
5KED1A	13400	3TZM47A	10100, 12100
5KED1A	26803	3TZM57B	08700, 11100, 12100
5KED1A	31301	3TZM67C	08700, 08800, 08900, 11100, 12100
6IDC2A	05500		
6IDC3A	05500		
6IDC4A	05500		
6MGA1A	05600		
6MGA1A	26803		
6MGA1A	27201		
6MSA1A	05600		
6MSA1A	26803		
6MSA1A	27201		
8RMS1A	14700		
8RMS1A	26802		
8RMS1A	27502		
8RMS2A	14700		
8RMS2A	26802		
8RMS2A	27502		
9VRW1A	19800		
9VRW1A	26802		
9VRW1A	27301		



FO-123. TPU Sweep/Azimuth Test Logic Diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUITCATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

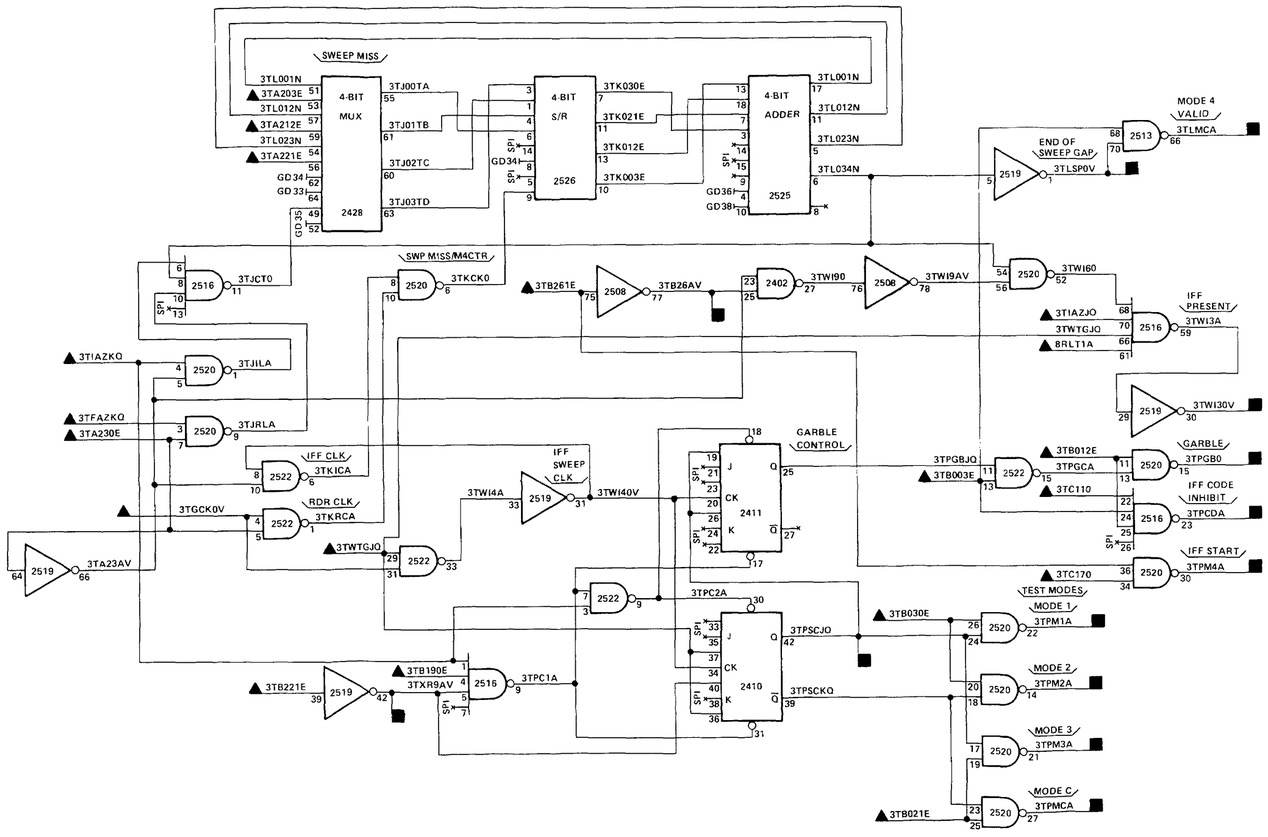
    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2130, A2230, A2330, A2430, AND A2530.

INPUT	
SIGNAL	SOURCE FO-SH
3TA203E	12000
3TA212E	12000
3TA221E	12000
3TA230E	12000
3TB003E	12100
3TB012E	12100
3TB021E	12100
3TB030E	12100
3TB190E	12100
3TB221E	12100
3TB261E	12100
3TC110	12100
3TFAZKQ	12200
3TGCK0V	12200
3TIAZJQ	12200
3TIAZKQ	12200
3TWTGJQ	12200
8RLT1A	14500
8RLT1A	26802
8RLT1A	26901

OUTPUT	
SIGNAL	DESTINATION FO-SH
3TB26AV	12200
3TLMCA	08503
3TLSP0V	12200
3TPCDA	08503
3TPGB0	08503
3TPMCA	09900
3TPM1A	09900
3TPM2A	09900
3TPM3A	09900
3TPM4A	08503
3TPSCJQ	12100
3TWI30V	12200
3TXR9AV	12100, 12200



NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTION XII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

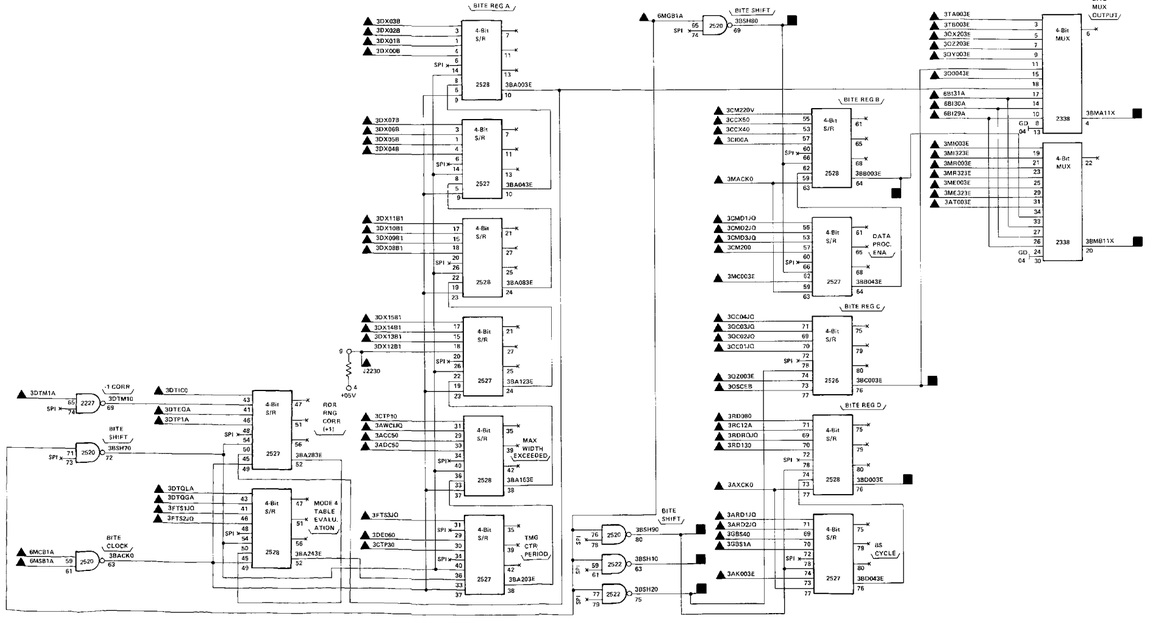
    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FOR MTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2130, A2230, A2330, A2430, AND A2530.

MS200725

FO-124. TPU Sweep Miss/Garble Control Test Logic Diagram

INPUT		INPUT		INPUT		OUTPUT	
SIGNAL	SOURCE FO-SN	SIGNAL	SOURCE FO-SN	SIGNAL	SOURCE FO-SN	SIGNAL	DESTINATION FO-SN
3ACC50	10900	3DX10B1	29601	6B130A	27901	3BB003E	11800
3ADC50	10900	3DX11B1	09200	6B131A	05300	3BC003E	08503
3AK003E	08100	3DX11B1	29601	6B131A	26802	3BD003E	05400, 07900, 26802
3ARD1JQ	08100	3DX12B1	09200	6B131A	26803	3BMA11X	05400, 26802
3ARD2JQ	08100	3DX12B1	29601	6B131A	27901	3BMB11X	05400, 26802
3AT003E	08100	3DX13B1	09200	6MCB1A	05600	3BSH10	08502, 08503
3AWC1JQ	11000	3DX13B1	29601	6MCB1A	27201	3BSH20	08501, 08503
3AXCK0	07900	3DX14B1	09200	6MGB1A	05600	3BSH80	11800
3CCX40	11300	3DX14B1	29601	6MGB1A	27201	3BSH90	07900, 08000, 08100
3CCX50	09300	3DX15B1	09200	6MSB1A	05600		
3CD00A	09600	3DX15B1	29601	6MSB1A	26802		
3CMD1JQ	08200	3FTS1JQ	10500	6MSB1A	26803		
3CMD2JQ	08200	3FTS2JQ	10500	6MSB1A	27201		
3CMD3JQ	08200	3FTS3JQ	10500				
3CM200	08200	3GBS1A	10600				
3CM220V	09000	3GBS40	10600				
3CTP10	07600	3MACK0	11800				
3CTP30	07600	3MC003E	11800				
3DED60	09000	3ME003E	11400				
3DED60	29602	3ME323E	11400				
3DTE0A	09000	3MI003E	11600				
3DTJC0	09000	3MI323E	11600				
3DTM1A	09000	3MI323E	29502				
3DTP1A	09000	3MR003E	11500				
3DTQGA	09000	3MR323E	11500				
3DTQLA	09000	3QC01JQ	08400				
3DX00B	09200	3QC02JQ	08400				
3DX00B	29601	3QC03JQ	08400				
3DX01B	09200	3QC04JQ	08400				
3DX02B	09200	3QSCEB	08300				
3DX02B	29601	3QX203E	08501				
3DX03B	09200	3QY003E	08503				
3DX03B	29601	3QZ003E	08503				
3DX04B	09200	3QZ203E	08501				
3DX04B	29601	3RD043E	08502				
3DX05B	09200	3RC12A	11100				
3DX05B	29601	3RDRQJQ	11100				
3DX06B	09200	3RDRQJQ	26802				
3DX06B	29601	3RD080	11100				
3DX07B	09200	3RD130	11100				
3DX07B	29601	3TA003E	12000				
3DX08B1	09200	3TB003E	12100				
3DX08B1	29601	6B129A	26802				
3DX09B1	09200	6B129A	26803				
3DX09B1	29601	6B129A	27901				
3DX10B1	09200	6B130A	26803				



FO-125. TPU BITE Registers and Output Multiplexers Logic diagram

NOTES: UNLESS OTHERWISE SPECIFIED

1. PARTIAL REFERENCE DESIGNATIONS ARE SHOWN ; FOR COMPLETE DESIGNATIONS , PREFIX WITH APPLICABLE UNIT NUMBER AND ASSEMBLY DESIGNATION.
2. ALL CIRCUITS SHOWN ON THIS FIGURE ARE CONTAINED IN EQUIPMENT RACK 1, VPU BAY 1 CARD CAGE (1A1A1A5).
3. REFERENCES ARE AS FOLLOWS:

   ▲ INPUT FROM ANOTHER FIGURE
   △ INPUT FROM SAME FIGURE
   ■ OUTPUT TO ANOTHER FIGURE
   □ OUTPUT TO BOTH SAME AND ANOTHER FIGURE
   ◪ OUTPUT TO SAME FIGURE

4. REFER TO TABLE 5-1 FOR CARD LOCATION IN LOGIC DIAGRAMS INDEX.
5. REFER TO TABLE 5-2 FOR KEY SIGNAL LOOK UP LISTING.
6. REFER TO CABLING DIAGRAM SECTIONXII FOR UNIT-TO-UNIT SIGNAL CABLING.
7. REFER TO RIE POWER DISTRIBUTION DIAGRAMS FOR DC POWER AND GROUND CIRCUITS.
8. REFER TO SECTION II FOR CIRCUIT CATD CHIP FUNCTION DESIGNATIONS.
9. CIRCUIT SYMBOLS INCLUDE CARD LOCATION AND CIRCUIT CARD PIN NUMBERS.
10. TO DETERMINE CIRCUIT CARD PIN/TEST POINT PERFORM THE FOLLOWING:

    A. FROM CIRCUIT SYMBOL NOTE CARD LOCATION AND CIRCUIT CARD PIN NUMBER.

    B. REFER TO TABLE 5-37 FOR CARD PART NUMBER.

    C. REFER TO TABLE 5-38 FOR CARD PIN/TEST POINT FORMTS TESTABLE CARDS.

11. SPIXXX INDICATES +5V PULLUP THROUGH RESISTOR CARDS A2130, A2230, A2330, A2430, AND A2530.

By Order of the Secretary of the Army:

JOHN A. WICKHAM, JR.
General, United States Army
Chief of Staff

Official:

DONALD J. DELANDRO
Brigadier General, United States Army
The Adjutant General

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The Metric System and Equivalents

Linear Measure

1 centimeter = 10 millimeters = .39 inch
1 decimeter = 10 centimeters = 3.94 inches
1 meter = 10 decimeters = 39.37 inches
1 dekameter = 10 meters = 32.8 feet
1 hectometer = 10 dekameters = 328.08 feet
1 kilometer = 10 hectometers = 3,280.8 feet

Weights

1 centigram = 10 milligrams = .15 grain
1 decigram = 10 centigrams = 1.54 grains
1 gram = 10 decigram = .035 ounce
1 decagram = 10 grams = .35 ounce
1 hectogram = 10 decagrams = 3.52 ounces
1 kilogram = 10 hectograms = 2.2 pounds
1 quintal = 100 kilograms = 220.46 pounds
1 metric ton = 10 quintals = 1.1 short tons

Liquid Measure

1 centiliter = 10 milliliters = .34 fl. ounce
1 deciliter = 10 centiliters = 3.38 fl. ounces
1 liter = 10 deciliters = 33.81 fl. ounces
1 dekaliter = 10 liters = 2.64 gallons
1 hectoliter = 10 dekaliters = 26.42 gallons
1 kiloliter = 10 hectoliters = 264.18 gallons

Square Measure

1 sq. centimeter = 100 sq. millimeters = .155 sq. inch
1 sq. decimeter = 100 sq. centimeters = 15.5 sq. inches
1 sq. meter (centare) = 100 sq. decimeters = 10.76 sq. feet
1 sq. dekameter (are) = 100 sq. meters = 1,076.4 sq. feet
1 sq. hectometer (hectare) = 100 sq. dekameters = 2.47 acres
1 sq. kilometer = 100 sq. hectometers = .386 sq. mile

Cubic Measure

1 cu. centimeter = 1000 cu. millimeters = .06 cu. inch
1 cu. decimeter = 1000 cu. centimeters = 61.02 cu. inches
1 cu. meter = 1000 cu. decimeters = 35.31 cu. feet

Approximate Conversion Factors

To change	*To*	*Multiply by*	*To change*	*To*	*Multiply by*
inches	centimeters	2.540	ounce-inches	Newton-meters	.007062
feet	meters	.305	centimeters	inches	.394
yards	meters	.914	meters	feet	3.280
miles	kilometers	1.609	meters	yards	1.094
square inches	square centimeters	6.451	kilometers	miles	.621
square feet	square meters	.093	square centimeters	square inches	.155
square yards	square meters	.836	square meters	square feet	10.764
square miles	square kilometers	2.590	square meters	square yards	1.196
acres	square hectometers	.405	square kilometers	square miles	.386
cubic feet	cubic meters	.028	square hectometers	acres	2.471
cubic yards	cubic meters	.765	cubic meters	cubic feet	35.315
fluid ounces	milliliters	29,573	cubic meters	cubic yards	1.308
pints	liters	.473	milliliters	fluid ounces	.034
quarts	liters	.946	liters	pints	2.113
gallons	liters	3.785	liters	quarts	1.057
ounces	grams	28.349	liters	gallons	.264
pounds	kilograms	.454	grams	ounces	.035
short tons	metric tons	.907	kilograms	pounds	2.205
pound-feet	Newton-meters	1.356	metric tons	short tons	1.102
pound-inches	Newton-meters	.11296			

Temperature (Exact)

°F	Fahrenheit temperature	5/9 (after subtracting 32)	Celsius temperature	°C

PIN: 057021-001